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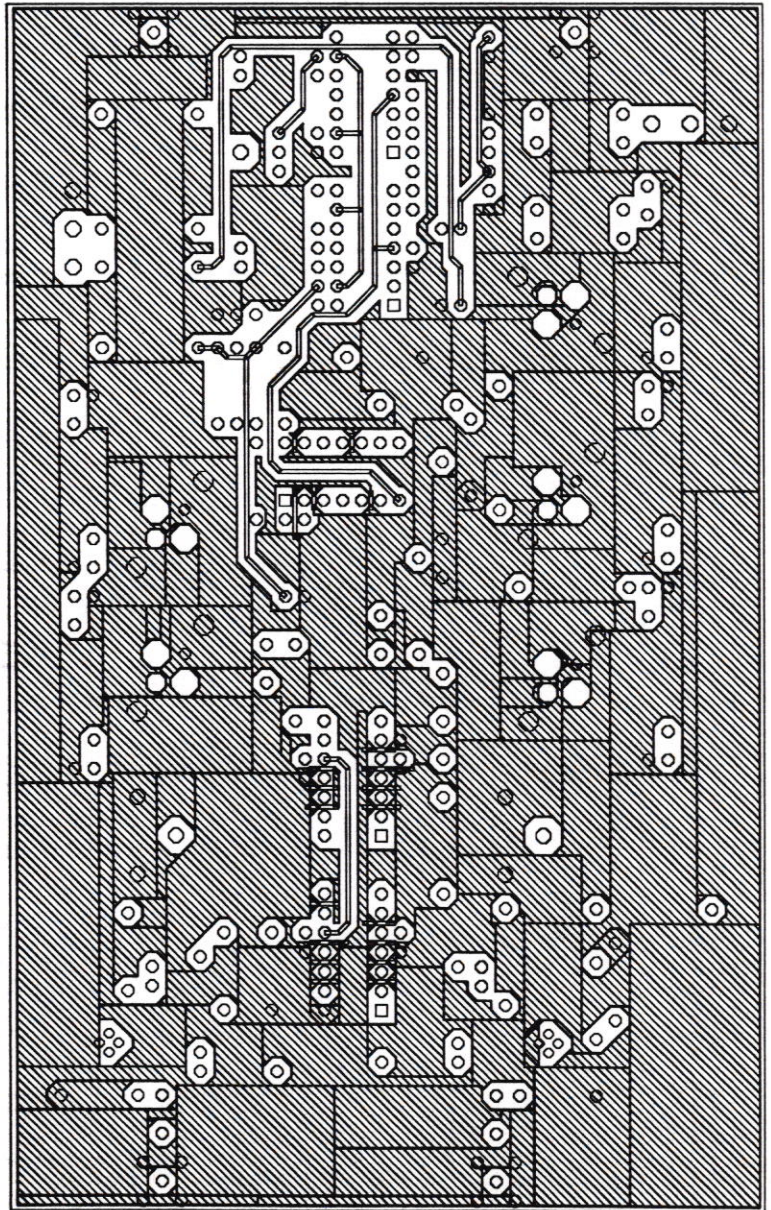
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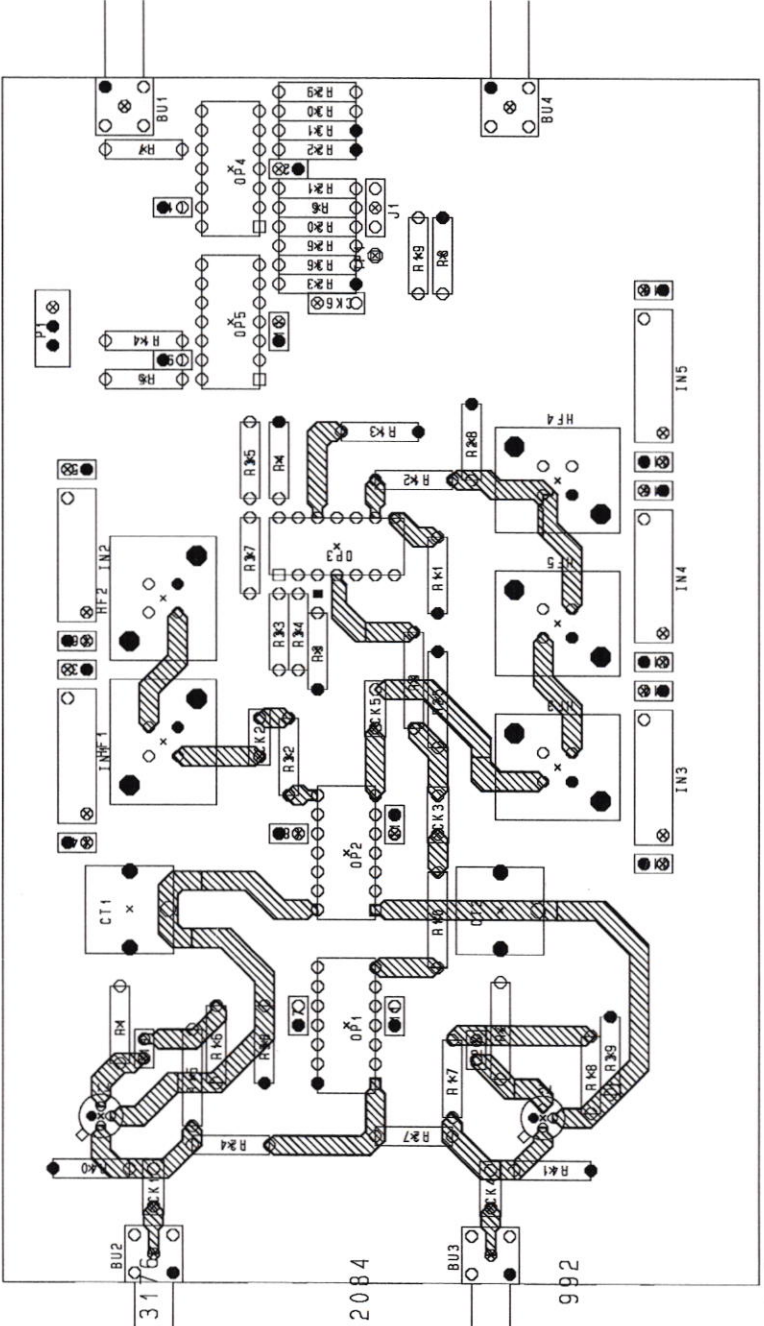
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Bestückungsliste

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## Danksagung

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Den Herren Dipl. Phys. T. Kandler, Dipl. Phys. M. Jäger und Dr. M. Jung danke ich für ihre kollegiale Hilfsbereitschaft bei vielen kleinen Problemen.

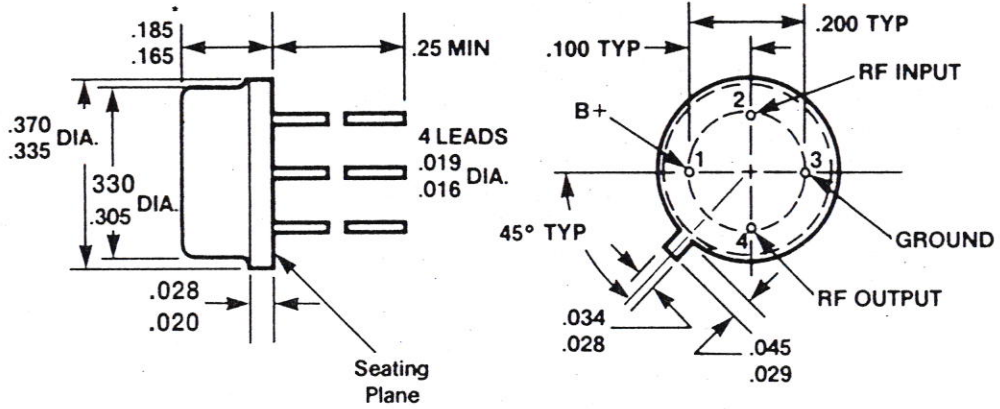
Ich danke Prof. Dr. K. Bethge für die Aufnahme in das Institut für Kernphysik der Johann Wolfgang Goethe-Universität und die Bereitstellung des Arbeitsplatzes. Prof. Dr. H.-J. Kluge danke ich für die Benutzung der GSI-Labors und -einrichtungen.

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Für kritische Diskussionen über die durchgeführten Messungen und Ergebnisse danke ich Dr. O. Jagutzki.

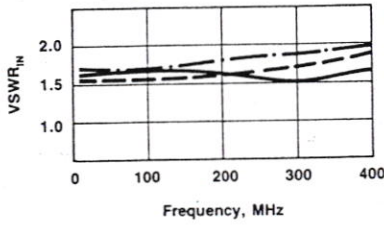
Mein ganz besonders herzlicher Dank gilt meinen Eltern, die mich in meinem Studium in allen Ihnen möglichen Bereichen unterstützt haben

# • TO-12 Case Drawings

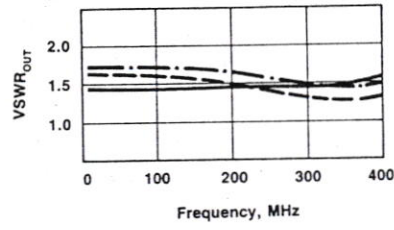


\*For GPD-405 (TO-12T Case) these dimensions are  $\frac{.230}{.260}$

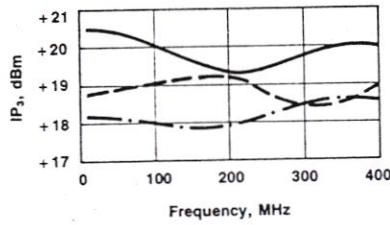
Input VSWR



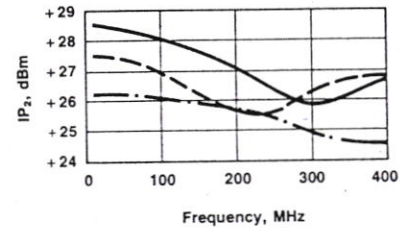
Output VSWR



Third-Order Intercept Point



Second-Order Intercept Point



**AUTOMATIC NETWORK ANALYZER MEASUREMENTS** (Typical production unit @ +25°C ambient)

**NUMERICAL READINGS**

BIAS = 15.00 VOLTS

FREQ MHz	VSWR IN	GAIN dB	PHASE DEG	PHASE DEV	GPDEL NSEC	VSWR OUT	ISOL dB
100.0	1.63	14.33	174.29	.02	.00	1.45	23.70
150.0	1.60	14.23	170.90	.05	.18	1.46	23.27
200.0	1.56	14.24	167.76	.33	.20	1.45	22.92
250.0	1.53	14.17	163.59	-.40	.20	1.45	22.76
300.0	1.53	14.23	160.48	-.11	.19	1.45	22.49
350.0	1.55	14.22	156.84	-.32	.18	1.47	22.00
400.0	1.58	14.09	154.18	.43	.17	1.51	21.79
450.0	1.68	14.11	150.88		.21	1.54	21.28
500.0	1.83	13.97	146.56		.21	1.61	21.01
550.0	2.06	13.81	143.23		.22	1.70	20.54
600.0	2.33	13.76	138.65		.26	1.82	20.37
650.0	2.67	13.38	133.97		.24	1.97	20.05
700.0	3.08	13.05	130.10		.22	2.15	19.79
750.0	3.60	12.74	126.17		.24	2.35	19.62
800.0	4.17	12.36	121.50		.22	2.58	19.88
850.0	4.74	11.56	118.16		.24	2.81	19.62
900.0	5.24	11.06	112.73		.27	3.05	20.33
950.0	5.71	10.37	108.40		.13	3.28	19.86
1000.0	6.03	9.51	108.17		.13	3.49	20.18

**S-PARAMETERS, MAGNITUDES AND ANGLES**

BIAS = 15.00 VOLTS

FREQ MHz	11 RATIO	11 ANGLE	21 dB	21 ANGLE	12 dB	12 ANGLE	22 RATIO	22 ANGLE
100.00	.235	-179.6	14.333	174.0	-23.399	15.6	.183	3.2
150.00	.230	-178.7	14.267	170.8	-23.288	23.6	.181	8.7
200.00	.221	-177.6	14.297	167.9	-23.158	28.4	.183	11.3
250.00	.209	-173.3	14.225	163.6	-22.753	36.3	.181	14.4
300.00	.207	-168.0	14.267	160.7	-22.286	42.9	.185	18.2
350.00	.212	-160.8	14.253	156.9	-22.099	49.7	.187	24.2
400.00	.228	-154.2	14.179	154.0	-21.392	55.4	.196	29.4
450.00	.258	-148.9	14.178	150.5	-21.186	58.8	.209	34.4
500.00	.298	-146.4	13.985	146.4	-20.985	64.6	.231	38.2
550.00	.346	-145.8	13.839	142.9	-20.484	67.1	.259	41.0
600.00	.400	-147.2	13.746	138.4	-20.250	71.3	.292	41.7
650.00	.457	-150.1	13.382	133.7	-20.036	75.1	.329	41.1
700.00	.512	-154.7	13.047	130.0	-19.837	77.9	.366	39.4
750.00	.569	-159.7	12.735	126.1	-19.672	79.9	.406	36.7
800.00	.617	-164.5	12.341	121.4	-19.820	81.9	.441	32.8
850.00	.653	-170.0	11.520	118.1	-19.530	86.9	.478	29.0
900.00	.684	-175.4	11.057	112.9	-20.285	85.9	.509	24.5
950.00	.709	-179.2	10.382	108.3	-19.932	93.3	.534	19.9
1000.00	.720	174.1	9.476	108.0	-20.194	93.0	.555	15.3

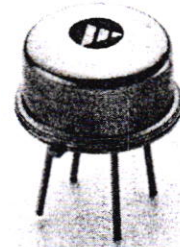
# GPD-402/-462

## Thin Film Cascadable Amplifier Module 5-400 MHz



### FEATURES

- Medium Output Level: +8 dBm (Typ)
- Low Cost
- Small Size



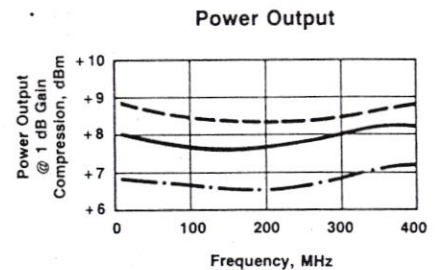
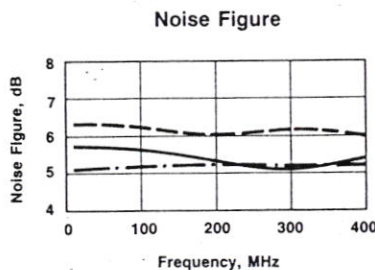
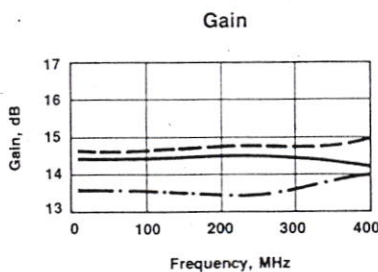
TO-12 Case  
(p. 403)

### ELECTRICAL SPECIFICATIONS (Measured in a 50-ohm system @ +15 VDC nominal)

Symbol	Characteristic	Typical T <sub>C</sub> = 25°C	Guaranteed Specifications		Unit
			T <sub>C</sub> = 0°-50°C	T <sub>C</sub> = -55°- +85°C	
BW	Frequency Range	5-400	5-400	5-400	MHz
GP	Small Signal Gain	14.0	13.0 Min.	12.0 Min.	dB
—	Gain Flatness	± 1.0	—	—	dB
NF	Noise Figure	8.0	—	—	dB
—	Reverse Isolation	20.0	—	—	dB
P <sub>1</sub> dB	Power Output @ +1 dB Compression	+8.0	—	—	dBm
—	Input VSWR	—	—	2.0:1 Max.	—
—	Output VSWR	—	—	2.0:1 Max.	—
IP <sub>3</sub>	Two Tone 3rd Order Intercept Point	+18.0	—	—	dBm
IP <sub>2</sub>	Two Tone 2nd Order Intercept Point	+24.0	—	—	dBm
I <sub>D</sub>	DC Current	24	—	—	mA

### TYPICAL PERFORMANCE OVER TEMPERATURE (@ +15 VDC unless otherwise noted)

KEY: +25°C ———  
+85°C - - - -  
-55°C - · - · -



### MAXIMUM RATINGS

DC Voltage ..... +17 Volts  
 Continuous RF Input Power ..... +13 dBm  
 Operating Case Temperature ..... -55°C to +125°C  
 Storage Temperature ..... -62°C to +150°C  
 "R" Series Burn-In Temperature ..... +125°C

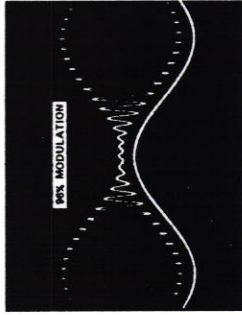
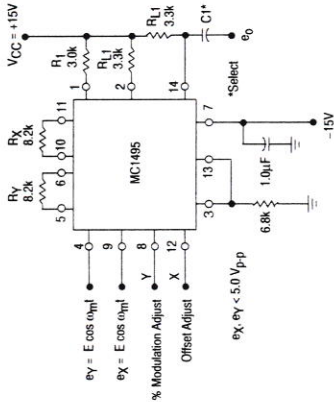
### THERMAL CHARACTERISTICS\*

θ<sub>JC</sub> ..... 150°C/W  
 Active Transistor Power Dissipation ..... 82 mW  
 Junction Temperature Above Case Temperature ..... 12.3°C  
 MTBF ..... 2,325,901 (402)/2,640,329 (462) Hrs.  
 \*For further information, see High Reliability section.

WEIGHT: (typical) — 1.5 grams

# MC1495, MC1595

Figure 30. Amplitude Modulation



The signal is applied to the unit's Y-input. Since the total input range is limited to 1.0 V<sub>p-p</sub>, a 2.0 V swing, a current source of 2.0 mA and an R<sub>x</sub> value of 1.0 kΩ is chosen. This takes best advantage of the dynamic range and insures linear operation in the Y-channel.

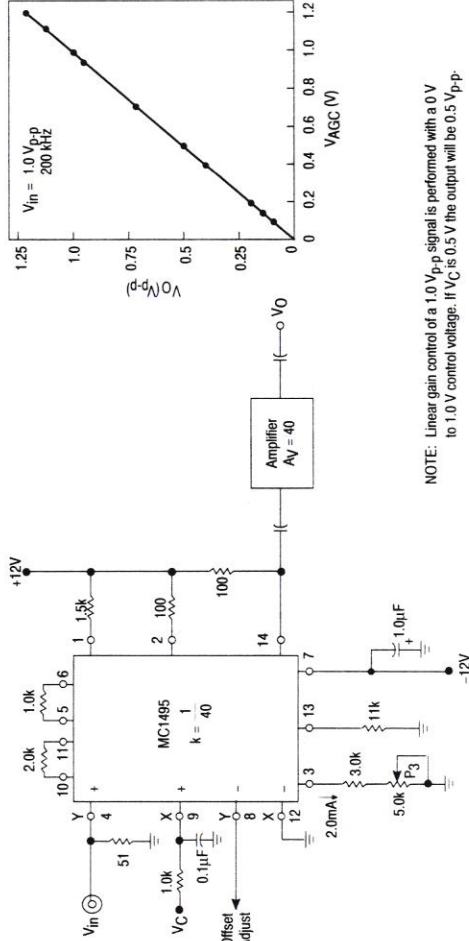
Since the X-input varies between 0 and +1.0 V, the current source selected was 1.0 mA, and the R<sub>x</sub> value chosen was 2.0 kΩ. This also insures linear operation over the X-input dynamic range. Choosing R<sub>L</sub> = 100 assures wide bandwidth operation.

Hence, the scale factor for this configuration is:

$$K = \frac{R_L}{R_X R_Y I_3} = \frac{100}{(2 \text{ k}) (1 \text{ k}) (2 \cdot 10^{-3}) \text{ V}^{-1}} = \frac{1}{40} \text{ V}^{-1}$$

The 2 in the numerator of the equation is missing in this scale factor expression because the output is single-ended and AC coupled.

Figure 31. Linear Gain Control



NOTE: Linear gain control of a 1.0 V<sub>p-p</sub> signal is performed with a 0 V to 1.0 V control voltage. If V<sub>C</sub> is 0.5 V the output will be 0.5 V<sub>p-p</sub>.

# MOTOROLA SEMICONDUCTOR TECHNICAL DATA

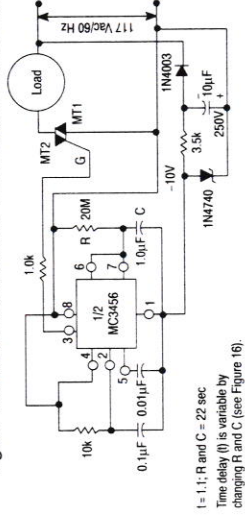
## MC3456

### Dual Timing Circuit

The MC3456 dual timing circuit is a highly stable controller capable of producing accurate time delays, or oscillation. Additional terminals are provided for triggering or resetting if desired. In the time delay mode of operation, the time is precisely controlled by one external resistor and capacitor per timer. For astable operation as an oscillator, the free running frequency and the duty cycle are both accurately controlled with two external resistors and one capacitor per timer. The circuit may be triggered and reset on falling waveforms, and the output structure can source or sink up to 200 mA or drive MTTL circuits.

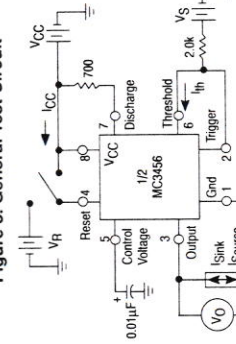
- Direct Replacement for NE555/SE556 Timers
- Timing From Microseconds Through Hours
- Operates in Both Astable and Monostable Modes
- Adjustable Duty Cycle
- High Current Output Can Source or Sink 200 mA
- Output Can Drive MTTL
- Temperature Stability of 0.005% per °C
- Normally "On" or Normally "Off" Output
- Dual Version of the Popular MC1455 Timer

Figure 1. 22 Second Solid State Time Delay Relay Circuit



I = 1.1; R and C = 22 sec  
Time delay (t) is variable by changing R and C (see Figure 1b).

Figure 3. General Test Circuit



Test circuit for measuring DC parameters (to set output and measure parameters):

- a) When V<sub>S</sub> ≥ 2/3 V<sub>CC</sub>, V<sub>O</sub> is low.
- b) When V<sub>S</sub> is 1/3 V<sub>CC</sub>, V<sub>O</sub> is high.
- c) When V<sub>O</sub> is low, Pin 7 sinks current. To test for Reset, set V<sub>O</sub> high, apply Reset voltage, and test for current flowing into Pin 7. When Reset is not in use, it should be tied to V<sub>CC</sub>.

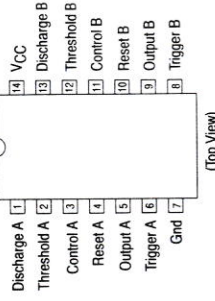
### DUAL TIMING CIRCUIT

SILICON MONOLITHIC INTEGRATED CIRCUIT



L SUFFIX CERAMIC PACKAGE CASE 632

### PIN CONNECTIONS



P SUFFIX PLASTIC PACKAGE CASE 751 (SO-14)



D SUFFIX PLASTIC PACKAGE CASE 646

### ORDERING INFORMATION

Device	Temperature Range	Package
MC3456L	0° to +70°C	Ceramic DIP
MC3456P		Plastic DIP
NE556D		SO-14

Figure 2. Block Diagram (1/2 Shown)

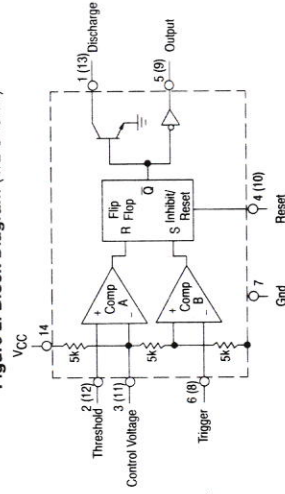
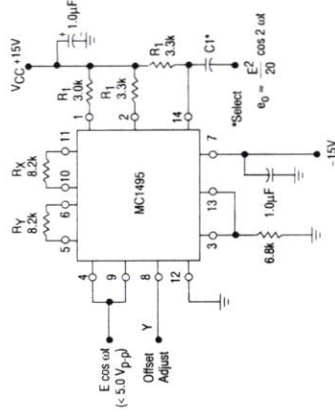




Figure 28. Frequency Doubler



When two equal cosine waves are applied to X and Y, the result is a wave whose frequency is twice the input frequency. For this example the input was a 10 kHz signal, output was 20 kHz.

The defining equation for balanced modulation is

$$K(E_m \cos \omega_m t) [E_c \cos \omega_c t + \cos(\omega_c + \omega_m)t + \cos(\omega_c - \omega_m)t]$$

where  $\omega_c$  is the carrier frequency,  $\omega_m$  is the modulator frequency and K is the multiplier gain constant. AC coupling at the output eliminates the need for level translation or an operational amplifier; a higher operating frequency results.

A problem common to communications is to extract the intelligence from single-sideband received signal. The ssb signal is of the form:

$$e_{ssb} = A \cos(\omega_c + \omega_m)t$$

and if multiplied by the appropriate carrier waveform,  $\cos \omega_c t$ ,  $e_{ssb} \cdot e_{carrier} = \frac{AK}{2} [\cos(2\omega_c + \omega_m)t + \cos(\omega_c)t]$

If the frequency of the band-limited carrier signal ( $\omega_c$ ) is ascertained in advance, the designer can insert a low pass filter and obtain the  $(AK/2) \cos(\omega_c t)$  term with ease. He/she also can use an operational amplifier for a combination level shift-active filter, as an external component. But in potted multipliers, even if the frequency range can be covered, the operational amplifier is inside and not accessible, so the user must accept the level shifting provided, and still add a low pass filter.

**Amplitude Modulation**

The multiplier performs amplitude modulation, similar to balanced modulation, when a DC term is added to the modulating signal with the Y-offset adjust potentiometer (see Figure 30).

Here, the identity is:

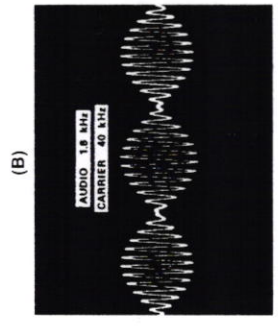
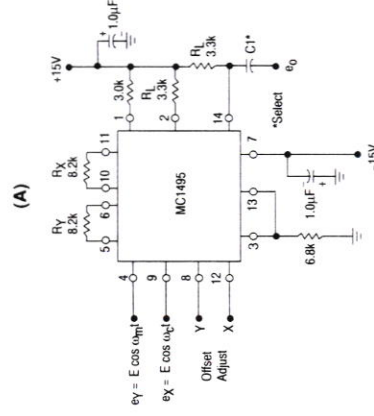
$$E_m(1 + m \cos \omega_m t) E_c \cos \omega_c t = K E_m E_c \cos \omega_c t + \frac{K E_m E_c m}{2} [\cos(\omega_c + \omega_m)t + \cos(\omega_c - \omega_m)t]$$

where m indicates the degrees of modulation. Since m is adjustable via potentiometer P1, 100% modulation is possible. Without extensive tweaking, 96% modulation may be obtained where  $\omega_c$  and  $\omega_m$  are the same as in the balanced modulator example.

**Linear Gain Control**

To obtain linear gain control, the designer can feed to one of the two MC1495 inputs a signal that will vary the unit's gain. The following example demonstrates the feasibility of this application. Suppose a 200 kHz sine wave, 1.0 V peak-to-peak, is the signal to which a gain control will be added. The dynamic range of the control voltage VC is 0 V to +1.0 V. These must be ascertained and the proper values of RX and RY can be selected for optimum performance. For the 200 kHz operating frequency, load resistors of 100  $\Omega$  were chosen to broaden the operating bandwidth of the multiplier, but gain was sacrificed. It may be made up with an amplifier operating at the appropriate frequency (see Figure 31).

Figure 29. Balanced Modulator



**AC APPLICATIONS**

The applications that follow demonstrate the versatility of the monolithic multiplier. If a potted multiplier is used for these cases, the results generally would not be as good because the potted units have circuits that, although they optimize DC multiplication operation, can hinder AC applications.

Frequency doubling often is done with a diode where the fundamental plus a series of harmonics are generated. However, extensive filtering is required to obtain the desired harmonic, and the second harmonic obtained under this technique usually is small in magnitude and requires amplification.

When a multiplier is used to double frequency the second harmonic is obtained directly, except for a DC term, which can be removed with AC coupling.

$$e_o = KE^2 \cos^2 \omega t$$

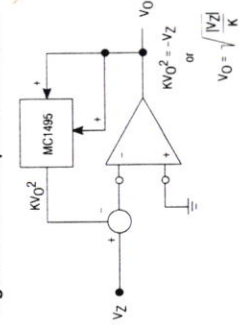
$$= \frac{KE^2}{2} (1 + \cos 2\omega t)$$

A potted multiplier can be used to obtain the double frequency component, but frequency would be limited by its internal level-shift amplifier. In the monolithic units, the amplifier is omitted.

In a typical doubler circuit, conventional  $\pm 15$  V supplies are used. An input dynamic range of 5.0 V peak-to-peak is allowed. The circuit generates wave-forms that are double frequency; less than 1% distortion is encountered without filtering. The configuration has been successfully used in excess of 200 kHz; reducing the scale factor by decreasing the load resistors can further expand the bandwidth.

Figure 29 represents an application for the monolithic multiplier as a balanced modulator. Here, the audio input signal is 1.6 kHz and the carrier is 40 kHz.

Figure 26. Basic Square Root Circuit



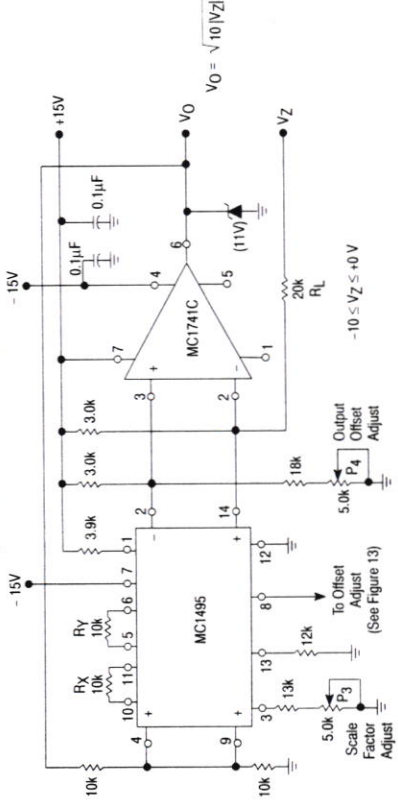
**Square Root**

A special case of the divide circuit in which the two inputs to the multiplier are connected together is the square root function as indicated in Figure 26. This circuit may suffer from latch-up problems similar to those of the divide circuit. Note that only one polarity of input is allowed and diode clamping (see Figure 27) protects against accidental latch-up.

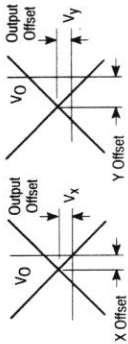
This circuit also may be adjusted in the closed-loop mode as follows:

1. Set VZ to -0.01 V and adjust P4 (output offset) for  $V_O = +0.316$  V, being careful to approach the output from the positive side to preclude the effect of the output diode clamping.
2. Set VZ to -0.9 V and adjust P2 (X adjust) for  $V_O = +3.0$  V.
3. Set VZ to -10 V and adjust P3 (scale factor adjust) for  $V_O = +10$  V.
4. Steps 1 through 3 may be repeated as necessary to achieve desired accuracy.

Figure 27. Square Root Circuit



X, Y and Output Offset Voltages



For most DC applications, all three offset adjust potentiometers (P1, P2, P4) will be necessary. One or more offset adjust potentiometers can be eliminated for AC applications (see Figures 28, 29, 30, 31).

If well regulated supply voltages are available, the offset adjust circuit of Figure 13 is recommended. Otherwise, the circuit of Figure 14 will greatly reduce the sensitivity to power supply changes.

**Scale Factor**

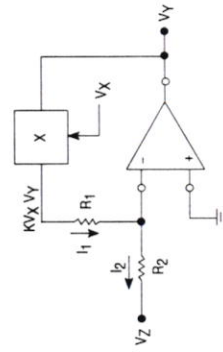
The scale factor K is set by P3 (Figure 21). P3 varies 1/3 which inversely controls the scale factor K. It should be noted that current I3 is one-half the current through R1. R1 sets the bias level for Q5, Q6, Q7, and Q8 (see Figure 3). Therefore, to be sure that these devices remain active under all conditions of input and output swing, care should be exercised in adjusting P3 over wide voltage ranges (see General Design Procedure).

**Adjustment Procedures**

The following adjustment procedure should be used to null the offsets and set the scale factor for the multiply mode of operation, (see Figure 21).

1. X-Input Offset
  - (a) Connect oscillator (1.0 kHz, 5.0 Vp-p sinewave) to the X-input (Pin 4).
  - (b) Connect X-input (Pin 9) to ground.
  - (c) Adjust X offset potentiometer (P2) for an AC null at the output.
2. Y-Input Offset
  - (a) Connect oscillator (1.0 kHz, 5.0 Vp-p sinewave) to the X-input (Pin 9).
  - (b) Connect Y-input (Pin 4) to ground.
  - (c) Adjust Y offset potentiometer (P1) for an AC null at the output.
3. Output Offset
  - (a) Connect both X and Y-inputs to ground.
  - (b) Adjust output offset potentiometer (P4) until the output voltage (V0) is 0 Vdc.
4. Scale Factor
  - (a) Apply +10 Vdc to both the X and Y-inputs.
  - (b) Adjust P3 to achieve +10 V at the output.
5. Repeat steps 1 through 4 as necessary.

Figure 24. Basic Divide Circuit



The ability to accurately adjust the MC1495 depends upon the characteristics of potentiometers P1 through P4. Multi-turn, infinite resolution potentiometers with low temperature coefficients are recommended.

**DC APPLICATIONS**

**Multiply**

The circuit shown in Figure 21 may be used to multiply signals from DC to 100 kHz. Input levels to the actual multiplier are 5.0 V (max). With resistive voltage dividers the maximum could be very large however, for this application two-to-one dividers have been used so that the maximum input level is 10 V. The maximum output level has also been designed for 10 V (max).

**Squaring Circuit**

If the two inputs are tied together, the resultant function is squaring; that is  $V_O = -KV^2$  where K is the scale factor. Note that all error terms can be eliminated with only three adjustment potentiometers, thus eliminating one of the input offset adjustments. Procedures for nulling with adjustments are given as follows:

**A. AC Procedure:**

1. Connect oscillator (1.0 kHz, 15 Vp-p) to input.
2. Monitor output at 2.0 kHz with tuned voltmeter and adjust P3 for desired gain. (Be sure to peak response of the voltmeter.)
3. Tune voltmeter to 1.0 kHz and adjust P1 for a minimum output voltage.
4. Ground input and adjust P4 (output offset) for 0 Vdc output.
5. Repeat steps 1 through 4 as necessary.

**B. DC Procedure:**

1. Set  $V_X = V_Y = 0$  V and adjust P4 (output offset potentiometer) such that  $V_O = 0$  Vdc.
2. Set  $V_X = V_Y = 1.0$  V and adjust P1 (Y-input offset potentiometer) such that the output voltage is +0.100 V.
3. Set  $V_X = V_Y = 10$  Vdc and adjust P3 such that the output voltage is +10 V.
4. Set  $V_X = V_Y = -10$  Vdc. Repeat steps 1 through 3 as necessary.

Figure 24. Basic Divide Circuit

**Divide Circuit**

Consider the circuit shown in Figure 24 in which the multiplier is placed in the feedback path of an operational amplifier. For this configuration, the operational amplifier will maintain a "virtual ground" at the inverting (-) input. Assuming that the bias current of the operational amplifier is negligible, then  $I_1 = I_2$  and,

$$\frac{KV_X V_Y}{R_1} = \frac{-V_Z}{R_2} \quad (1)$$

$$\text{Solving for } V_Y, \quad V_Y = \frac{-R_1 V_Z}{R_2 K V_X} \quad (2)$$

$$\text{If } R_1 = R_2, \quad V_Y = \frac{-V_Z}{KV_X} \quad (3)$$

$$\text{If } R_1 = KR_2, \quad V_Y = \frac{-V_Z}{KV_X} \quad (4)$$

Hence, the output voltage is the ratio of  $V_Z$  to  $V_X$  and provides a divide function. This analysis is, of course, the ideal condition. If the multiplier error is taken into account, the output voltage is found to be:

$$V_Y = -\left[ \frac{R_1}{R_2 K} \right] \frac{V_Z}{V_X} + \frac{\Delta E}{KV_X} \quad (5)$$

where  $\Delta E$  is the error voltage at the output of the multiplier. From this equation, it is seen that divide accuracy is strongly dependent upon the accuracy at which the multiplier can be set, particularly at small values of  $V_Y$ . For example, assume that  $R_1 = R_2$ , and  $K = 1/10$ . For these conditions the output of the divide circuit is given by:

$$V_Y = -\frac{10 V_Z}{V_X} + \frac{10 \Delta E}{V_X} \quad (6)$$

From Equation 6, it is seen that only when  $V_X = 10$  V is the error voltage of the divide circuit as low as the error of the multiply circuit. For example, when  $V_X$  is small, (0.1 V) the error voltage of the divide circuit can be expected to be a hundred times the error of the basic multiplier circuit.

In terms of percentage error,

$$\text{percentage error} = \frac{\text{error}}{\text{actual}} \times 100\%$$

or from Equation (5),

$$\text{PED} = \frac{\frac{\Delta E}{KV_X}}{\left[ \frac{R_1}{R_2 K} \right] \frac{V_Z}{V_X}} = \left[ \frac{R_2}{R_1} \right] \frac{\Delta E}{V_Z} \quad (7)$$

From Equation 7, the percentage error is inversely related to voltage  $V_Z$  (i.e., for increasing values of  $V_Z$ , the percentage error decreases).

A circuit that performs the divide function is shown in Figure 25.

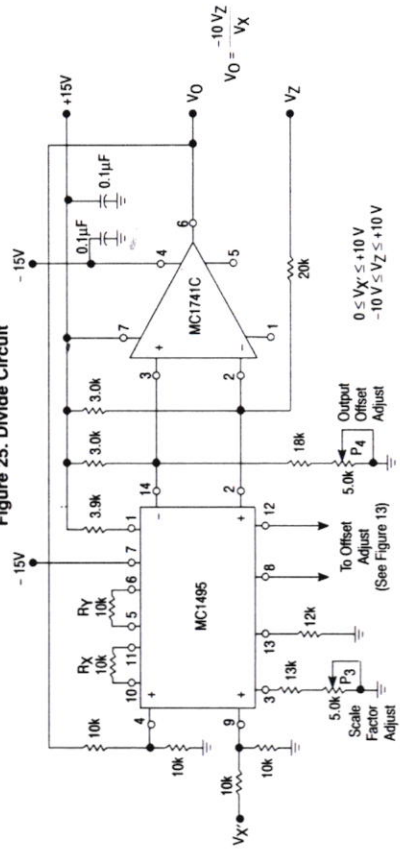
Two things should be emphasized concerning Figure 25.

1. The input voltage ( $V_X$ ) must be greater than zero and must be positive. This insures that the current out of Pin 2 of the multiplier will always be in a direction compatible with the polarity of  $V_Z$ .
2. Pin 2 and 14 of the multiplier have been interchanged in respect to the operational amplifiers input terminals. In this instance, Figure 25 differs from the circuit connection shown in Figure 21; necessitated to insure negative feedback around the loop.

A suggested adjustment procedure for the divide circuit.

1. Set  $V_Z = 0$  V and adjust the output offset potentiometer (P4) until the output voltage ( $V_O$ ) remains at some (not necessarily zero) constant value as  $V_X$  is varied between +1.0 V and +10 V.
2. Keep  $V_Z$  at 0 V, set  $V_X$  at +10 V and adjust the Y input offset potentiometer (P1) until  $V_O = 0$  V.
3. Let  $V_X = V_Z$  and adjust the X-input offset potentiometer (P2) until the output voltage remains at some (not necessarily -10 V) constant value as  $V_Z = V_X$  is varied between +1.0 and +10 V.
4. Keep  $V_X = V_Z$  and adjust the scale factor potentiometer (P3) until the average value of  $V_O$  is -10 V as  $V_Z = V_X$  is varied between +1.0 V and +10 V.
5. Repeat steps 1 through 4 as necessary to achieve optimum performance.

Figure 25. Divide Circuit



avoids increasing  $R_L$  significantly in order to maintain a K of 0.1.

The versatility of the MC1495 allows the user to optimize its performance for various input and output signal levels.

**OFFSET AND SCALE FACTOR ADJUSTMENT**

**Offset Voltages**

Within the monolithic multiplier (Figure 3) transistor base-emitter junctions are typically matched within 1.0 mV and resistors are typically matched within 2%. Even with this careful matching, an output error can occur. This output error is comprised of X-input offset voltage, Y-input offset voltage, and output offset voltage. These errors can be adjusted to zero with the techniques shown in Figure 21. Offset terms can be shown analytically by the transfer function:

$$V_O = K[V_X \pm V_{iox} \pm V_X(\text{off})] [V_Y \pm V_{ioy} \pm V_Y(\text{off})] \pm V_{OO} \quad (1)$$

Where:

- $K$  = scale factor
- $V_X$  = "x" input voltage
- $V_Y$  = "y" input voltage
- $V_{iox}$  = "x" input offset voltage
- $V_{ioy}$  = "y" input offset voltage
- $V_X(\text{off})$  = "x" input offset adjust voltage
- $V_Y(\text{off})$  = "y" input offset adjust voltage
- $V_{OO}$  = output offset voltage.

The choice of an operational amplifier for this application should have low bias currents, low offset current, and a high common mode input voltage range as well as a high common mode rejection ratio. The MC1456, and MC1741C operational amplifiers meet these requirements.

Referring to Figure 21, the level shift components will be determined. When  $V_X = V_Y = 0$ , the currents  $I_2$  and  $I_4$  will be equal to  $I_3$ . In Step 3,  $R_L$  was found to be 20 k $\Omega$  and in Step 4,  $V_2$  and  $V_4$  were found to be approximately 11 V. From this information  $R_O$  can be found easily from the following equation (neglecting the operational amplifiers bias current):

$$\frac{V_2}{R_L} + I_{13} = \frac{V^+ - V_2}{R_O}$$

And for this example,  $\frac{11 \text{ V}}{20 \text{ k}\Omega} + 1.0 \text{ mA} = \frac{15 \text{ V} - 11 \text{ V}}{R_O}$

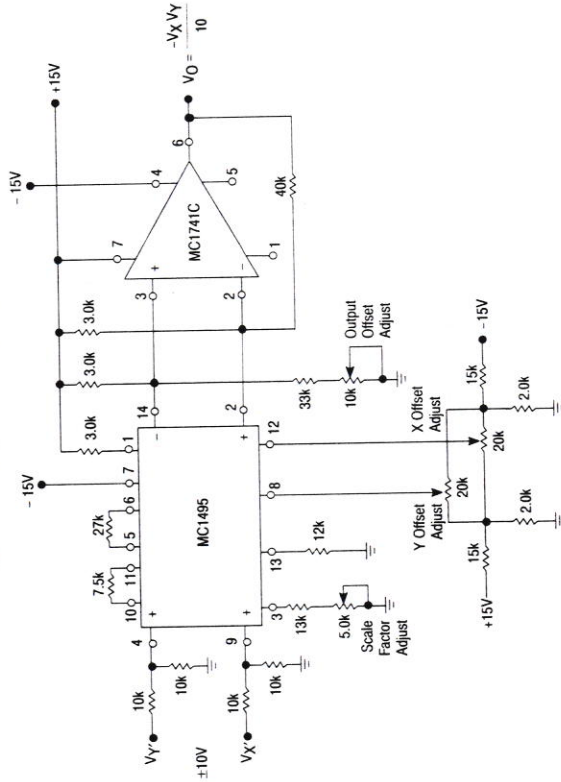
Solving for  $R_O$ :  $R_O = 2.6 \text{ k}\Omega$ , thus, select  $R_O = 3.0 \text{ k}\Omega$ .

For  $R_O = 3.0 \text{ k}\Omega$ , the voltage at Pins 2 and 14 is calculated to be:

$$V_2 = V_4 = 10.4 \text{ V.}$$

The linearity of this circuit (Figure 21) is likely to be as good or better than the circuit of Figure 5. Further improvements are possible as shown in Figure 23 where  $R_Y$  has been increased substantially to improve the Y linearity, and  $R_X$  decreased somewhat so as not to materially affect the X linearity. This

Figure 23. Multiplier with Improved Linearity



$V_Y = 5.0 \text{ V}$ ), their respective collector voltage should be at least a few tenths of a volt higher than the maximum input voltage. It should also be noticed that the collector voltage of transistors  $Q_3$  and  $Q_4$  is at a potential which is two diode-drops below the voltage at Pin 1. Thus, the voltage at Pin 1 should be about 2.0 V higher than the maximum input voltage. Therefore, to handle +5.0 V at the inputs, the voltage at Pin 1 must be at least +7.0 V. Let  $V_1 = 9.0 \text{ Vdc}$ .

Since the current flowing into Pin 1 is always equal to  $2I_3$ , the voltage at Pin 1 can be set by placing a resistor ( $R_1$ ) from Pin 1 to the positive supply:

$$R_1 = \frac{V^+ - V_1}{2I_3}$$

Let  $V^+ = 15 \text{ V}$ , then  $R_1 = \frac{15 \text{ V} - 9.0 \text{ V}}{(2)(1.0 \text{ mA})}$

$$R_1 = 3.0 \text{ k}\Omega.$$

Note that the voltage at the base of transistors  $Q_5$ ,  $Q_6$ ,  $Q_7$  and  $Q_8$  is one diode-drop below the voltage at Pin 1. Thus, in order that these transistors stay active, the voltage at Pins 2 and 14 should be approximately halfway between the voltage at Pin 1 and the positive supply voltage. For this example, the voltage at Pins 2 and 14 should be approximately 11 V.

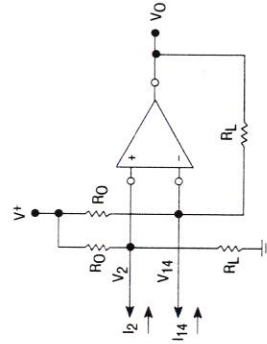
Step 5. For DC applications, such as the multiply, divide and square-root functions, it is usually desirable to convert the differential output to a single-ended output voltage referenced to ground. The circuit shown in Figure 22 performs this function. It can be shown that the output voltage of this circuit is given by:

$$V_O = (I_2 - I_4) R_L$$

$$\text{And since } I_A - I_B = I_2 - I_4 = \frac{2I_X V_Y}{I_3} = \frac{2V_X V_Y}{I_3 R_X R_Y}$$

then  $V_O = \frac{2R_L V_X V_Y}{4R_X R_Y I_3}$  where,  $V_X$ ,  $V_Y$  is the voltage at the input to the voltage dividers.

Figure 22. Level Shift Circuit



To set currents  $I_3$  and  $I_{13}$  to the desired value, it is only necessary to connect a resistor between Pin 13 and ground, and between Pin 3 and ground. From the schematic shown in Figure 3, it can be seen that the resistor values necessary are given by:

$$R_{13} + 500 \Omega = \frac{|V_1 - 0.7 \text{ V}|}{I_{13}}$$

$$R_3 + 500 \Omega = \frac{|V_1 - 0.7 \text{ V}|}{I_3}$$

Let  $V_1 = -15 \text{ V}$ , then  $R_{13} + 500 = \frac{14.3 \text{ V}}{1.0 \text{ mA}}$  or  $R_{13} = 13.8 \text{ k}\Omega$

Let  $R_{13} = 12 \text{ k}\Omega$ . Similarly,  $R_3 = 13.8 \text{ k}\Omega$ , let  $R_3 = 15 \text{ k}\Omega$

However, for applications which require an accurate scale factor, the adjustment of  $R_3$  and consequently,  $I_3$ , offers a convenient method of making a final trim of the scale factor. For this reason, as shown in Figure 21, resistor  $R_3$  is shown as a fixed resistor in series with a potentiometer.

For applications not requiring an exact scale factor (balanced modulator, frequency doubler, AGC amplifier, etc.) Pins 3 and 13 can be connected together and a single resistor from Pin 3 to ground can be used. In this case, the single resistor would have a value of 1/2 the above calculated value for  $R_{13}$ .

Step 2. The next step is to select  $R_X$  and  $R_Y$ . To insure that the input transistors will always be active, the following conditions should be met:

$$\frac{V_X}{R_X} < I_{13}, \quad \frac{V_Y}{R_Y} < I_3$$

A good rule of thumb is to make  $I_3 R_Y \geq 1.5 V_Y(\text{max})$  and  $I_{13} R_X \geq 1.5 V_X(\text{max})$ . The larger the  $I_3 R_Y$  and  $I_{13} R_X$  product in relation to  $V_Y$  and  $V_X$  respectively, the more accurate the multiplier will be (see Figures 17 and 18).

Let  $R_X = R_Y = 10 \text{ k}\Omega$ ,  
then  $I_3 R_Y = 10 \text{ V}$   
 $I_{13} R_X = 10 \text{ V}$

since  $V_X(\text{max}) = V_Y(\text{max}) = 5.0 \text{ V}$ , the value of  $R_X = R_Y = 10 \text{ k}\Omega$  is sufficient.

Step 3. Now that  $R_X$ ,  $R_Y$  and  $I_3$  have been chosen,  $R_L$  can be determined:

$$K = \frac{2R_L}{R_X R_Y I_3} = \frac{4}{10 \cdot 10 \cdot (1.0 \text{ mA})} = \frac{4}{100} = 0.04$$

Thus  $R_L = 20 \text{ k}\Omega$ .

Step 4. To determine what power supply voltage is necessary for this application, attention must be given to the circuit schematic shown in Figure 3. From the circuit schematic it can be seen that in order to maintain transistors  $Q_1$ ,  $Q_2$ ,  $Q_3$  and  $Q_4$  in an active region when the maximum input voltages are applied ( $V_X = V_Y = 10 \text{ V}$  or  $V_X = 5.0 \text{ V}$ ,

OPERATION AND APPLICATIONS INFORMATION

THEORY OF OPERATION

The MC1495 is a monolithic, four-quadrant multiplier which operates on the principle of variable transconductance. A detailed theory of operation is covered in Application Note AN489, *Analysis and Basic Operation of the MC1595*. The result of this analysis is that the differential output current of the multiplier is given by:

$$I_A - I_B = \Delta I = \frac{2V_X V_Y}{R_X R_Y I_3}$$

where,  $I_A$  and  $I_B$  are the currents into Pins 14 and 2, respectively, and  $V_X$  and  $V_Y$  are the X and Y input voltages at the multiplier input terminals.

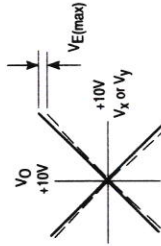
DESIGN CONSIDERATIONS

General

The MC1495 permits the designer to tailor the multiplier to a specific application by proper selection of external components. External components may be selected to optimize a given parameter (e.g. bandwidth) which may in turn restrict another parameter (e.g. maximum output voltage swing). Each important parameter is discussed in detail in the following paragraphs.

Linearity, Output Error, ERX or ERY

Linearity error is defined as the maximum deviation of output voltage from a straight line transfer function. It is expressed as error in percent of full scale (see figure below).



For example, if the maximum deviation,  $V_E(max)$ , is  $\pm 100$  mV and the full scale output is 10 V, then the percentage error is:

$$ER = \frac{V_E(max)}{VO(max)} \cdot 100 = \frac{100 \cdot 10^{-3}}{10} \cdot 100 = \pm 1.0\%$$

Linearity error may be measured by either of the following methods:

1. Using an X-Y plotter with the circuit shown in Figure 5, obtain plots for X and Y similar to the one shown above.
2. Use the circuit of Figure 4. This method nulls the level shifted output of the multiplier with the original input. The peak output of the null operational amplifier will be equal to the error voltage,  $V_E(max)$ .

One source of linearity error can arise from large signal nonlinearity in the X and Y input differential amplifiers. To avoid introducing error from this source, the emitter degeneration

resistors  $R_X$  and  $R_Y$  must be chosen large enough so that nonlinear base-emitter voltage variation can be ignored. Figures 17 and 18 show the error expected from this source as a function of the values of  $R_X$  and  $R_Y$  with an operating current of 1.0 mA in each side of the differential amplifiers (i.e.,  $I_3 = I_13 = 1.0$  mA).

3 dB Bandwidth and Phase Shift

Bandwidth is primarily determined by the load resistors and the stray multiplier output capacitance and/or the operational amplifier used to level shift the output. If wideband operation is desired, low value load resistors and/or a wideband operational amplifier should be used. Stray output capacitance will depend to a large extent on circuit layout.

Phase shift in the multiplier circuit results from two sources: phase shift common to both X and Y channels (due to the load resistor-output capacitance pole mentioned above) and relative phase shift between X and Y channels (due to differences in transadmittance in the X and Y channels). If the input to output phase shift is only 0.6°, the output product of two sine waves will exhibit a vector error of 1%. A 3° relative phase shift between  $V_X$  and  $V_Y$  results in a vector error of 5%.

Maximum Input Voltage

$V_X(max)$ ,  $V_Y(max)$  input voltages must be such that:

$$V_X(max) < I_{13} R_Y$$

$$V_Y(max) < I_{13} R_X$$

Exceeding this value will drive one side of the input amplifier to "cutoff" and cause nonlinear operation.

Current  $I_3$  and  $I_{13}$  are chosen at a convenient value (observing power dissipation limitation) between 0.5 mA and 2.0 mA, approximately 1.0 mA. Then  $R_X$  and  $R_Y$  can be determined by considering the input signal handling requirements.

For  $V_X(max) = V_Y(max) = 10$  V:

$$R_X = R_Y > \frac{10 \text{ V}}{1.0 \text{ mA}} = 10 \text{ k}\Omega$$

$$\text{The equation } I_A - I_B = \frac{2V_X V_Y}{R_X R_Y I_3}$$

$$\text{is derived from } I_A - I_B = \frac{2V_X V_Y}{(R_X + \frac{2kT}{qI_{13}})(R_Y + \frac{2kT}{qI_{13}}) I_3}$$

with the assumption  $R_X \gg \frac{2kT}{qI_{13}}$  and  $R_Y \gg \frac{2kT}{qI_{13}}$

At  $T_A = +25^\circ\text{C}$  and  $I_{13} = I_3 = 1.0$  mA,

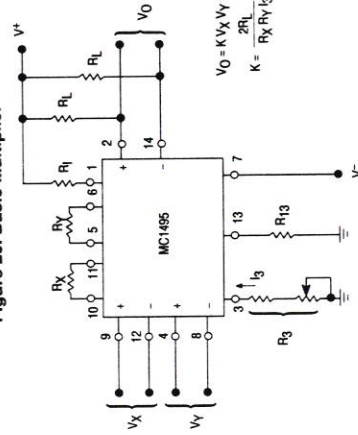
$$\frac{2kT}{qI_{13}} = \frac{2kT}{q(1.0)} = 52 \Omega$$

Therefore, with  $R_X = R_Y = 10$  k $\Omega$  the above assumption is valid. Reference to Figure 19 will indicate limitations of  $V_X(max)$  or  $V_Y(max)$  due to  $V_{14}$  and  $V_{17}$ . Exceeding these limits will cause saturation or "cutoff" of the input transistors. See Step 4 of General Design Procedure for further details.

**Maximum Output Voltage Swing**  
The maximum output voltage swing is dependent upon the factors mentioned below and upon the particular circuit being considered.

For Figure 20 the maximum output swing is dependent upon  $V^+$  for positive swing and upon the voltage at Pin 1 for negative swing. The potential at Pin 1 determines the quiescent level for transistors Q5, Q6, Q7 and Q8. This potential should be related so that negative swing at Pins 2 or 14 does not saturate those transistors. See General Design Procedure for further information regarding selection of these potentials.

Figure 20. Basic Multiplier



If an operational amplifier is used for level shift, as shown in Figure 21, the output swing (of the multiplier) is greatly reduced. See Section 3 for further details.

GENERAL DESIGN PROCEDURE

Selection of component values is best demonstrated by the following example. Assume resistive dividers are used at the X and Y inputs to limit the maximum multiplier input to  $\pm 5.0$  V [ $V_X = V_Y(max)$ ] for a  $\pm 10$  V input [ $V_X' = V_Y'(max)$ ] (see Figure 21). If an overall scale factor of 1/10 is desired, then,  $V_O = \frac{V_X' V_Y'}{10} = \frac{(2V_X)(2V_Y)}{10} = 4/10 V_X V_Y$

Therefore,  $K = 4/10$  for the multiplier (excluding the divider network).

Step 1. The first step is to select current  $I_3$  and current  $I_{13}$ . There are no restrictions on the selection of either of these currents except the power dissipation of the device.  $I_3$  and  $I_{13}$  will normally be 1.0 mA or 2.0 mA. Further,  $I_3$  does not have to be equal to  $I_{13}$ , and there is normally no need to make them different. For this example, let

$$I_3 = I_{13} = 1.0 \text{ mA.}$$

Figure 21. Multiplier with Operational Amplifier Level Shift

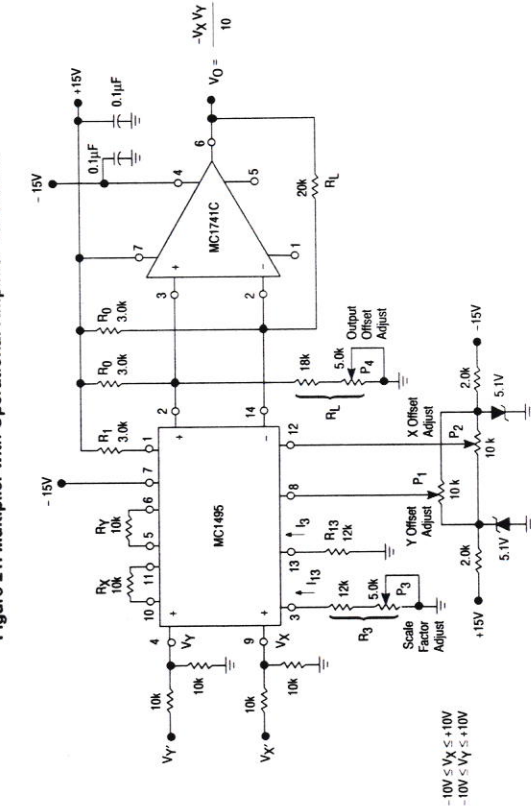


Figure 12. Power Supply Sensitivity

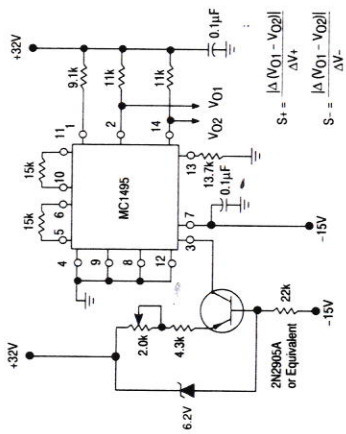


Figure 13. Offset Adjust Circuit

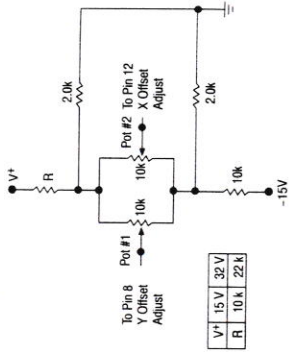


Figure 14. Offset Adjust Circuit (Alternate)

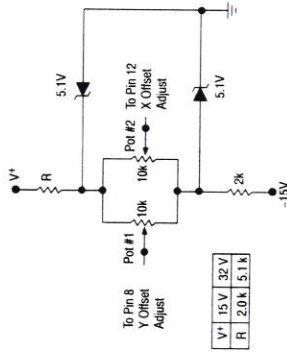


Figure 15. Linearity versus Temperature

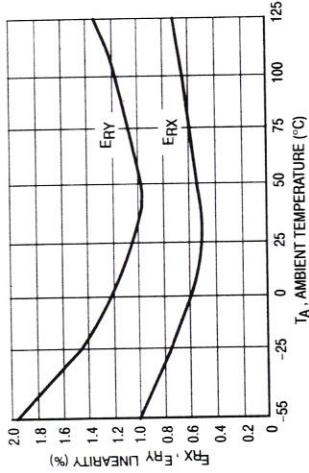


Figure 16. Scale Factor versus Temperature

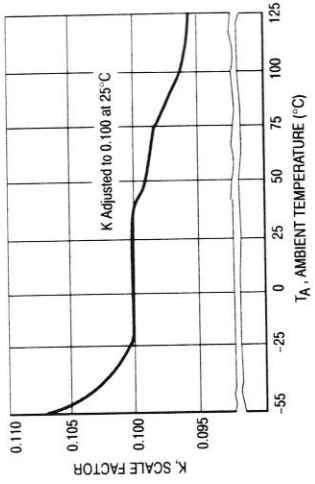


Figure 17. Error Contributed by Input Differential Amplifier

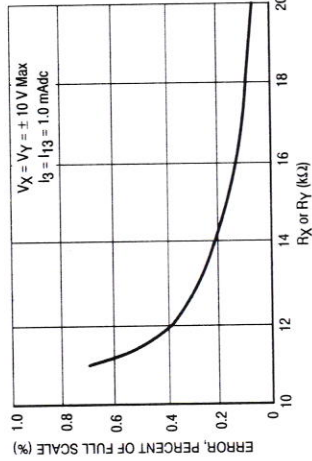


Figure 18. Error Contributed by Input Differential Amplifier

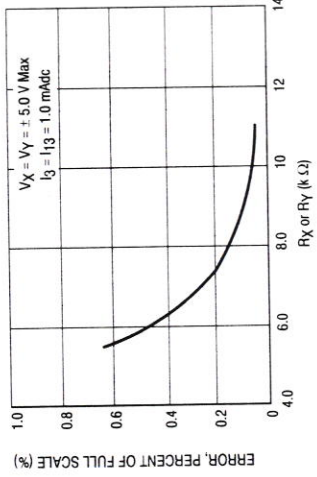
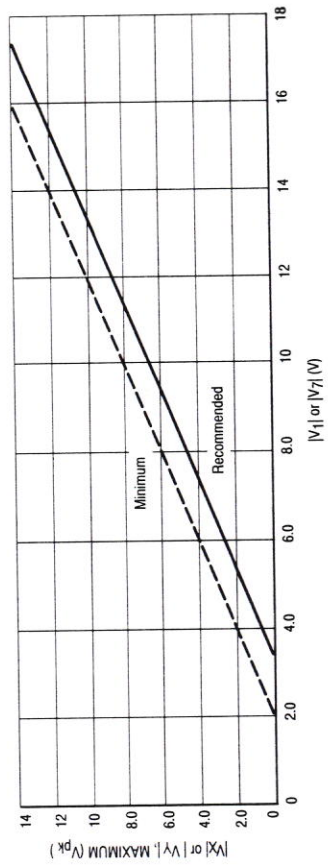


Figure 19. Maximum Allowable Input Voltage versus Voltage at Pin 1 or Pin 7

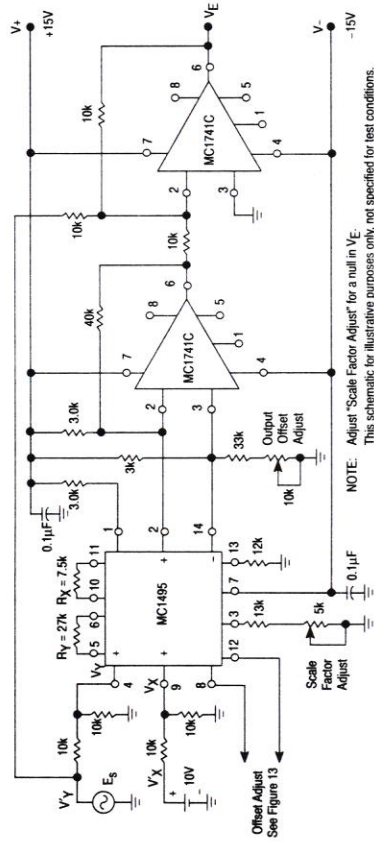


# MC1495, MC1595

MAXIMUM RATINGS (TA = +25°C, unless otherwise noted.)

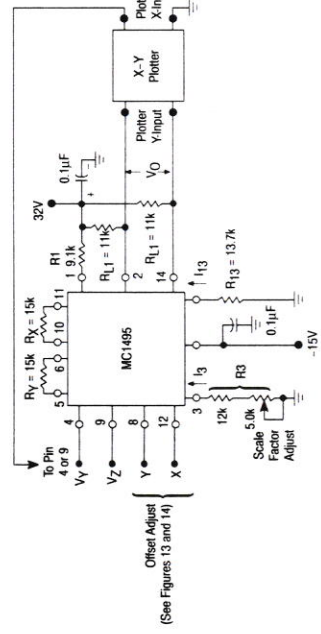
Rating	Symbol	Value	Unit
Applied Voltage (V2-V1, V14-V1, V1-V9, V1-V12, V1-V4, V1-V6, V12-V7, V9-V7, V6-V7, V4-V7)	$\Delta V$	30	Vdc
Differential Input Signal	V12-V9 V4-V8	$\pm(6+I_3 R_X)$ $\pm(6+I_3 R_Y)$	Vdc
Maximum Bias Current	I3 I13	10 10	mA
Power Dissipation (Package Limitation) D Suffix, Plastic Package Derate above TA = +25°C J Suffix, Ceramic Package Derate above TA = +25°C	PD	862 145 750 5.0	mW mW/ °C/W
Operating Temperature Range	TA	0 to +70 -55 to +125	°C
Storage Temperature Range	Tstg	-65 to +150	°C

Figure 4. Linearity (Using Null Technique)



NOTE: Adjust "Scale Factor Adjust" for a null in VE.  
This schematic for illustrative purposes only, not specified for test conditions.

Figure 5. Linearity (Using X-Y Plotter Technique)



# MC1495, MC1595

Figure 6. Input and Output Current

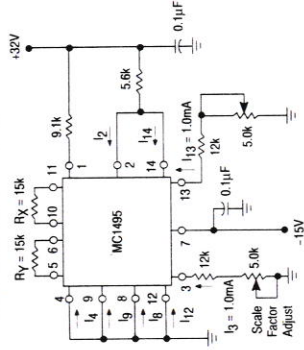


Figure 7. Input Resistance

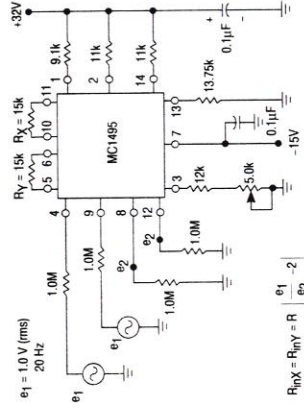


Figure 8. Output Resistance

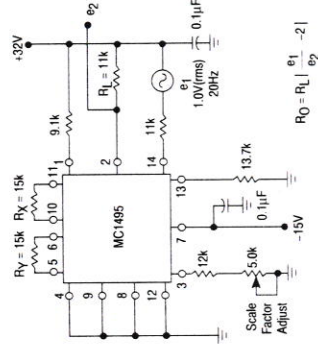


Figure 9. Bandwidth (RL = 11kΩ)

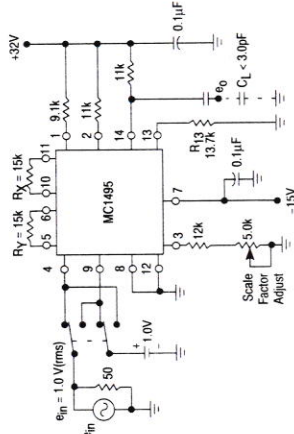


Figure 10. Bandwidth (RL = 50 Ω)

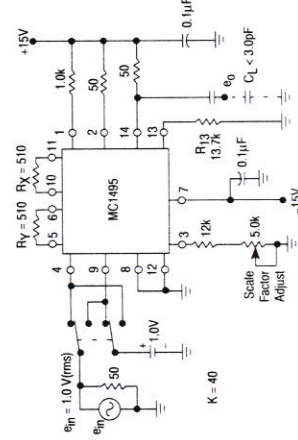
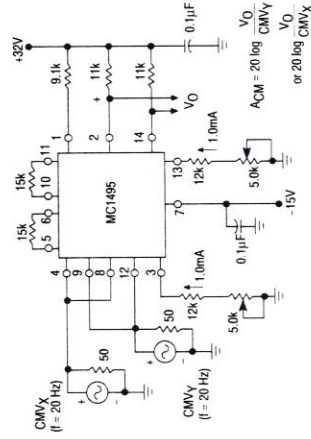


Figure 11. Common Mode Gain and Common Mode Input Swing



## Wideband Linear Four-Quadrant Multiplier

The MC1495/1595 is designed for use where the output is a linear product of two input voltages. Maximum versatility is assured by allowing the user to select the level-shift method. Typical applications include: multiply, divide, square root\*, mean square\*, phase detector, frequency doubler, balanced modulator/demodulator, and electronic gain control.

- Wide Bandwidth
- Excellent Linearity:
  - 2% max Error on X Input, 4% max Error on Y Input (MC1495)
  - 1% max Error on X Input, 2% max Error on Y Input (MC1595)
- Adjustable Scale Factor, K
- Excellent Temperature Stability
- Wide Input Voltage Range:  $\pm 10$  V
- $\pm 15$  V Operation

\*When used with an operational amplifier.

### LINEAR FOUR-QUADRANT MULTIPLIER

### SILICON MONOLITHIC INTEGRATED CIRCUIT



**D SUFFIX**  
PLASTIC PACKAGE  
CASE 751A  
(SO-14)



**L SUFFIX**  
CERAMIC PACKAGE  
CASE 632

Figure 1. Multiplier Transfer Characteristic

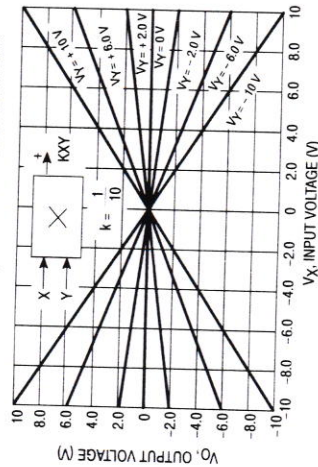


Figure 2. Transconductance Bandwidth

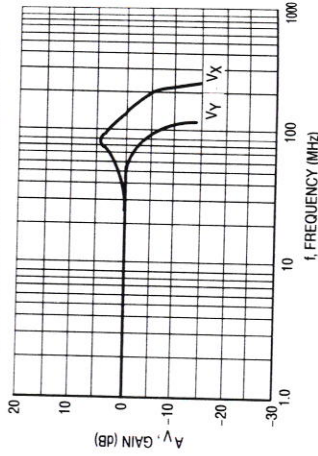
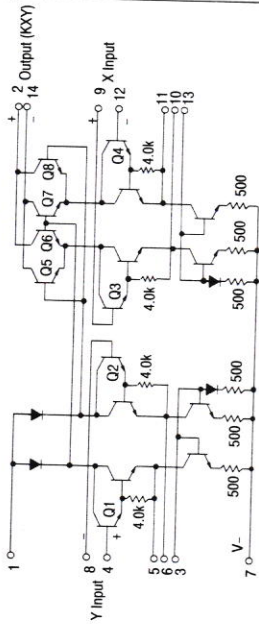


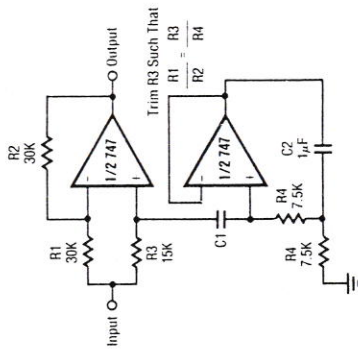
Figure 3. Circuit Schematic



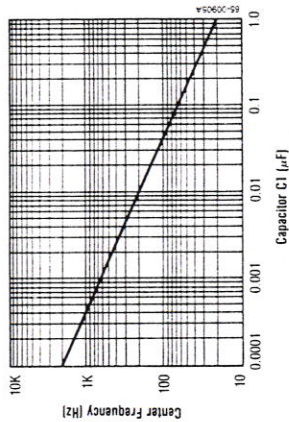
**ELECTRICAL CHARACTERISTICS** ( $V = +32$  V,  $-V = -15$  V,  $T_A = +25^\circ\text{C}$ ,  $I_B = I_{I3} = 1.0$  mA,  $R_X = R_Y = 15$  k $\Omega$ ,  $R_L = 11$  k $\Omega$ , unless otherwise noted.)

Characteristics	Figure	Symbol	Min	Typ	Max	Unit
Linearity (Output Error in percent of full scale)	5					%
$T_A = +25^\circ\text{C}$						
$-10 < V_X < +10$ ( $V_Y = \pm 10$ V)		ERX	—	$\pm 1.0$	$\pm 2.0$	
$-10 < V_Y < +10$ ( $V_X = \pm 10$ V)		ERY	—	$\pm 0.5$	$\pm 1.0$	
$T_A = 0^\circ$ to $+70^\circ\text{C}$						
$-10 < V_X < +10$ ( $V_Y = \pm 10$ V)		ERX	—	$\pm 1.5$	—	
$-10 < V_Y < +10$ ( $V_X = \pm 10$ V)		ERY	—	$\pm 3.0$	—	
$T_A = -55^\circ$ to $+125^\circ\text{C}$						
$-10 < V_X < +10$ ( $V_Y = \pm 10$ V)		ERX	—	$\pm 0.75$	—	
$-10 < V_Y < +10$ ( $V_X = \pm 10$ V)		ERY	—	$\pm 1.5$	—	
Square Mode Error (Accuracy in percent of full scale after Offset and Scale Factor adjustment)	5	ESQ	—	—	—	%
$T_A = +25^\circ\text{C}$						
MC1495			—	$\pm 0.75$	—	
MC1595			—	$\pm 0.5$	—	
$T_A = 0^\circ$ to $+70^\circ\text{C}$						
MC1495			—	$\pm 1.0$	—	
MC1595			—	$\pm 0.75$	—	
Scale Factor (Adjustable)	—	K	—	0.1	—	
$K = \frac{2R_L}{R_X R_Y}$						
Input Resistance ( $f = 20$ Hz)	7	$R_{InX}$	—	30	—	M $\Omega$
MC1495			—	35	—	
MC1595			—	20	—	
MC1495			—	35	—	
MC1595			—	300	—	k $\Omega$
Differential Output Resistance ( $f = 20$ Hz)	6	$R_O$	—	—	—	$\mu\text{A}$
Input Bias Current						
MC1495			—	2.0	12	
MC1595			—	2.0	8.0	
MC1495			—	2.0	12	
MC1595			—	2.0	8.0	
Input Offset Current						
MC1495			—	0.4	2.0	$\mu\text{A}$
MC1595			—	0.2	1.0	
MC1495			—	0.4	2.0	
MC1595			—	0.2	1.0	
Average Temperature Coefficient of Input Offset Current ( $T_A = 0^\circ$ to $+70^\circ\text{C}$ )	6	$ TC_{IO} $	—	2.5	—	nA/ $^\circ\text{C}$
( $T_A = -55^\circ$ to $+125^\circ\text{C}$ )			—	2.5	—	
Output Offset Current	6	$ IOO $	—	20	100	$\mu\text{A}$
MC1495			—	10	50	
MC1595			—	20	—	nA/ $^\circ\text{C}$
Average Temperature Coefficient of Output Offset Current ( $T_A = 0^\circ$ to $+70^\circ\text{C}$ )	6	$ TC_{IOO} $	—	20	—	nA/ $^\circ\text{C}$
( $T_A = -55^\circ$ to $+125^\circ\text{C}$ )			—	20	—	
Frequency Response	9,10	BW(3dB) TBW(3dB) $f_0$	—	—	—	MHz
3.0 dB Bandwidth, $R_L = 11$ k $\Omega$			—	3.0	—	
3.0 dB Bandwidth, $R_L = 50 \Omega$ (Transconductance Bandwidth)			—	80	—	
3 $^\circ$ Relative Phase Shift Between $V_X$ and $V_Y$			—	750	—	
1% Absolute Error Due to Input-Output Phase Shift			—	30	—	
Common Mode Input Swing (Either Input)	11	CMV	$\pm 10.5$ $\pm 11.5$	$\pm 12$ $\pm 13$	—	Vdc
MC1495						
MC1595						
Common Mode Gain (Either Input)	11	ACM	-40 -50	-50 -60	—	dB
MC1495						
MC1595						
Common Mode Quiescent Output Voltage	12	$V_{O1}$ $V_{O2}$	—	21	—	Vdc
MC1495						
MC1595						
Differential Output Voltage Swing Capability	9	$V_O$	—	$\pm 14$	—	Vpk
Power Supply Sensitivity	12	S+	—	5.0	—	mV/V
MC1495						
MC1595						
Power Supply Current	12	$I_7$	—	6.0	7.0	mA
DC Power Dissipation	12	PD	—	135	170	mW

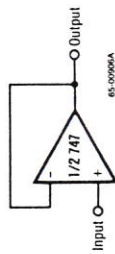
Typical Applications (Continued)



Notch Frequency as a Function of C1

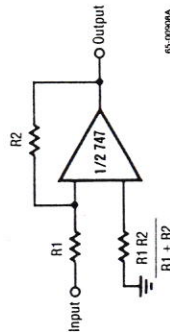


Notch Filter Using the 747 as a Gyration



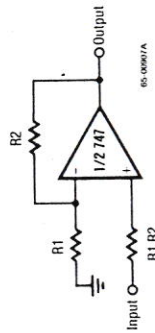
$R_{in} = 400M\Omega$   
 $C_{in} = 1pF$   
 $R_{out} \ll 1\Omega$   
 $BW = 1MHz$

Unity Gain Voltage Follower



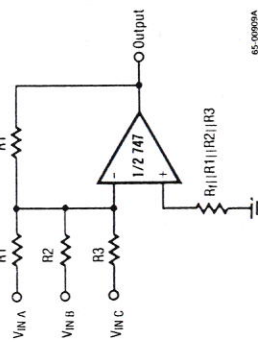
Gain	R1	R2	B.W.	R <sub>in</sub>
1	10kΩ	10kΩ	1MHz	10kΩ
10	1kΩ	10kΩ	100kHz	1kΩ
100	100Ω	10kΩ	10kHz	100Ω
1000	10Ω	10kΩ	1kHz	10Ω

Inverting Amplifier



Gain	R1	R2	B.W.	R <sub>in</sub>
10	1kΩ	9kΩ	100kHz	400MΩ
100	100Ω	9.9kΩ	10kHz	200MΩ
1000	100Ω	99.9kΩ	1kHz	80MΩ

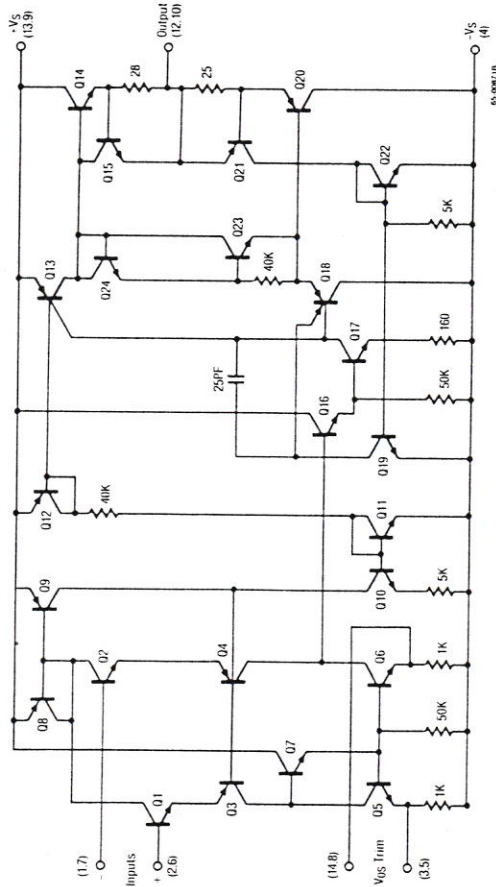
Non-Inverting Amplifier



$$-V_O = V_{in A} \left( \frac{R_1}{R_1} \right) - V_{in B} \left( \frac{R_1}{R_2} \right) - V_{in C} \left( \frac{R_1}{R_3} \right)$$

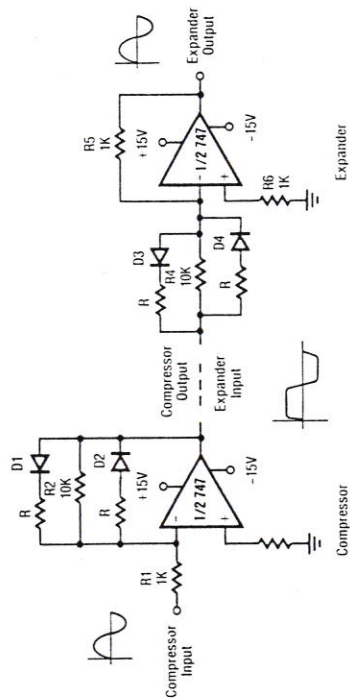
Weighted Averaging Amplifier

Schematic Diagram (1/2 Shown)



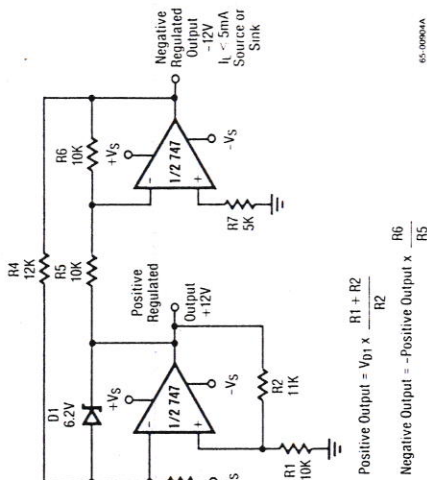


Typical Applications (Continued)



Compressor  
Expander  
Maximum compression expansion ratio =  $R1/R$  ( $10K\Omega > R \geq 0$ )  
Note: Diodes D1 through D4 are matched FD6666 or equivalent  
65-00903A

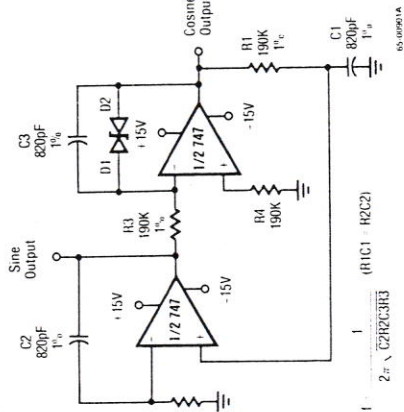
Compressor/Expander Amplifiers



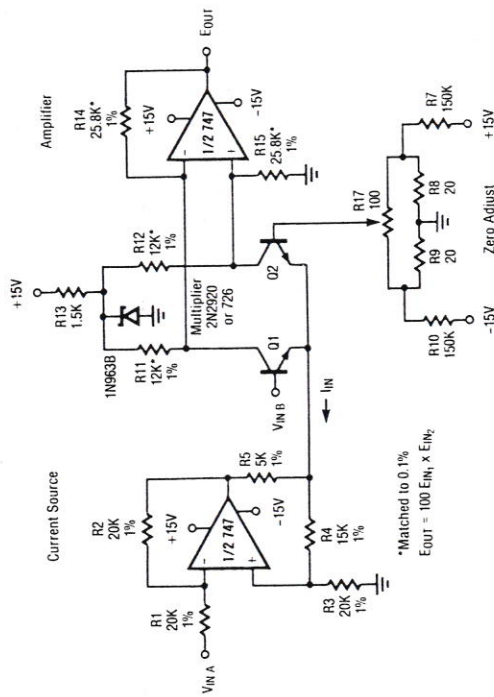
Positive Regulated Output +12V  
Negative Regulated Output -12V  
I<sub>s</sub> = 5mA  
Source or Sink  
Positive Output =  $V_{O1} \times \frac{R1 + R2}{R2}$   
Negative Output =  $-V_{O2} \times \frac{R6}{R5}$   
65-00904A

Tracking Positive and Negative Voltage References

Typical Applications



Quadrature Oscillator

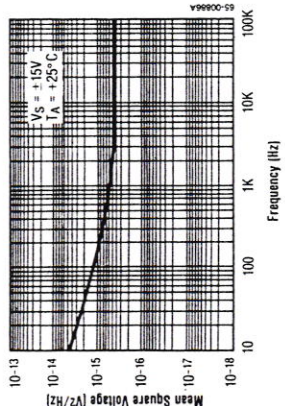


\*Matched to 0.1%  
E<sub>OUT</sub> = 100 E<sub>N1</sub> x E<sub>N2</sub>

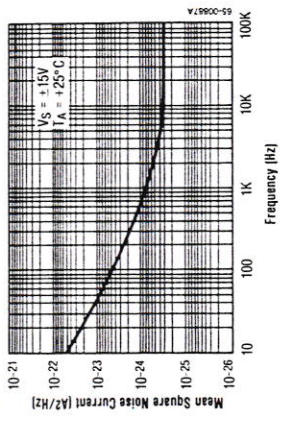
Analog Multiplier

### Typical Performance Characteristics (Continued)

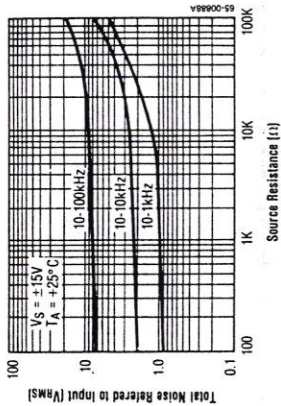
Input Noise Voltage as a Function of Frequency



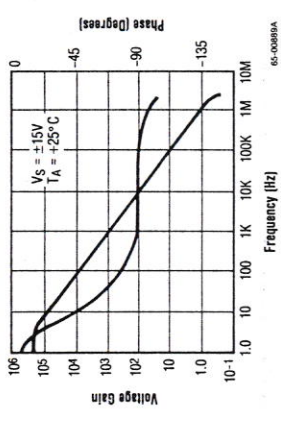
Input Noise Current as a Function of Frequency



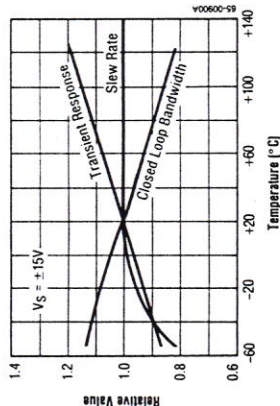
Broadband Noise for Various Bandwidths



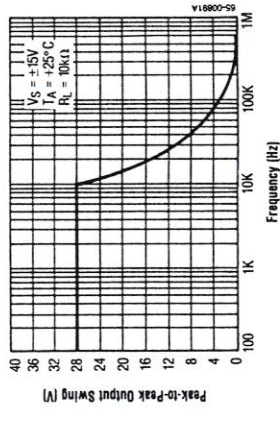
Open Loop Voltage Gain as a Function of Frequency



Frequency Characteristics as a Function of Ambient Temperature

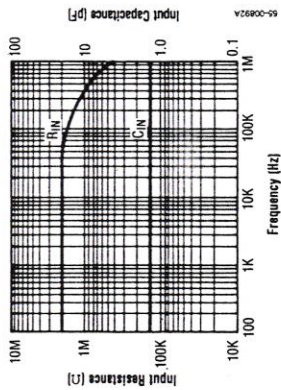


Output Voltage Swing as a Function of Frequency

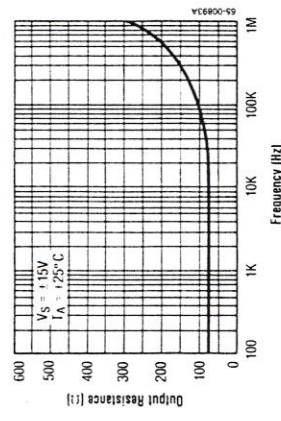


### Typical Performance Characteristics (Continued)

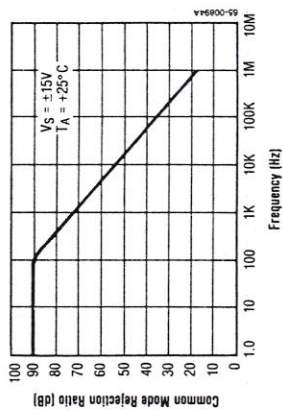
Input Resistance and Input Capacitance as a Function of Frequency



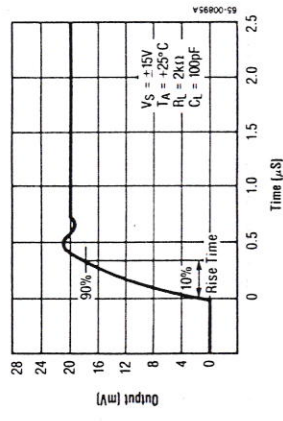
Output Resistance as a Function of Frequency



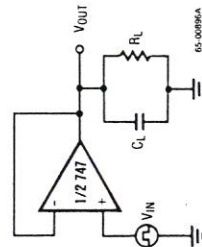
Common Mode Rejection Ratio as a Function of Frequency



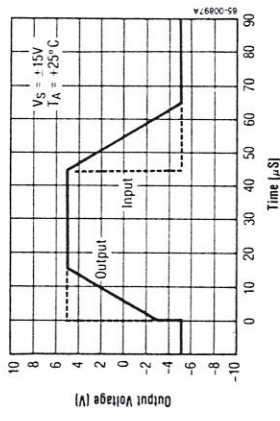
Transient Response



Transient Response Test Circuit

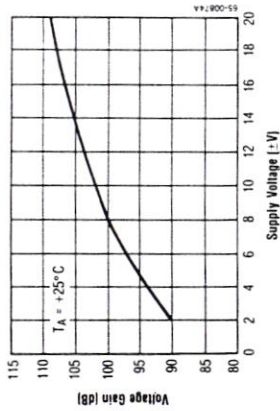


Voltage Follower Large Signal Pulse Response

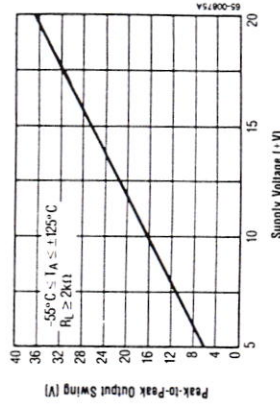


### Typical Performance Characteristics (Continued)

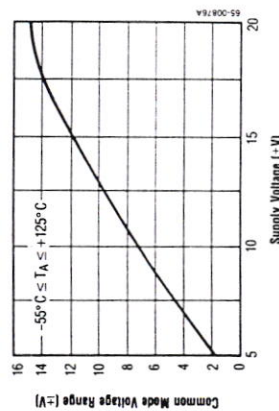
Open Loop Voltage Gain as a Function of Supply Voltage



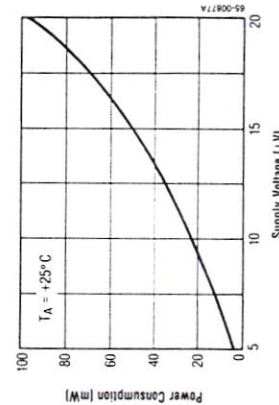
Output Voltage Swing as a Function of Supply Voltage



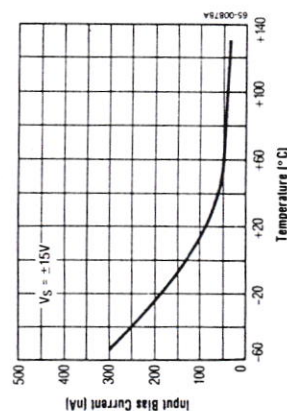
Input Common Mode Voltage Range as a Function of Supply Voltage



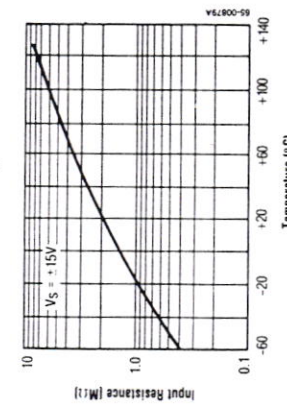
Power Consumption as a Function of Supply Voltage



Input Bias Current as a Function of Ambient Temperature

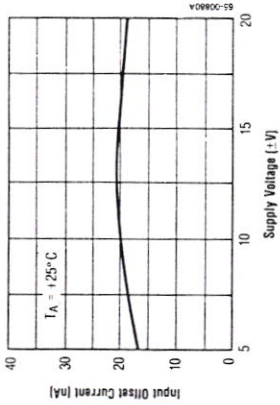


Input Resistance as a Function of Ambient Temperature

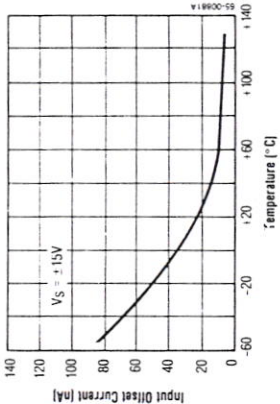


### Typical Performance Characteristics (Continued)

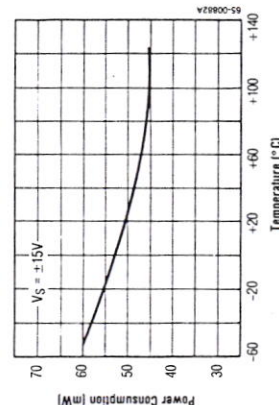
Input Offset Current as a Function of Supply Voltage



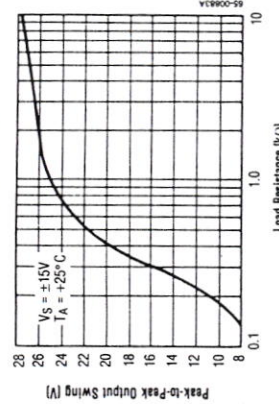
Input Offset Current as a Function of Ambient Temperature



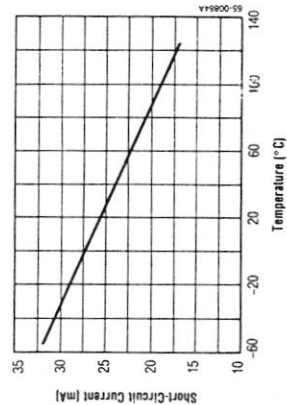
Power Consumption as a Function of Ambient Temperature



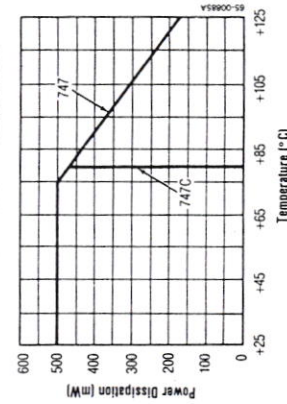
Output Voltage Swing as a Function of Load Resistance



Output Short Circuit Current as a Function of Ambient Temperature



Absolute Maximum Power Dissipation as a Function of Ambient Temperature



**Thermal Characteristics**

	14-Lead Plastic DIP	14-Lead Ceramic DIP	10-Lead TO-100 Metal Can
Max. Junction Temp.	125°C	175°C	175°C
Max. P <sub>D</sub> T <sub>A</sub> < 50°C	468 mW	1042 mW	658 mW
Therm. Res. $\theta_{JC}$	—	60°C/W	50°C/W
Therm. Res. $\theta_{JA}$	160°C/W	120°C/W	190°C/W
For T <sub>A</sub> > 50°C Derate at	6.25 mW/°C	8.33 mW/°C	5.26 mW/°C

**Electrical Characteristics** (V<sub>S</sub> = ±15V and T<sub>A</sub> = +25°C unless otherwise noted)

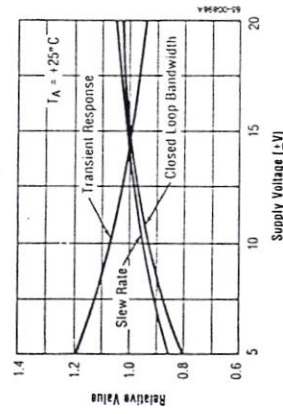
Parameters	Test Conditions	RM747	RC747	Units
Input Offset Voltage	R <sub>S</sub> ≤ 10 kΩ	Min 1.0 Typ 5.0 Max 5.0	Min 2.0 Typ 6.0 Max 6.0	mV
Input Offset Current		20	20	nA
Input Bias Current		80	80	nA
Input Resistance (Diff. Mode)		0.3	0.3	MΩ
Large Signal Voltage Gain	R <sub>L</sub> ≥ 2 kΩ, V <sub>out</sub> = ±10V	50	50	V/mV
Output Voltage Swing	R <sub>L</sub> ≥ 10 kΩ	±12	±12	V
Input Voltage Range	R <sub>L</sub> ≥ 2 kΩ	±10	±10	V
Common Mode Rejection Ratio		±12	±13	V
Power Supply Rejection Ratio	R <sub>S</sub> ≤ 10 kΩ	80	90	dB
Power Consumption		76	90	dB
Transient Response		100	170	mW
Rise Time	V <sub>in</sub> = 20 mV, R <sub>L</sub> = 2 kΩ	0.3	0.3	μs
Overshoot	C <sub>L</sub> ≤ 100 pF	5.0	5.0	%
Slew Rate	R <sub>L</sub> ≥ 2 kΩ	0.5	0.5	V/μs
Channel Separation	f = 1 kHz	98	98	dB

**Electrical Characteristics** (-55°C ≤ T<sub>A</sub> ≤ +125°C for RM747; 0°C ≤ T<sub>A</sub> ≤ +70°C for RC747)

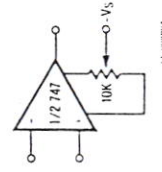
Parameters	Test Conditions	RM747	RC747	Units
Input Offset Voltage	R <sub>S</sub> ≤ 10 kΩ	Min 6.0 Typ 7.5 Max 7.5	Min 300 Typ 300 Max 300	mV
Input Offset Current	T <sub>A</sub> = +125°C, T <sub>A</sub> = +70°C	200	300	nA
Input Bias Current	T <sub>A</sub> = -55°C T <sub>A</sub> = 0°C	500	300	nA
Input Bias Current	T <sub>A</sub> = +125°C, T <sub>A</sub> = +70°C	500	800	nA
Large Signal Voltage Gain	T <sub>A</sub> = -55°C, T <sub>A</sub> = 0°C	1500	800	nA
Output Voltage Swing	R <sub>L</sub> ≥ 2 kΩ, V <sub>out</sub> = ±10V	25	25	V/mV
Common Mode Rejection Ratio	R <sub>L</sub> ≥ 10K	±12	±10	V
Power Supply Rejection Ratio	R <sub>S</sub> ≤ 10 kΩ	70	70	dB
Power Consumption	R <sub>S</sub> = ±10 kΩ T <sub>A</sub> = +125°C	76	76	dB
Input Voltage Range	T <sub>A</sub> = -55°C	±12	±12	V

**Typical Performance Characteristics**

Frequency Characteristics as a Function of Ambient Temperature



Voltage Offset Null Circuit



# RC747 *Lg Mr. 13144*

## General Purpose Operational Amplifier

### Features

- Short circuit protection
- No frequency compensation required
- No latch-up
- Large common mode and differential voltage ranges
- Low power consumption
- Parameter tracking over temperature range
- Gain and phase match between amplifiers

### Description

The RC/RM747 integrated circuits are high gain, operational amplifiers internally compensated and constructed on a single silicon chip using an advanced epitaxial process.

The military version, RM747, operates over a temperature range from -55°C to +125°C. The commercial version, RC747, operates from 0°C to +70°C.

Combining the features of the 741 with the close parameter matching and tracking of a dual device on a monolithic chip results in unique performance characteristics. Excellent channel separation allows the use of the dual device in all single 741 operational amplifier applications providing high packaging density. It is especially well suited for applications in differential-in, differential-out as well as in potentiometric amplifiers and where gain and phase matched channels are mandatory.

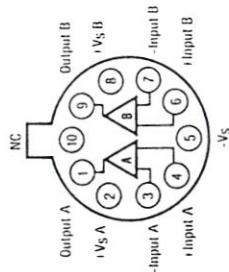
### Ordering Information

Part Number	Package	Operating Temperature Range
RC747N	N	0°C to +70°C
RC747T	T	0°C to +70°C
RM747D	D	-55°C to +125°C
RM747D/883B*	D	-55°C to +125°C
RM747T	T	-55°C to +125°C
RM747T/883B*	T	-55°C to +125°C

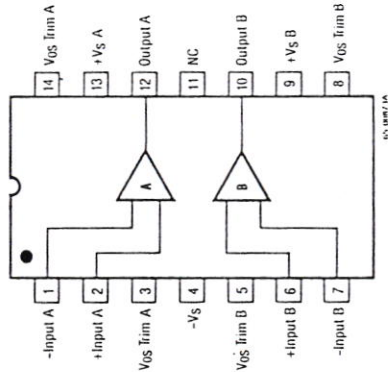
Notes:  
 \*883B suffix denotes Mil-Std-883, Level B processing  
 N = 14-lead plastic DIP  
 D = 10-lead ceramic DIP  
 T = 10-lead metal can TO-99  
 Contact a Raytheon sales office or representative for ordering information on special package/temperature range combinations.

### Connection Information

10-Lead TO-100 Metal Can (Top View)



14-Lead Dual In-Line Package (Top View)



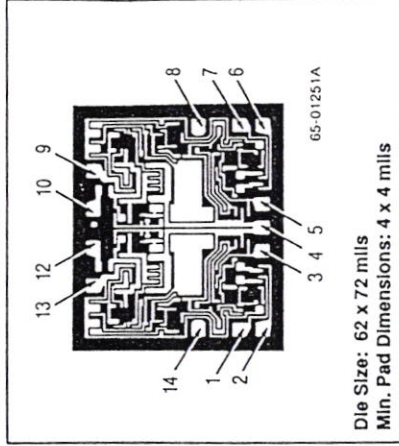
+Vs A is internally connected to +Vs B for the 747S

### Absolute Maximum Ratings

Supply Voltage	.....±22V
RM747	.....±18V
RC747	.....±15V
Differential Input Voltage	.....30V
Input Voltage*	.....±15V
Output Short-Circuit Duration**	.....Indefinite
Storage Temperature Range	.....-65°C to +150°C
Operating Temperature Range	.....-55°C to +125°C
RM747	.....0°C to +70°C
RC747	.....0°C to +70°C
Lead Soldering Temperature (60 sec)	.....+300°C

\*For supply voltages less than ±15V, the absolute maximum input voltage is equal to the supply voltage.  
 \*\*Short-circuit may be to ground or either supply. Rating applies to +125°C case temperature or +75°C ambient temperature for RC747.

### Mask Pattern



Die Size: 62 x 72 mils  
 Min. Pad Dimensions: 4 x 4 mils

# Am733/733C

## Differential Video Amplifier

### Distinctive Characteristics

The Am733 and Am733C differential video amplifiers are functionally, electrically and pin-for-pin equivalent to the Fairchild  $\mu$ A733 and 733C.

Bandwidths: 40 to 120 MHz

Rise Times: 2.5 to 10 ns

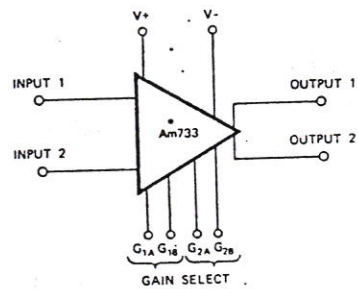
Propagation Delay: 3.6 to 10 ns

- 100% reliability assurance testing in compliance with MIL STD 883.
- Electrically tested and optically inspected dice for hybrid manufacturers.
- Mixing privilege for obtaining price discounts. Refer to price list.
- Available in metal can, hermetic dual-in-line or hermetic flat packages.

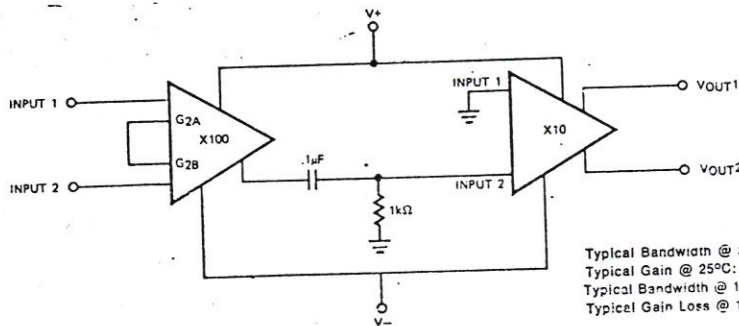
### FUNCTIONAL DESCRIPTION

The Am733 is a monolithic two-stage differential input, emitter follower differential output video amplifier. Internal series-shunt feedback is used to obtain fixed gains of 10, 100 or 400, and adjustable gains from 10 to 400 by the use of an external resistor.

### FUNCTIONAL DIAGRAM



### TYPICAL APPLICATION HIGH-GAIN WIDEBAND AMPLIFIER



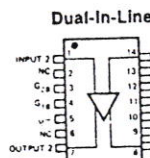
Typical Bandwidth @ 25°C: 65 MHz  
 Typical Gain @ 25°C: 54 dB  
 Typical Bandwidth @ 125°C: 57 MHz  
 Typical Gain Loss @ 125°C: 1 dB

### ORDERING INFORMATION

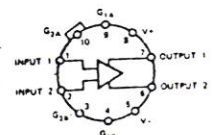
Part Number	Package Type	Temperature Range	Order Number
Am733C	Metal Can	0°C - +70°C	U5F7733393
	DIP	0°C - +70°C	U6A7733393
	Flat Pak	0°C - +70°C	U3F7733393
Am733	Metal Can	-55°C - +125°C	U5F7733312
	DIP	-55°C - +125°C	U6A7733312
	Flat Pak	-55°C - +125°C	U3F7733312
Am733	Dice	Note	UXX7733XXD

Note: The dice supplied will contain units which meet 0°C to 70°C, and -55°C to +125°C temperature ranges.

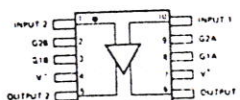
### CONNECTION DIAGRAMS Top Views



### Metal Can



### Flat Package



### NOTES:

- (1) On Metal Can, pin 5 is connected to case.
- (2) On DIP, pin 5 is connected to bottom of package.
- (3) On Flat Package, pin 4 is connected to bottom of package.

# Am733/733C

## Differential Video Amplifier

### Distinctive Characteristics

The Am733 and Am733C differential video amplifiers are functionally, electrically and pin-for-pin equivalent to the Fairchild  $\mu$ A733 and 733C.

Bandwidths: 40 to 120 MHz

Rise Times: 2.5 to 10 ns

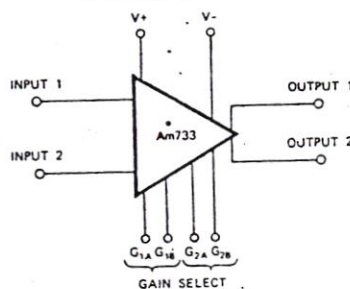
Propagation Delay: 3.6 to 10 ns

- 100% reliability assurance testing in compliance with MIL STD 883.
- Electrically tested and optically inspected dice for hybrid manufacturers.
- Mixing privilege for obtaining price discounts. Refer to price list.
- Available in metal can, hermetic dual-in-line or hermetic flat packages.

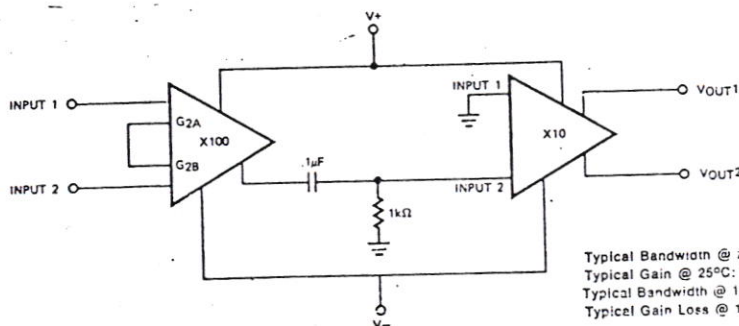
### FUNCTIONAL DESCRIPTION

The Am733 is a monolithic two-stage differential input, emitter follower differential output video amplifier. Internal series-shunt feedback is used to obtain fixed gains of 10, 100 or 400, and adjustable gains from 10 to 400 by the use of an external resistor.

### FUNCTIONAL DIAGRAM



### TYPICAL APPLICATION HIGH-GAIN WIDEBAND AMPLIFIER



Typical Bandwidth @ 25°C: 65 MHz  
 Typical Gain @ 25°C: 54 dB  
 Typical Bandwidth @ 125°C: 57 MHz  
 Typical Gain Loss @ 125°C: 1 dB

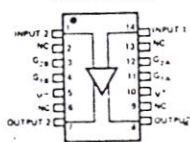
### ORDERING INFORMATION

Part Number	Package Type	Temperature Range	Order Number
Am733C	Metal Can	0°C - +70°C	U5F7733393
	DIP	0°C - +70°C	U6A7733393
	Flat Pak	0°C - +70°C	U3F7733393
Am733	Metal Can	-55°C - +125°C	U5F7733312
	DIP	-55°C - +125°C	U6A7733312
	Flat Pak	-55°C - +125°C	U3F7733312
Am733	Dice	Note	UXX7733XXD

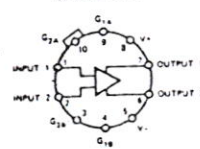
Note: The dice supplied will contain units which meet 0°C to 70°C, and -55°C to +125°C temperature ranges.

### CONNECTION DIAGRAMS Top Views

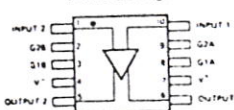
#### Dual-In-Line



#### Metal Can



#### Flat Package



#### NOTES:

- (1) On Metal Can, pin 5 is connected to case.
- (2) On DIP, pin 5 is connected to bottom of package.
- (3) On Flat Package, pin 4 is connected to bottom of package.

LM733/LM733C

Typical Performance Characteristics (Continued)

Test Circuits

TL/H/7866-3

TL/H/7866-4

Voltage Gain Adjust Circuit

TL/H/7866-5

$V_S = 6V, T_A = 25^\circ C$

(Pin numbers apply to TO-5 package)

Schematic Diagram

Numbers in parentheses show DIP connections.

TL/H/7866-7

3-16

3-17

TL/H/7866-8

3



**Absolute Maximum Ratings**

If Military/Aerospace specified devices are required, contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Differential Input Voltage	±5V	Power Dissipation (Note 1)	500 mW
Common Mode Input Voltage	±6V	Junction Temperature	+150°C
V <sub>CC</sub>	±8V	Storage Temperature Range	-65°C to +150°C
Output Current	10 mA	Operating Temperature Range	-55°C to +125°C 0°C to +70°C 260°C
		Lead Temperature (Soldering, 10 sec.)	260°C

**Electrical Characteristics** (T<sub>A</sub> = 25°C, unless otherwise specified, see test circuits, V<sub>S</sub> = ±6.0V)

Characteristics	Test Circuit	Test Conditions	LM733		LM733C		Units	
			Min	Typ	Max	Min		Typ
Differential Voltage Gain	1	R <sub>L</sub> = 2 kΩ, V <sub>OUT</sub> = 3 Vp-p	300	400	500	250	400	600
Gain 1 (Note 2)			90	100	110	80	100	120
Gain 2 (Note 3)			9.0	10	11	8.0	10	12
Gain 3 (Note 4)								
Bandwidth	2	V <sub>OUT</sub> = 1 Vp-p	40	90	120	40	90	120
Gain 1			10.5	4.5	10	10.5	4.5	12
Gain 2			4.5	2.5		4.5	2.5	
Gain 3								
Rise Time	2	V <sub>OUT</sub> = 1 Vp-p	7.5	6.0	3.6	7.5	6.0	3.6
Gain 1			7.5	6.0	3.6	7.5	6.0	3.6
Gain 2			6.0	3.6		6.0	3.6	
Gain 3								
Propagation Delay	2	V <sub>OUT</sub> = 1 Vp-p	4.0	30	250	4.0	30	250
Gain 1			4.0	30	250	4.0	30	250
Gain 2			30	250		30	250	
Gain 3								
Input Resistance		Gain 2	20	2.0		2.0	2.0	kΩ
Gain 1			20	2.0		2.0	2.0	kΩ
Gain 2			2.0			2.0		kΩ
Gain 3								
Input Capacitance		Gain 2	0.4	3.0		0.4	3.0	pF
Gain 1			0.4	3.0		0.4	3.0	pF
Gain 2			3.0			3.0		pF
Gain 3								
Input Offset Current		Gain 2	9.0	20		9.0	20	μA
Gain 1			9.0	20		9.0	20	μA
Gain 2			20			20		μA
Gain 3								
Input Bias Current		Gain 2	12	±1.0		12	±1.0	μA
Gain 1			12	±1.0		12	±1.0	μA
Gain 2			±1.0			±1.0		μA
Gain 3								
Input Noise Voltage		Gain 2	60	86	60	60	86	μVrms
Gain 1			60	86	60	60	86	μVrms
Gain 2			86	60	60	86	60	μVrms
Gain 3								
Input Voltage Range		Gain 2	±1.0	±1.0		±1.0	±1.0	V
Gain 1			±1.0	±1.0		±1.0	±1.0	V
Gain 2			±1.0	±1.0		±1.0	±1.0	V
Gain 3								
Common Mode Rejection Ratio	1	V <sub>CM</sub> = ±1V f ≤ 100 kHz V <sub>CM</sub> = ±1V f = 5 MHz	2.4	2.9	3.4	2.4	2.9	3.4
Gain 1			2.4	2.9	3.4	2.4	2.9	3.4
Gain 2			2.9	3.4	3.0	3.0	4.0	3.0
Gain 3								
Supply Voltage Rejection Ratio	1	ΔV <sub>S</sub> = ±0.5V	2.5	3.6		2.5	3.6	dB
Gain 1			2.5	3.6		2.5	3.6	dB
Gain 2			3.6			3.6		dB
Gain 3								
Output Offset Voltage	1	R <sub>L</sub> = ∞	0.6	1.5		0.6	1.5	V
Gain 1			0.6	1.5		0.6	1.5	V
Gain 2 and 3			1.5			1.5		V
Output Common Mode Voltage	1	R <sub>L</sub> = ∞	2.4	2.9	3.4	2.4	2.9	3.4
Gain 1			2.4	2.9	3.4	2.4	2.9	3.4
Gain 2			2.9	3.4	3.0	3.0	4.0	3.0
Gain 3								
Output Voltage Swing	1	R <sub>L</sub> = 2k	2.5	3.6		2.5	3.6	mA
Gain 1			2.5	3.6		2.5	3.6	mA
Gain 2			3.6			3.6		mA
Gain 3								
Output Sink Current	1	R <sub>L</sub> = ∞	18	24		18	24	mA
Gain 1			18	24		18	24	mA
Gain 2 and 3			24			24		mA
Output Resistance	1	R <sub>L</sub> = ∞	18	24		18	24	Ω
Gain 1			18	24		18	24	Ω
Gain 2 and 3			24			24		Ω
Power Supply Current	1	R <sub>L</sub> = ∞	18	24		18	24	mA
Gain 1			18	24		18	24	mA
Gain 2 and 3			24			24		mA

**Electrical Characteristics** (Continued)

(The following specifications apply for -55°C < T<sub>A</sub> < 125°C for the LM733 and 0°C < T<sub>A</sub> < 70°C for the LM733C, V<sub>S</sub> = ±6.0V)

Characteristics	Test Circuit	Test Conditions	LM733		LM733C		Units	
			Min	Max	Min	Max		
Differential Voltage Gain	1	R <sub>L</sub> = 2 kΩ, V <sub>OUT</sub> = 3 Vp-p	200	600	250	600		
Gain 1			80	120	80	120		
Gain 2			8.0	12.0	8.0	12.0		
Gain 3								
Input Resistance Gain 2			8		8		kΩ	
Input Offset Current			5		6		μA	
Input Bias Current			40		40		μA	
Input Voltage Range	1		±1		±1		V	
Common Mode Rejection Ratio			Gain 2	V <sub>CM</sub> = ±1V f ≤ 100 kHz	50		50	dB
Supply Voltage Rejection Ratio					Gain 2	ΔV <sub>S</sub> = ±0.5V	50	
Output Offset Voltage	1	R <sub>L</sub> = ∞	1.5		1.5		V	
Gain 1			1.2		1.2		V	
Gain 2 and 3			2.5		2.5		V <sub>pp</sub>	
Output Voltage Swing	1	R <sub>L</sub> = 2k	2.2		2.5		mA	
Output Sink Current			2.2		2.5		mA	
Power Supply Current			27		27		27	mA

Note 1: The maximum junction temperature of the LM733 is 150°C, while that of the LM733C is 100°C. For operation at elevated temperatures devices in the TO-100 package must be derated based on a thermal resistance of 150°C/W junction to ambient or 45°C/W junction to case. Thermal resistance of the dual-in-line package is 90°C/W.

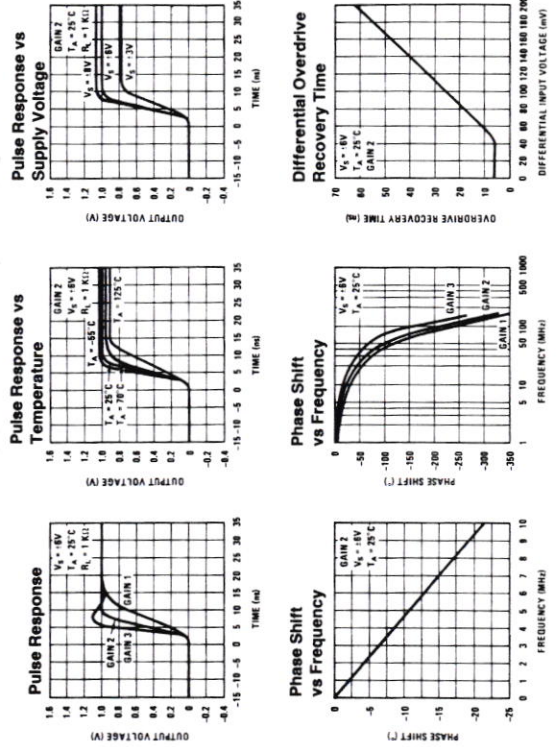
Note 2: Pins G1A and G1B connected together.

Note 3: Pins G2A and G2B connected together.

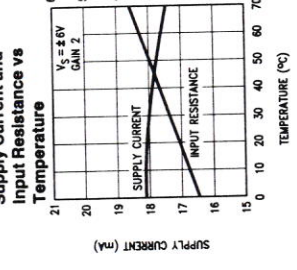
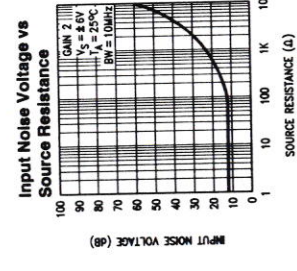
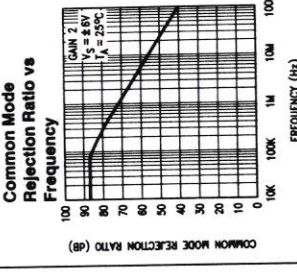
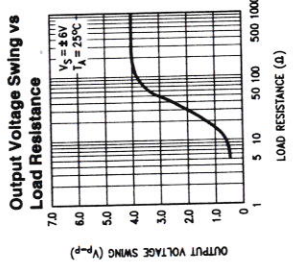
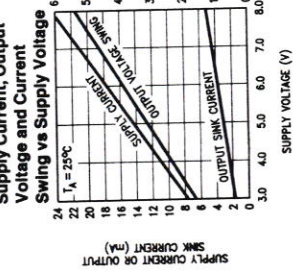
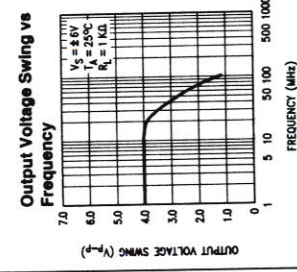
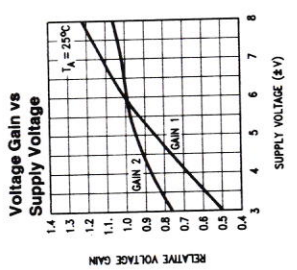
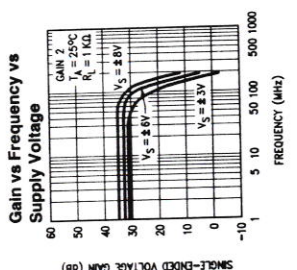
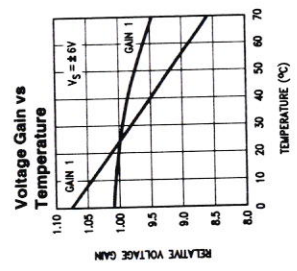
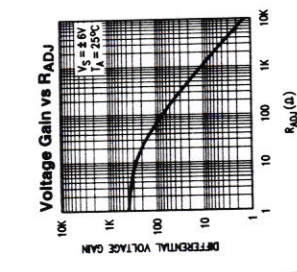
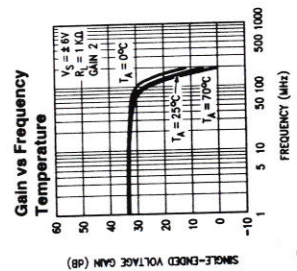
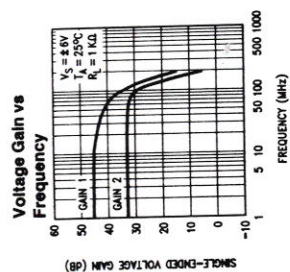
Note 4: Gain select pins open.

Note 5: Refer to RETS733X drawing for specifications of LM733H version.

**Typical Performance Characteristics**



Typical Performance Characteristics (Continued)



LM733/LM733C Differential Amp

General Description

The LM733/LM733C is a two-stage, differential input, differential output, wide-band video amplifier. The use of internal series-shunt feedback gives wide bandwidth with low phase distortion and high gain stability. Emitter-follower outputs provide a high current drive, low impedance capability. Its 120 MHz bandwidth and selectable gains of 10, 100 and 400, without need for frequency compensation, make it a very useful circuit for memory element drivers, pulse amplifiers, and wide band linear gain stages.

The LM733 is specified for operation over the -55°C to +125°C military temperature range. The LM733C is specified for operation over the 0°C to +70°C temperature range.

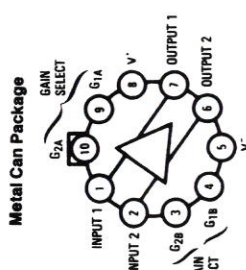
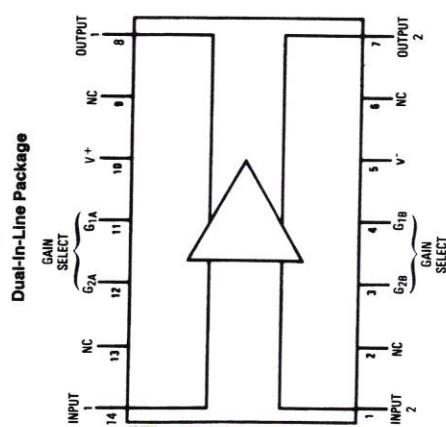
Features

- 120 MHz bandwidth
- 250 kΩ input resistance
- Selectable gains of 10, 100, 400
- No frequency compensation
- High common mode rejection ratio at high frequencies

Applications

- Magnetic tape systems
- Disk file memories
- Thin and thick film memories
- Woven and plated wire memories
- Wide band video amplifiers

Connection Diagrams



TL/H/7866-2

Note: Pin 5 connected to case.

Top View

Order Number LM733H or LM733CH  
See NS Package Number H10D

TL/H/7866-1

Top View

Order Number LM733CN  
See NS Package Number N14A

**JFET Low-Frequency/Low-Noise**

The following table is a listing of small-signal JFETs intended for low-noise applications in the audio range. These devices exhibit good linearity and are candidates for hi-fi and instrumentation equipment.

Bestell- bezeichnung	$R_e Y_{fs} @ f$		$R_e Y_{os} @ f$		$C_{iss}$ pF Max	$C_{rss}$ pF Max	$V_{(BR)GSS}$ $V_{(BR)GDO}$ V Min	$V_{GS(off)}$ V		$I_{DSS}$ mA	
	mmho Min	MHz	μmho Max	MHz				Min	Max	Min	Max

Case 20-03 – TO-206 AF (TO-72) – N-Channel

2N4220	1.0	15	10	15	6.0	2.0	30	–	4.0	0.5	3.0
2N4220A	1.0	15	10	15	6.0	2.0	30	–	4.0	0.5	3.0
2N3821	1.5	15	10	15	6.0	3.0	50	–	4.0	0.5	2.5
2N4221	2.0	15	20	15	6.0	2.0	30	–	6.0	2.0	6.0
2N3822	2.0	15	20	15	6.0	3.0	50	–	6.0	2.0	10
2N4222	2.5	15	40	15	6.0	2.0	30	–	8.0	5.0	15
2N4222A	2.5	15	40	15	6.0	2.0	30	–	8.0	5.0	15

Case 20-03 – TO-206AF (TO-72) – P-Channel

2N3909A	2.2	0.001	100	0.001	9.0	3.0	20	0.3	7.9	1.0	15
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Case 29-04 – TO-226AA (TO-92) – N-Channel

2N5458	1.5	15	50	15	7.0	3.0	25	1.0	7.0	2.0	9.0
2N5457	2.0	15	50	15	7.0	3.0	25	0.5	6.0	1.0	5.0
2N5459	2.0	15	50	15	7.0	3.0	25	2.0	8.0	4.0	16

Case 29-04 – TO-226AA (TO-92) – P-Channel

2N5460	1.0	0.001	50	0.001	7.0	2.0	40	0.75	6.0	1.0	5.0
2N5463	1.0	0.001	75	0.001	7.0	2.0	60	0.5	4.0	1.0	5.0
2N5461	1.5	0.001	50	0.001	7.0	2.0	40	0.5	7.5	2.0	9.0
2N5462	2.0	0.001	50	0.001	7.0	2.0	40	1.8	9.0	4.0	16
2N5465	2.0	0.001	75	0.001	7.0	2.0	60	1.5	6.0	4.0	16

**JFET High-Frequency Amplifiers**

The following is a listing of small-signal JFETs that are intended for hi-frequency applications. These are candidates for VHF/UHF oscillators, mixers and front-end amplifiers.

Bestell- bezeichnung	$R_e Y_{fs} @ f$		$R_e Y_{os} @ f$		$C_{iss}$ pF Max	$C_{rss}$ pF Max	NF @ $R_G = 1 K$		$V_{(BR)GSS}$ $V_{(BR)GDO}$ V Min	$V_{GS(off)}$ V		$I_{DSS}$ mA		
	mmho Min	MHz	μmho Max	MHz			dB Max	f MHz		Min	Min	Max	Min	Max

Case 20-03 – TO-206AF (TO-72) – N-Channel

2N4416	4.0	400	100	400	4.0	0.8	4.0	400	30	2.0	6.0	5.0	15
--------	-----	-----	-----	-----	-----	-----	-----	-----	----	-----	-----	-----	----

Case 27-02 – TO-206AC (TO-52) – N-Channel

U310	10	0.001	150	100	5.0	2.5	3.0*	450	25	2.5	6.0	24	60
------	----	-------	-----	-----	-----	-----	------	-----	----	-----	-----	----	----

\*Typical

Gehäuse s. Seite A05.066–A05.068

A05.060

Preise siehe A1.

**JFET High-Frequency Ampli**

Bestell- bezeichnung	$R_e Y_{fs} @ f$	
	mmho Min	MHz

Case 29-04 – TO-226AA (TO-92) – N-Channel

2N5484	2.5	100
2N5485	3.0	400
2N5486	3.5	400
J309	12*	100
J310	12*	100

**JFET Switches and Chopper**

The following is a listing of JFETs intended for

Bestell- bezeichnung	$r_{ds(on)}$ Ω Max
-------------------------	--------------------------

Case 20-03 – TO-206AF (TO-72) – P-Channel

2N3993	150
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Case 22-03 – TO-206AA (TO-18) – N-Channel

2N4856	26
2N4859	25
2N4391	30
2N4091	30
2N4857	40
2N4860	40
2N4092	50
2N4392	60
2N4858	60
2N4861	60
2N4093	80
2N4393	100
2N3972	100

Case 29-04 – TO-226AA (TO-92) – N-Channel

2N5638	30
J111	30
J112	30
MPF4392	60
2N5639	60
MPF4393	100
2N5640	100
J113	100
BF246A	35*

\*Typical

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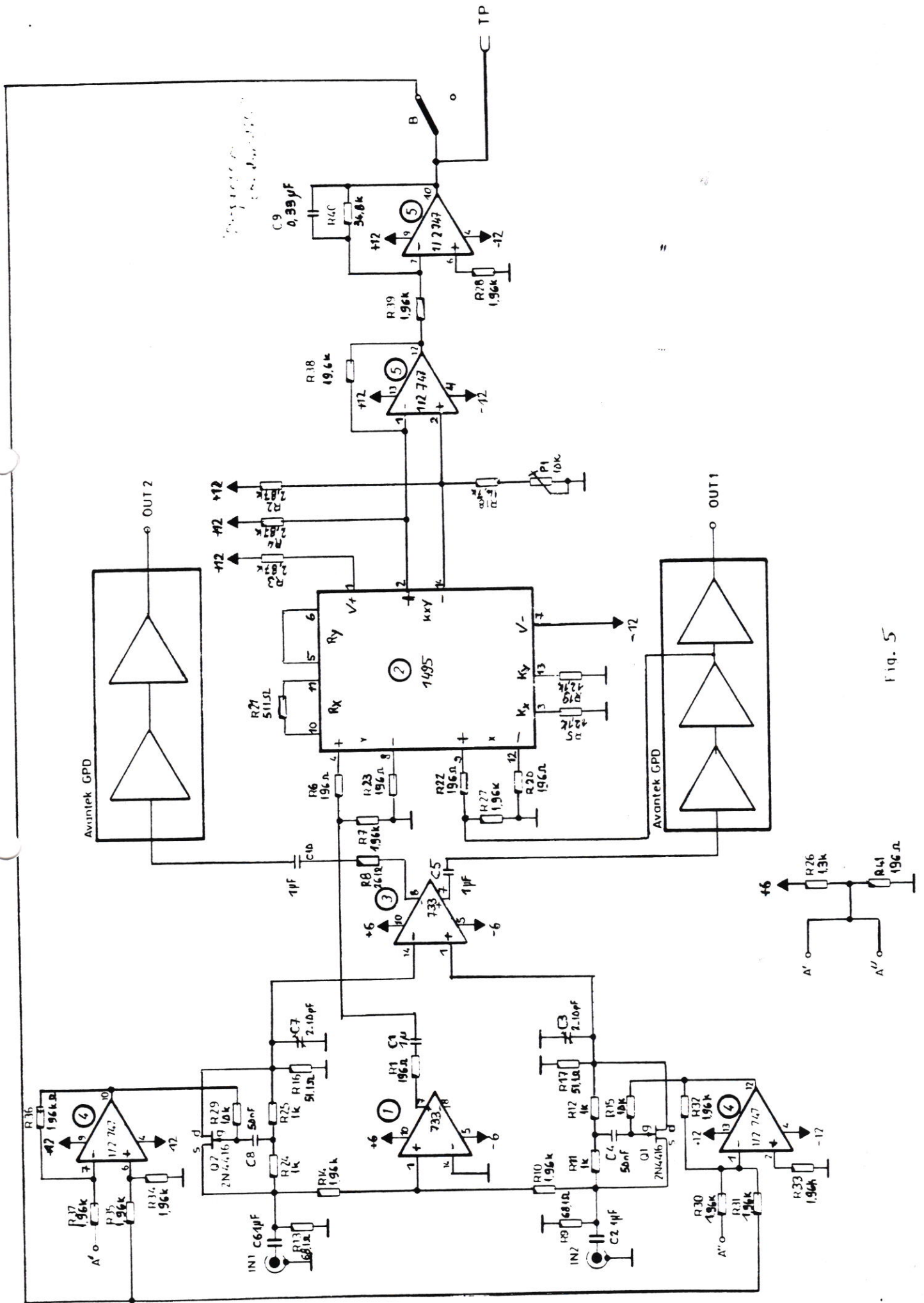
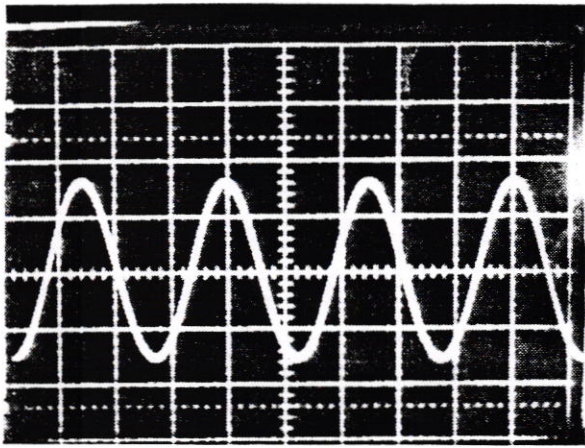
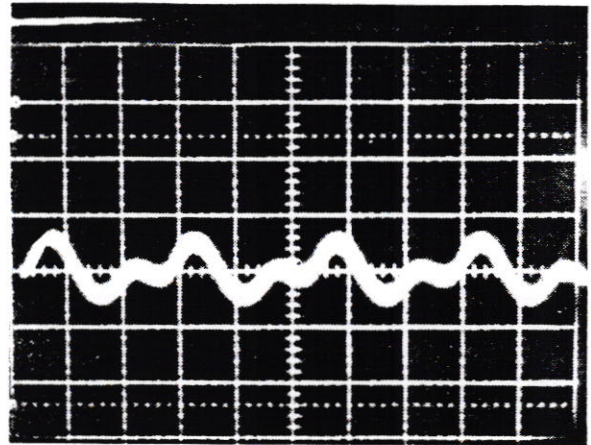


Fig. 5

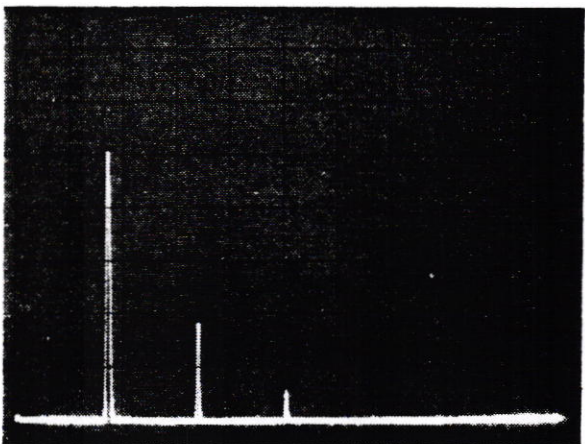


a)

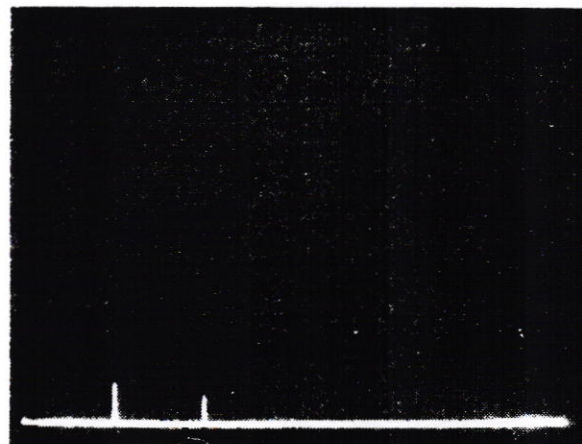


b)

Fig. 6



a)



b)

Fig. 7

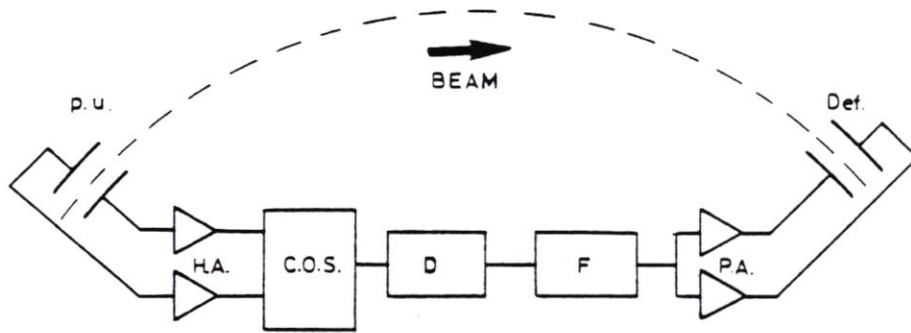


Fig. 1

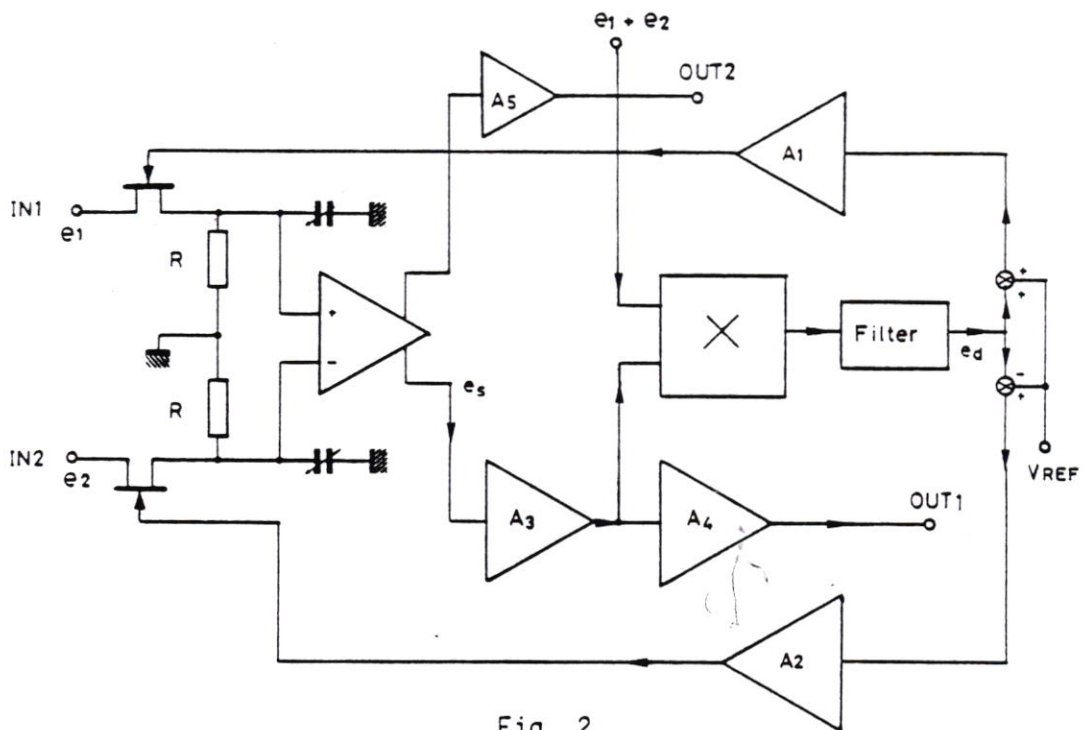


Fig. 2

- Suppression factor for sinusoidal input signals at 8 MHz carrier frequency, with 1.25 amplitude ratio: 34 dB min. at 500 mV<sub>pp</sub> common-mode input signal.
- Output impedance: 50 Ω.
- Output (1): Suppressed signal output, to be used for transverse feedback loop.
- Output (2): Observation output, with lower suppression factor.

#### 4. CALIBRATION PROCEDURE

##### 4.1 Internal switch in open-loop position

- a) Apply a 5 MHz, 20 mV<sub>pp</sub> signal to one of the inputs, terminating the other one with 50 Ω. Gradually increase the input level, observing the output signal.
- b) Verify that there is a 3 V<sub>pp</sub> min. saturation level of the GPD amplifier.
- c) Check the gain value of 35 ± 3 dB.
- d) Repeat the above measurements, interchanging the inputs.

##### 4.2 Internal switch in closed-loop position

- a) With both inputs 50 Ω terminated, adjust the offset potentiometer P1 until "zero" is reached on a 3 V voltmeter range at test point TP.
- b) With a phase splitter (50 Ω), apply a 8 MHz 500 mV<sub>pp</sub> signal to both inputs and observe the output with a 50 Ω input scope.
- c) Set trimmers C<sub>3</sub> and C<sub>7</sub> to their minimum capacitance value. Identify the one that reduces the output signal and use it to adjust a minimum on that signal. A strong second harmonic component should be observed (see Fig. 6b).
- d) Verify that when reducing the input level by 10 dB steps, the output reduces smoothly. If not, retouch potentiometer P1 in order to get the minimum output signal at minimum input level.
- e) Connect the splitter, with 2 dB difference in its output levels (specially made for this purpose), to the inputs. Care must be taken that the length of the cables used is matched within 0.15 ns (0.5 degrees at 8 MHz).
- f) Adjust a 1100 mV<sub>pp</sub> signal at the input of the splitter.
- g) Connect the spectrum analyser to the output. Verify that when inverting the inputs, the first harmonic of the output signal remains at approximately the same level. If not, retouch the trimmers used in step (c) (see Fig. 7).
- h) Short-circuit switch B with a wire in order to assure closed-loop operation of the module.

Considering the Fourier development of the signals, it can be proved<sup>2)</sup> that the power content (PC) of the differential voltage after suppression becomes

$$PC = kf^2t^2 \sum n^2c_n^2, \quad n = 1, 2, 3, \dots,$$

where  $t$  is the FET time constant,  $f$  is the fundamental frequency of the carrier, and  $c_n$  its Fourier coefficients.

The suppression factor can now be written as

$$F = \frac{\sum c_n^2}{kf^2t^2 \sum n^2c_n^2}, \quad n = 1, 2, 3, \dots$$

Using an RF FET such as the 2N 4416, a time constant as low as  $t = 0.4$  ns can be achieved.

For a bunch-formed carrier the form factor

$$\frac{\sum c_n^2}{\sum n^2c_n^2} \approx 0.6,$$

and for the PS Booster the highest carrier repetition frequency is  $f = 8$  MHz. Under these conditions the suppression factor becomes

$$F = 10^3.$$

This figure reduces the output power, at the present highest intensity of the PSB beam and with a gain of 2000 from pick-up to deflector, to less than 10 W.

### 2.3 Circuit details

The self-balancing bridge is made up of two attenuators with 2N 4416 FETs as controllable resistors. The lengths of the paths on the printed circuit from the inputs through the attenuators up to the differential amplifier inputs are carefully equalized, in order to avoid phase differences between the signals. A phase difference of 0.8 degrees at the fundamental frequency of the carrier reduces the suppression factor by a factor of two. This corresponds to a delay of 0.3 ns, so the lengths of the cables from the pick-up to the closed-orbit suppressor have to be equalized accordingly in order not to degrade the circuit performance. An adjustable coaxial delay line has been introduced in the signal path after the head amplifiers in order to adjust this length in the actual operating conditions.

The trimmers  $C_3$  and  $C_7$  provide the means for adjusting the reactive component of the bridge at balance.



## 1. SUMMARY AND INTRODUCTION

In the PSB (along with most circular particle accelerators and storage rings) transverse instabilities can develop, giving rise to fast oscillations of the beam around its equilibrium position (closed orbit).

These oscillations can be damped by feeding back to the beam (through an electromagnetic deflector) a signal that is proportional to them, and with the proper phase<sup>1)</sup> (Fig. 1).

As the power requirements of the deflector-driving amplifier depend on the amplitude of the signals it has to handle, there is a practical interest in keeping these signals no larger than is required for damping.

The instability signal can be obtained from an electrostatic pick-up similar to those normally used for orbit observation. Such a pick-up produces two signals, consisting of two bunch-shaped carriers modulated in opposite phase by the beam position. The difference between these two signals no longer contains the carrier, but only two sidebands with the spectrum of the beam position.

The beam position is the sum of the closed orbit plus the instabilities. The closed orbit can be, and normally is, predominant; therefore it largely determines the power in the spectrum and hence the power requirements of the deflector-driving amplifier.

It might therefore be interesting to remove the closed-orbit component of the pick-up difference signal and leave only the component due to the instabilities, which we need in order to operate the feedback system. This is what the circuit described in the following sections is designed to do, taking advantage of the fact that the closed orbit and the instabilities occupy different portions of the composite signal spectrum.

## 2. CIRCUIT DESCRIPTION

### 2.1 Functional description

The circuit is realized as a self-balancing bridge<sup>2)</sup>, reaching its equilibrium point when the closed-orbit component of the difference signal is zero. The bridge is shown in the block diagram (Fig. 2) as two FET-controlled attenuators driven by the phase opposing signals coming from amplifiers  $A_1$  and  $A_2$ . These closed-orbit signals are obtained by phase detection through an analog multiplier, making the product of the sum and difference signals, followed by a low-pass filter.

The transverse displacements of the beam appear as an amplitude modulation of the bunch signals. Whilst the amplitude of the modulating wave is the same on

*Becker*

CLOSED-ORBIT SIGNAL SUPPRESSOR  
FOR THE TRANSVERSE FEEDBACK OF THE PSB

C. Christiansen \*)

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\*) Visitor from La Plata University, Argentina.

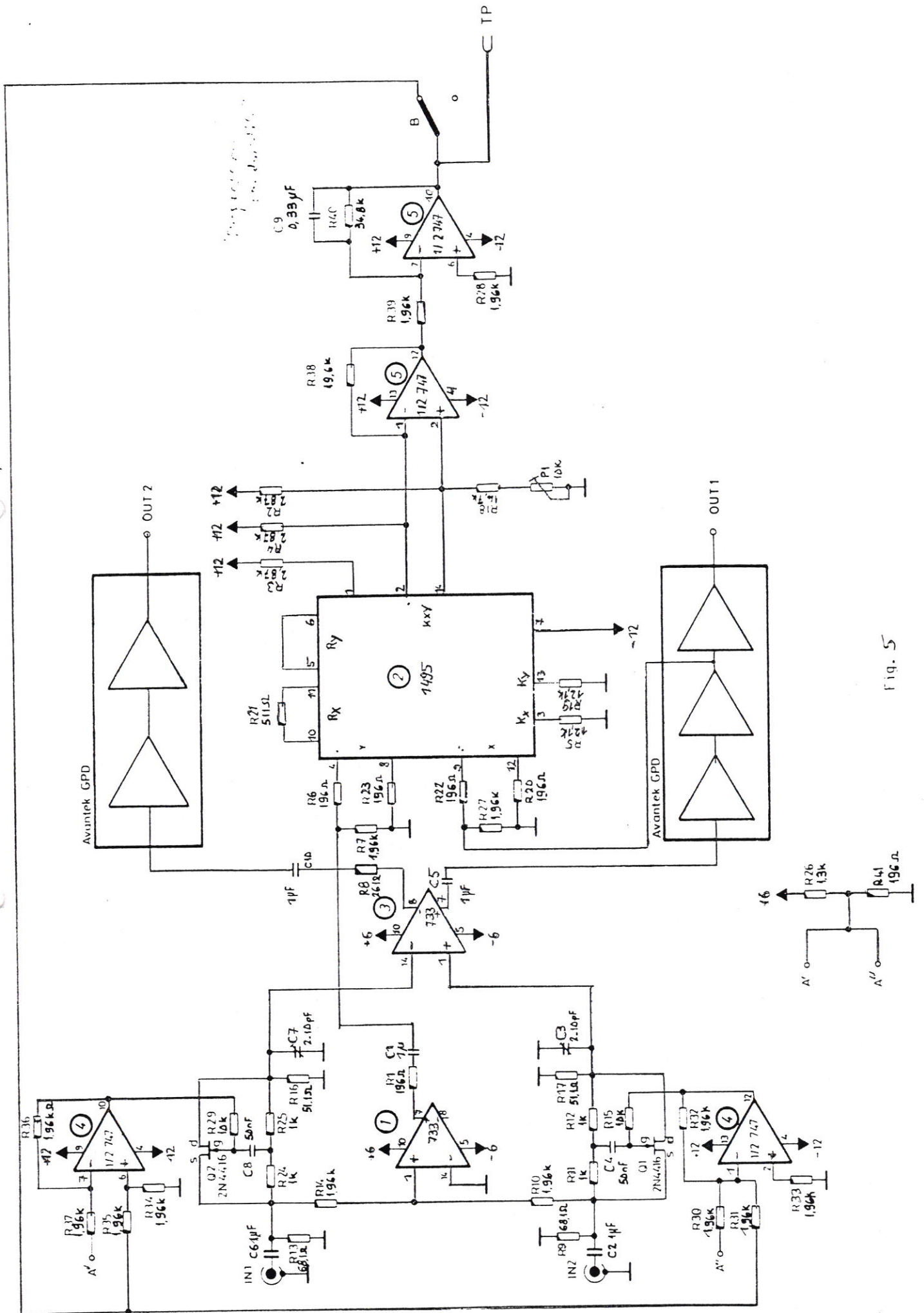
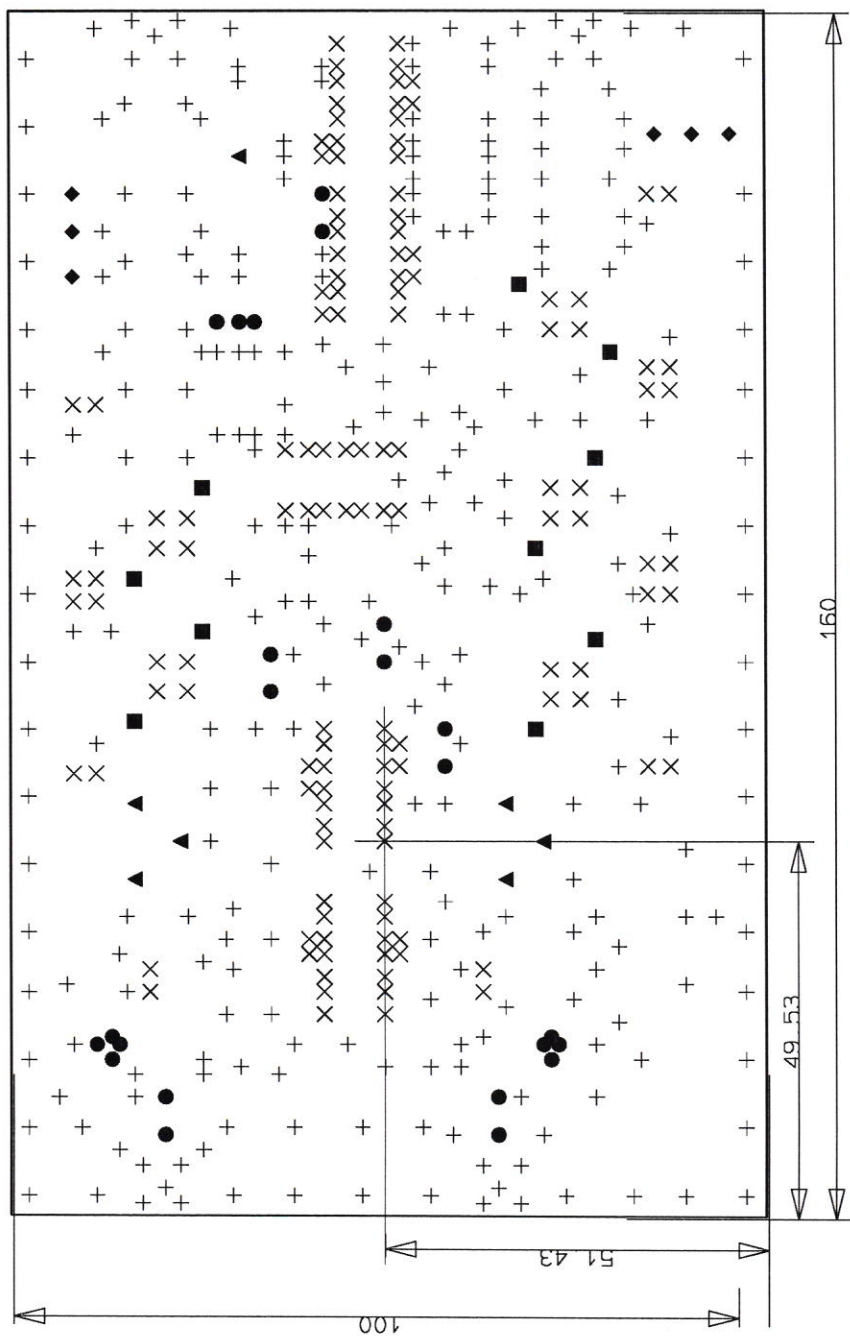


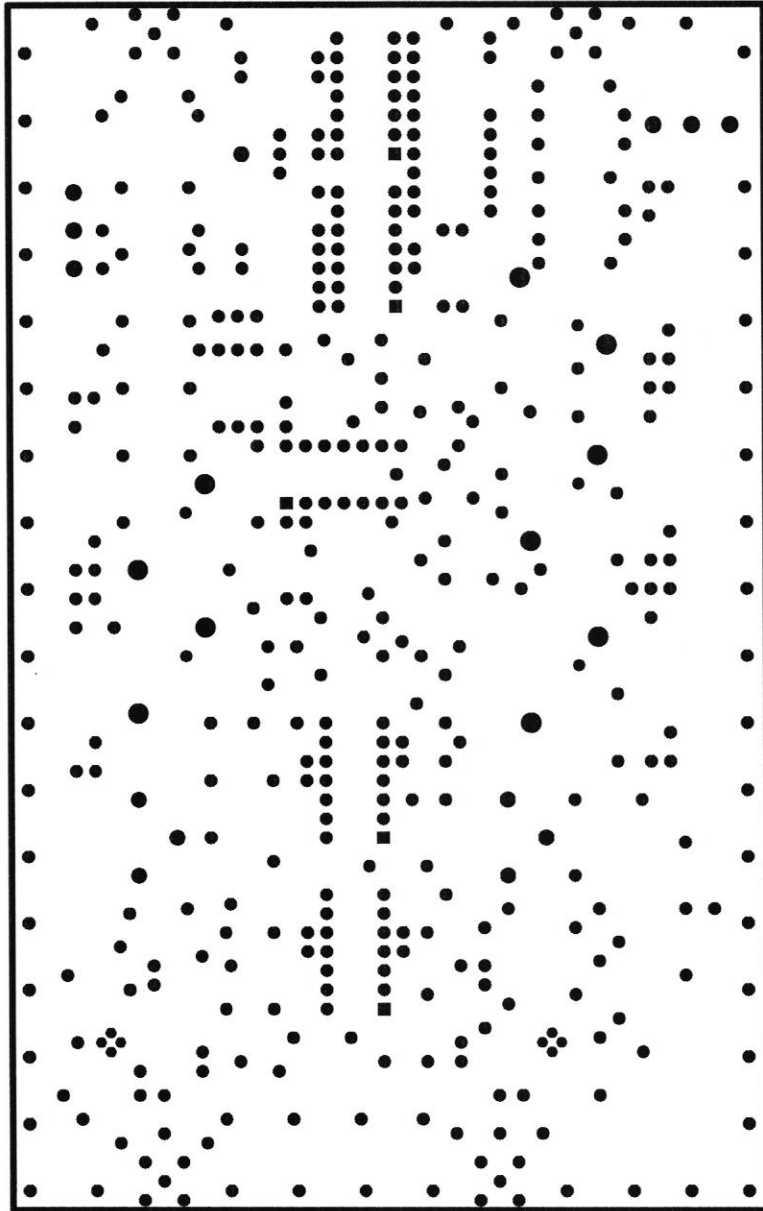
Fig. 5



■ /0010  
 ◆ /0006  
 ▲ /0007  
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 x /0130  
 ● /0023  
 ■ /0002

■ ◆ ▲ + x ● ■

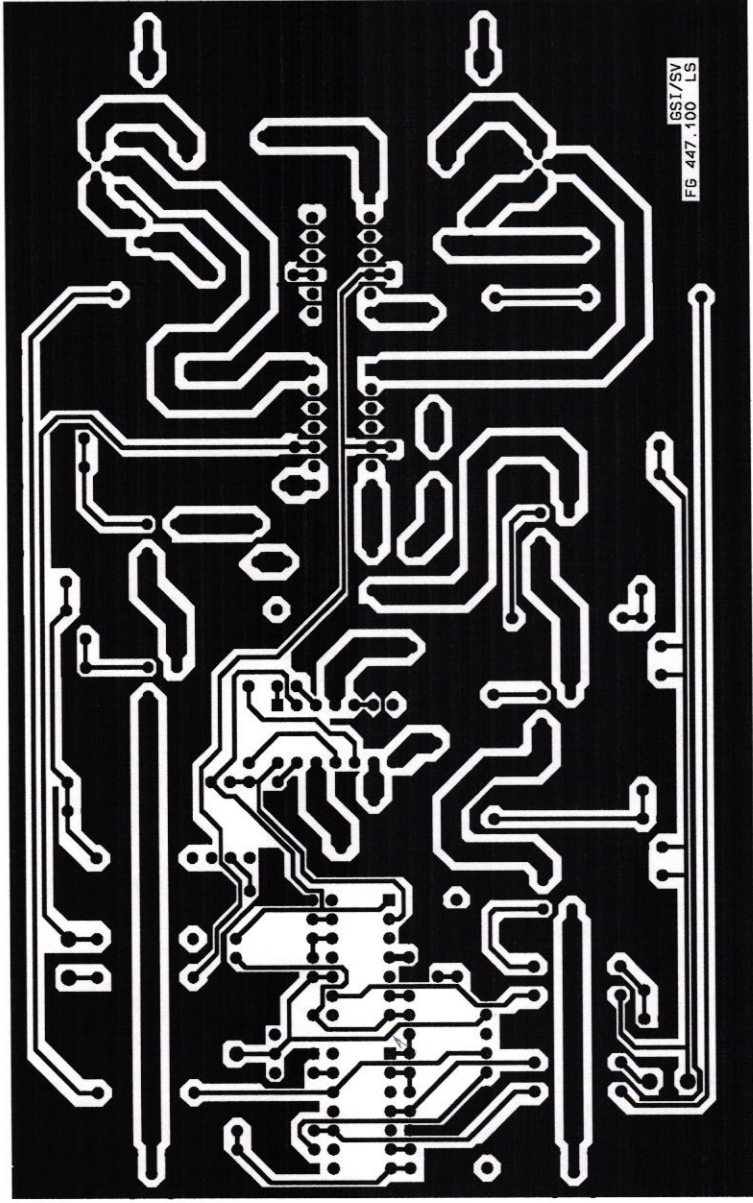
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FG 447.100

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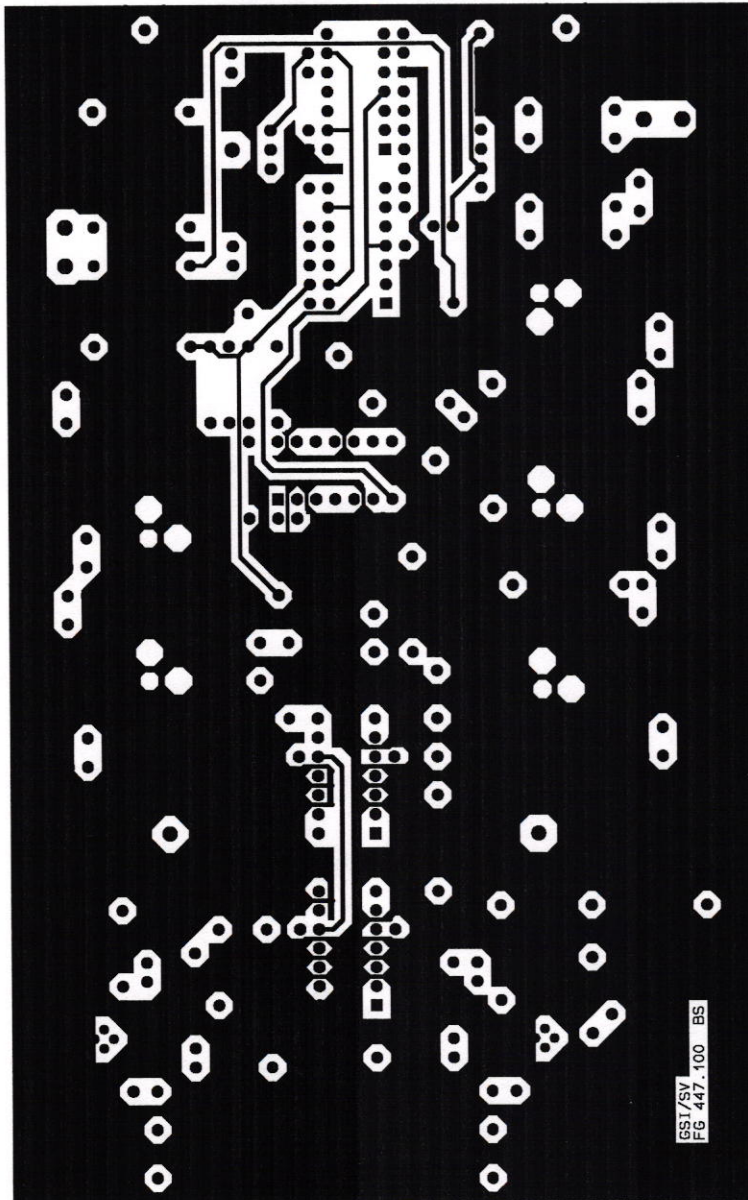
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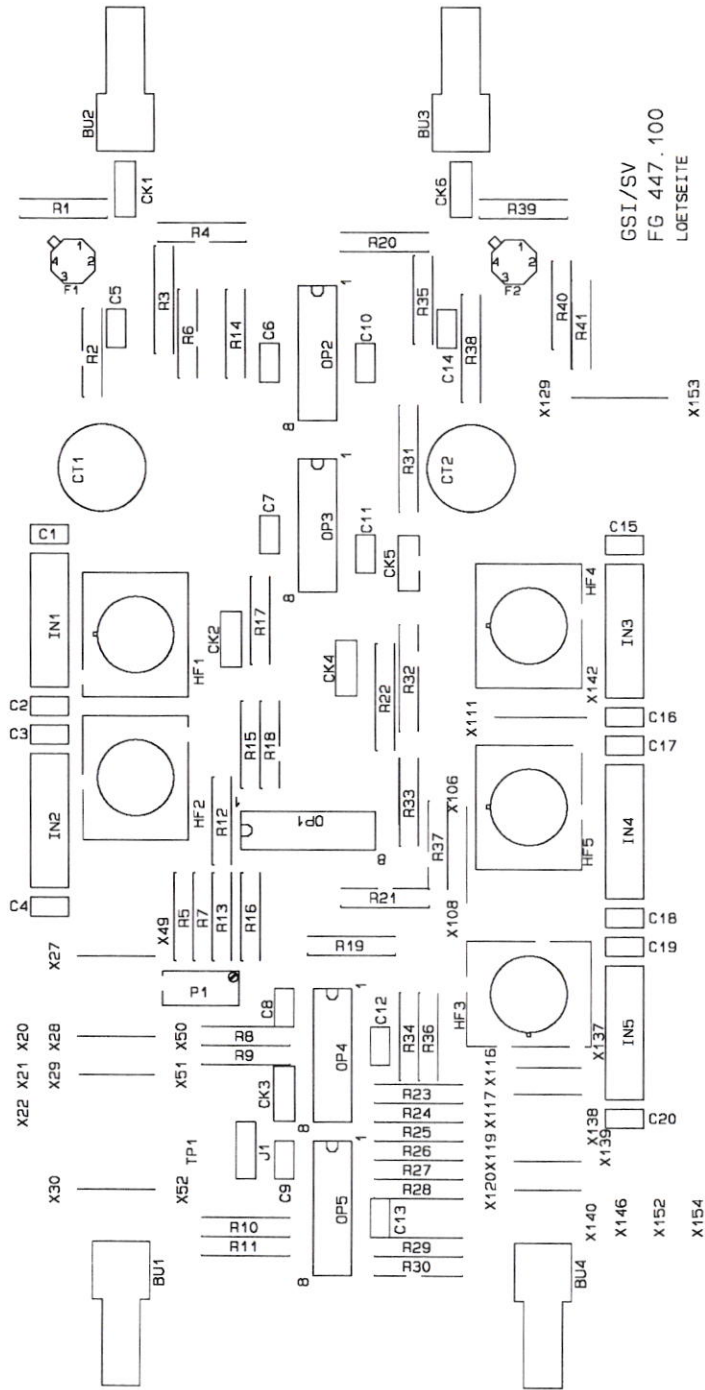
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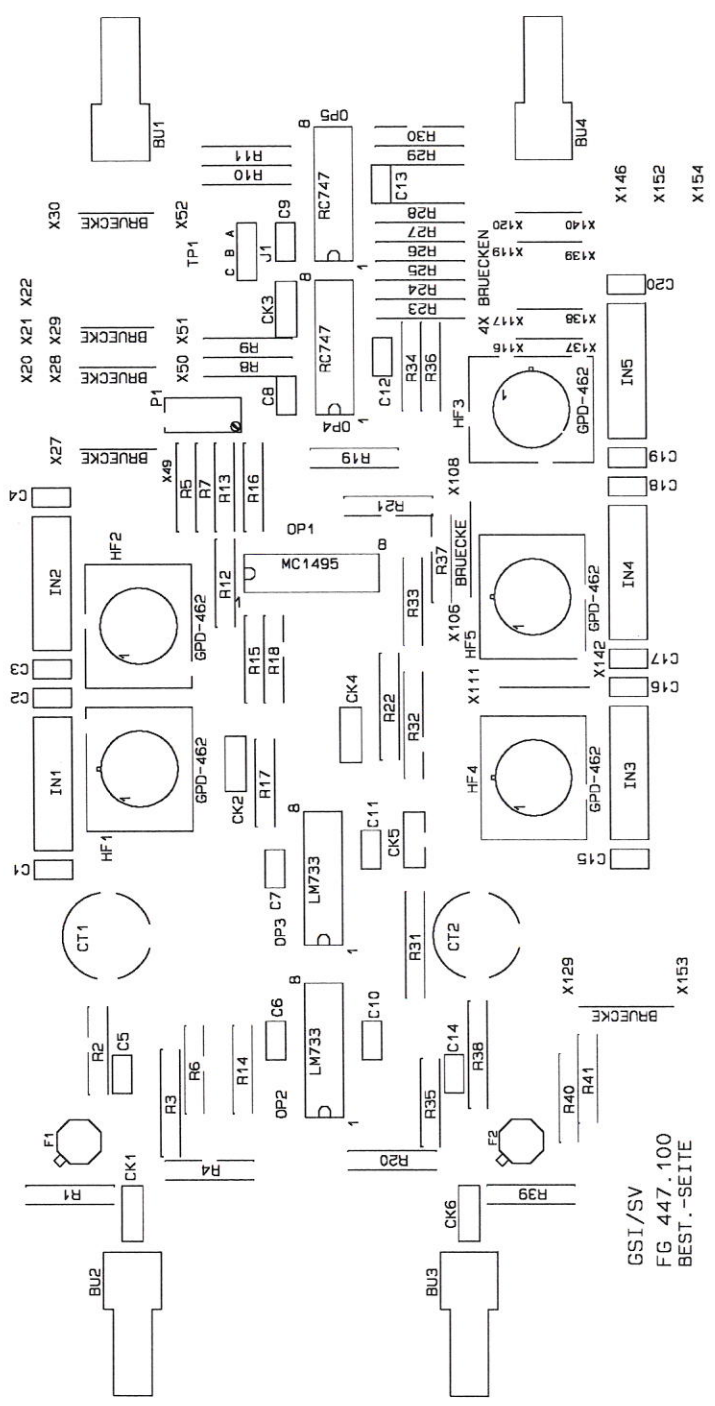
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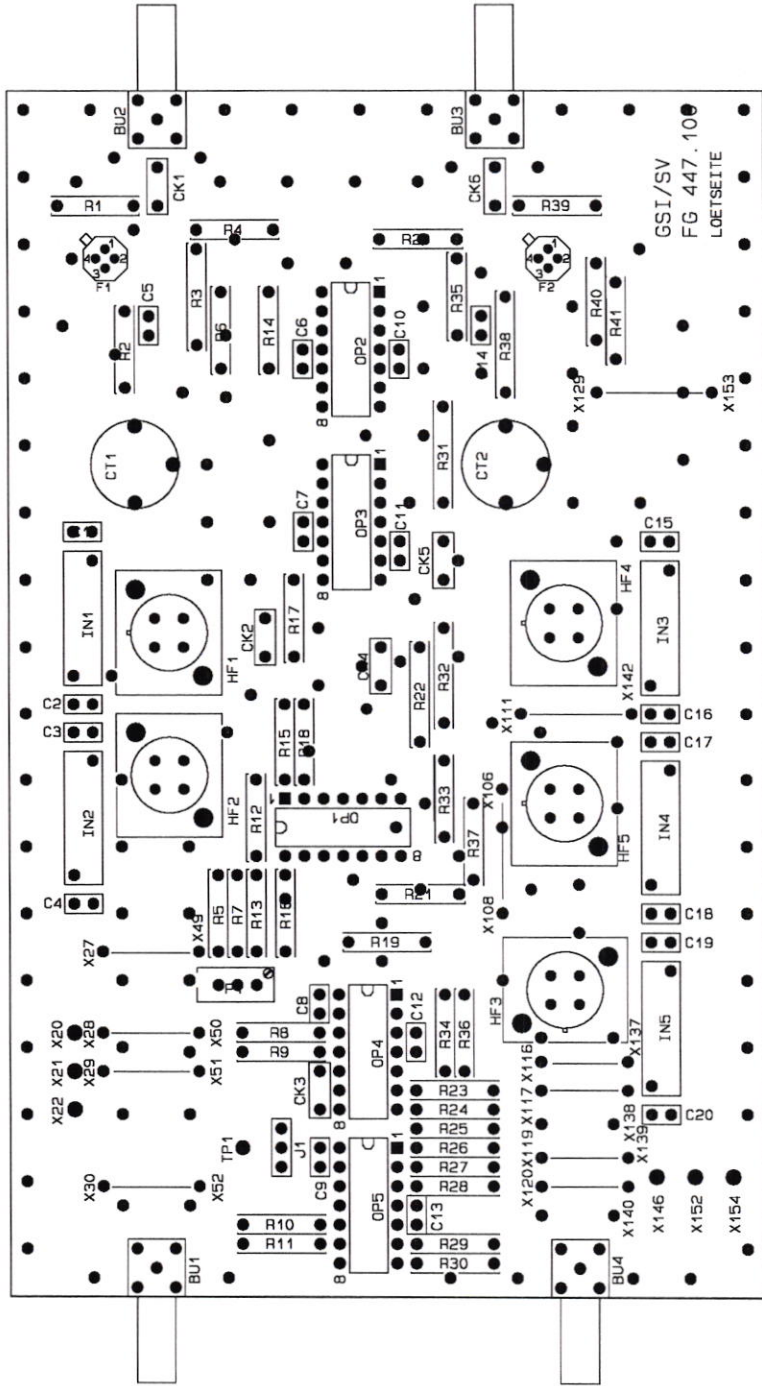




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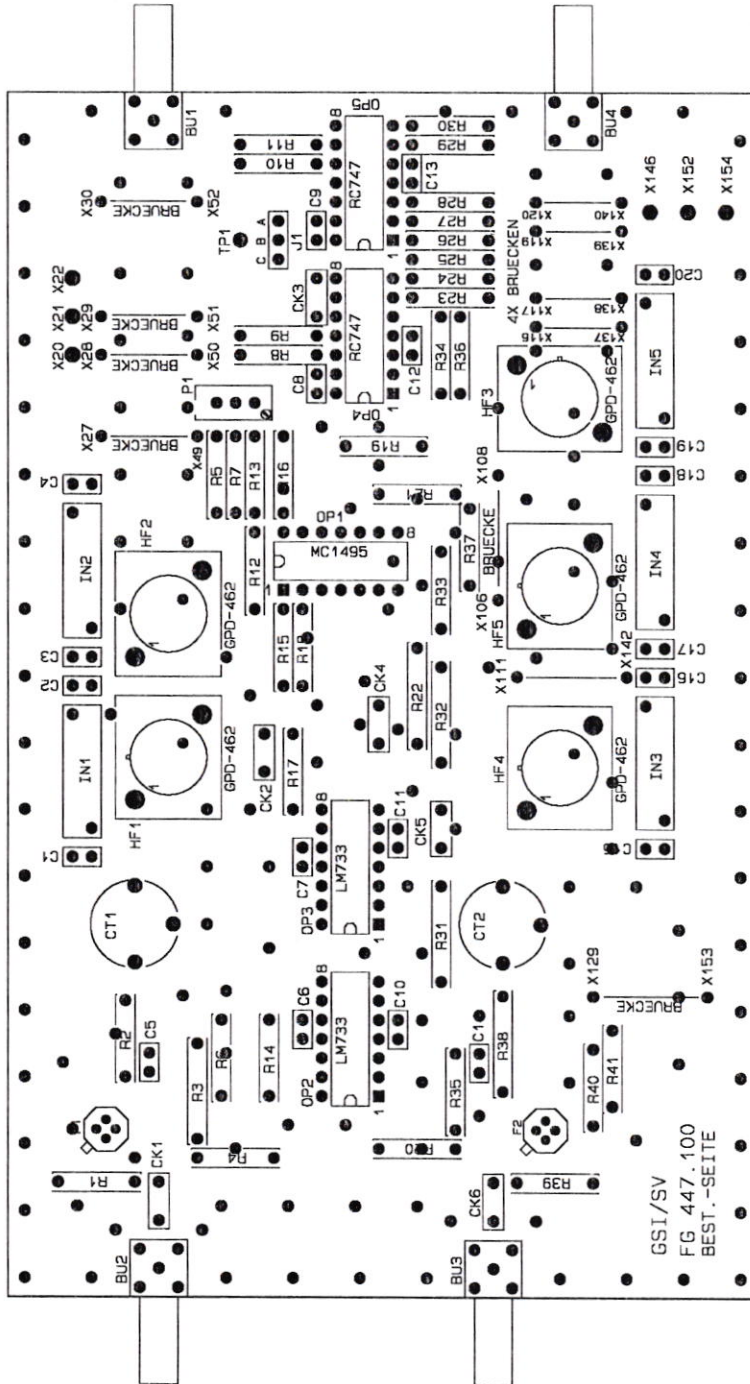
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2	F1, F2	2N4416		MOTOROLA	APL-DL	JFET HIGH-FREQUENCY AMPLIFIER, N-FET TO-72 GEH.	2N4416
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2	OP2, OP3	LM733		NATIONAL	APL-DL	LM733 DIFFERENTIAL AMPLIFIER	LM733
1	OP1	MC1495		MOTOROLA	APL-DL	MC1495 WIDEBAND LINEAR 4-QUADRANT MULTIPLIER	MC1495
2	OP4, OP5	RC747		SIGNETICS	13 144	RC747 GENERAL PURPOSE OPERATIONAL AMPLIFIER	RC747
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1	R2	10K	4		10 071	METALLSCHICHTWIDERSTAND, 0.6W/1% RASTER=4	10K*J4
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4	R19, R21, R33, R34	196R	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	196R*J4

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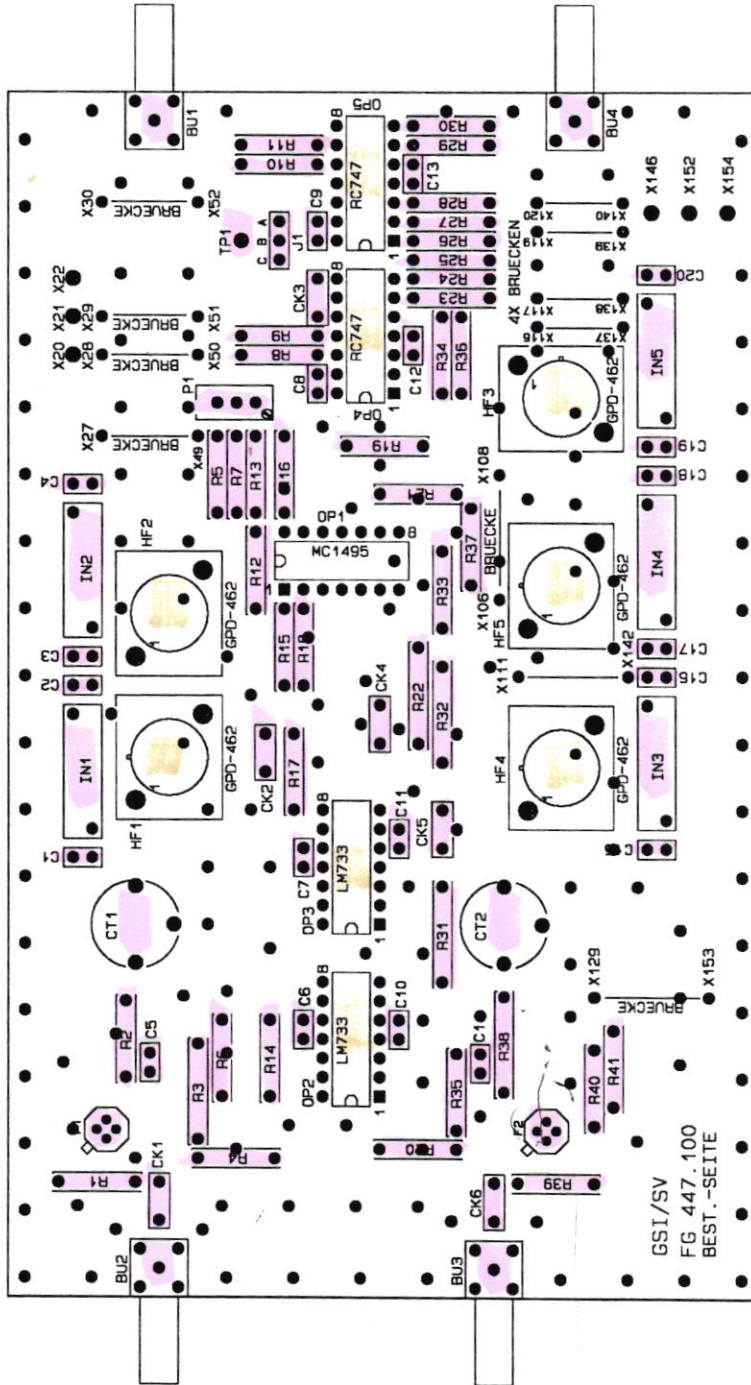
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1	R12	511R 510R	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	511R*J4
2	R14, R41	51R1 51K	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	51R1*J4
2	R1, R39	68R1 68K	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	68R1*J4
2	F1, F2	2N4416		MOTOROLA	APL-DL	JFET HIGH-FREQUENCY AMPLIFIER, N-FET TO-72 GEH.	2N4416
1	TP1	VERO12			16 082	VERO-FEDERPIN O/BOHR.1.2	VERO12
	<del>SILBERDRAHT 1mm UND ISOLIERMAT FÜR BRÜCKEN</del>						
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11	WIDERSTAND	0.2				(BEWEISEN)	
6	x 20, x 21, x 22, x 146, x 152, x 154	VERO 12			16082	(ÄNDERUNG)	
8	SMD KONDENSATOR	100NF					

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18	C1, C2, C3, C4, C6, C7	100N	1	UNION CARB	11 096	Union Carbide C320C104K1R5CA stehend Vielschicht-K	100N*A
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5	IN1, IN2, IN3, IN4, IN5	14UH	6		12 101	INDUKTIVITAET 14UH RM=15.24MM	14UH
2	OP2, OP3	LM733		NATIONAL	APL-DL	LM733 DIFFERENTIAL AMPLIFIER	LM733
1	OP1	MC1495	1595	MOTOROLA	APL-DL	MC1495 WIDEBAND LINEAR 4-QUADRANT MULTIPLIER	MC1495
2	OP4, OP5	RC747		SIGNETICS	13 144	RC747 GENERAL PURPOSE OPERATIONAL AMPLIFIER	RC747
1	P1	10K			10 807	POTI-BECKMAN 66WR- 10K/0.10W/5%	P10K*66
1	R2	10K	4		10 071	METALLSCHICHTWIDERSTAND, 0.6W/1% RASTER=4	10K*J4
1	R38	10K	5		10 071	METALLSCHICHTWIDERSTAND 0.6W/1% RASTER=5	10K*J5
2	R16, R18	12K1	12K	4 BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	12K1*J4
1	R7	14K7	15K	4 BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	14K7*J4
2	R10, R26	196K	180K	4 BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	196K*J4
4	R19, R21, R33, R34	196R	200R	4 BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	196R*J4

GS1 DARMSTADT	BEZ.:	NUMMER:	BEARB.:	DATUM	BLATT
		FG 447.100	LOOS	24.Oct.1995	1 VON 2





1. FA

2. Einkauf:

**Bedarfsmeldung (BM)**

Keine Bestellung für Lieferanten

№ 029708

Die stark umrandeten Felder werden vom FA bzw. EKF ausgefüllt.

Name des Anforderers: LOOS, D		Abt./Sekt. FPL	Tel. 2363	Anford.Kostenst. 62100	Bedarfszeitpunkt	Tag 13.10.95	
Pos.	Menge	Einh.	Genau Bezeichnung der zu beschaffenden Gegenstände: (gfls Verwendungszwecke   Betriebsverhältnisse   angeben)	Preise (soweit vorhanden)	Nr. (s.u.)	zu belastende Kostenstelle	FA Konto
1	25	Stück	Motorola JFET - 2N4416	2,10/Stück			
2	25	"	Multipliere MC1495	10,80/Stück	2	68370	

Der Bedarf kann aus vorhandenen Beständen — nicht — gedeckt werden.

Gesamtwert von Pos. 1 —

DM ca. 350,-

Abladestelle:	Lieferzeit:
Fracht:	Verp.:
Best. bei:	Z. b.:
Nr.:	am:

- 1 = Neuteil zur Erweiterung einer bestehenden Anlage
- 2 = Neuteil zum Einbau in eine im Bau befindliche Anlage
- 3 = Ersatzteil
- 4 = neues selbstständig nutzbares Gerät
- 5 = Verbrauchsmaterial und Betriebsstoffe
- 6 = Reparatur
- 7 = Austauschgerät
- 8 = Leiharbeitnehmer

Bearbeitungsvermerke EKF

Lieferantenvorschlag:

SPOERLE

Unterschrift des Anforderers

genehmigt

Sichtvermerk

Datum

13.10.95



**Bedarfsmeldung (BM)**  
Keine Bestellung für Lieferanten

No 029708

Die stark umrandeten Felder werden vom FA bzw. EKF ausgefüllt.

1. FA	
2. Einkauf	

Pos.	Menge	Einr.	Genauere Bezeichnung der zu beschaffenden Gegenstände: (dies Verwendungszweck / Betriebsverhältnisse angeben)	Anford. Kostentst. (sowohl vorhanden) / Preise	Nr. (s.u.)	zu belastende Kostenstelle	Bedarfszeitpunkt	Tag
1	100		Hauptgüter, HCN 192 L	2,10 / 2,10		FA Konto		12.10.97
2	100		Hauptgüter, HCN 192 L	2,10 / 2,10		FA Konto		12.10.97

Der Bedarf kann aus vorhandenen Beständen — nicht — gedeckt werden

- 1 = Neuzeit zur Erweiterung einer bestehenden Anlage
- 2 = Neuzeit zum Einbau in eine im Bau befindliche Anlage
- 3 = Ersatzteil
- 4 = neues selbstständig nutzbares Gerät
- 5 = Verbrauchsmaterial und Betriebsstoffe
- 6 = Reparatur
- 7 = Austauschgerät
- 8 = Leiharbeitsnehmer

Nr.	am	Best. per	Fracht	Abbestelle	Gesamtwert von Pos. 1 —
					DM ca 320,-

Bedarfsvermerk EKF		Lieferantenvorschlag	
Unterschrift des Anforders		SPORRE	
Sichtvermerk		Datum	
Genehmigt		12.10.97	



1. FA \_\_\_\_\_  
 2. Einkauf: \_\_\_\_\_

# Bedarfsmeldung (BM)

Keine Bestellung für Lieferanten

No 029708

Die stark umrandeten Felder werden vom FA bzw. EKF ausgefüllt.

Name des Anforderers: L005, D		Abt./Sekt. APL		Tel. 2363		Anford.Kostenst. 62100	Bedarfszeitpunkt	Tag 13.10.95
Pos.	Menge	Einh.	Genau Bezeichnung der zu beschaffenden Gegenstände: (gfls Verwendungszwecke   Betriebsverhältnisse   angeben)	Preise (soweit vorhanden)	Nr. (s.u.)	zu belastende Kostenstelle	FA Konto	
1	25	Stück	Motorola JFET - 2N4416	2,10/Stück		197 Tel 6	1,55	
2	25	"	Multipliere MC1495 L ↑ MC1595 L	10,80/Stück	2	983 sub 2 68370		
Pos. 1 EBV Auftrieb					251			
Pos 2 Spärle					215			

Der Bedarf kann aus vorhandenen Beständen — nicht — gedeckt werden.

Gesamtwert von Pos. 1 —

DM ca. 350,-

- 1 = Neuteil zur Erweiterung einer bestehenden Anlage
- 2 = Neuteil zum Einbau in eine im Bau befindliche Anlage
- 3 = Ersatzteil
- 4 = neues selbstständig nutzbares Gerät
- 5 = Verbrauchsmaterial und Betriebsstoffe
- 6 = Reparatur
- 7 = Austauschgerät
- 8 = Leiharbeitnehmer

Abladestelle:

Lieferzeit:

Fracht:

Verp.:

Best. bei:

Z. b.

Nr.:

am:

Bearbeitungsvermerke EKF

Lieferantenvorschlag:

SPOERLE

Unterschrift des Anforderers

genehmigt

Sichtvermerk

Datum

13.10.95

Haus-Telefonadresse: Pfaffenstraße 1 · D - 64581 Darmstadt

Menge	Einheit	Preis	Bezeichnung	Werkstoff	Werkstoff
12	Stk	1200	...	...	...
...	...	...	...	...	...

Bitte für den Besteller, oder den gemeinsamen Besteller zur Genehmigung abgeben

Bitte für den Besteller, oder den gemeinsamen Besteller zur Genehmigung abgeben

Bitte für den Besteller, oder den gemeinsamen Besteller zur Genehmigung abgeben

Bestellnummer: 123456  
 Datum: 12.12.81  
 Menge: 12 Stk  
 Preis: 1200,-



Darmstadt

Hdm grundschonenerwert für fast alle

...

Werkstoff	Werkstoff	Werkstoff
...	...	...

...

- 1 = ...
- 2 = ...
- 3 = ...
- 4 = ...
- 5 = ...
- 6 = ...
- 7 = ...
- 8 = ...
- 9 = ...
- 0 = ...



Gesellschaft für Schwerionenforschung mbH  
Darmstadt

Anforderer

Ges. für Schwerionenforschung mbH · Postfach 11 05 52 · D-64220 Darmstadt

STOERLE ELEKTRONIK  
POSTFACH 102 140  
63261 DREIEICH

Ihre Zeichen:

Ihre Nachricht:

Unsere Zeichen:  
Ekt.

Tag:

Planckstraße 1  
D-64291 Darmstadt  
Telefon (0 61 51) 3 59 - 0  
Druckwahl 359  
Telefax (0 61 51) 3 59 - 27 85  
Telex 04 19 593  
Stückgut-Nr. 1471

Bitte auf Sendungen, Versandpapieren undr Korrespondenz stets angeben!

interne Best.-Nr.

215.....Abruf zur Bestellung Nr. 1/08641/95

vom

08.08.95

Liefertermin SOFORT

Wir rufen aus unserer oben genannten Bestellung zur Lieferung ab:

Interne Vermerke

- 1 = Neuteil zur Erweiterung einer bestehenden Anlage
- 2 = Neuteil zum Einbau in eine im Bau befindliche Anlage
- 3 = Ersatzteil
- 4 = neues, selbständig nutzbares Gerät
- 5 = Verbrauchsmaterial und Betriebsstoffe
- 6 = Reparatur
- 9 = Austauschgerät

81100

anfordernde Kostenstelle

Menge	Einh.	Preise	Nr. (s.o.)	zu belastende Kostenstelle bzw. Kostensammel-Nr.	F A Konto
25	SH	MC 1495 L		68370	
		DILL			
		Preis u Lieferzeit gem. Abprache mit Frau Endler.			
		Gesamt 245.75			

Versand an

Rechnung von

# SPOERLE ELECTRONIC

Handelsgesellschaft mbH & Co.

Max-Planck-Straße 1-3

Postfach 10 21 40

D-63303 Dreieich

D-63287 Dreieich

Telefon Verwaltung (06103) 304 - 8

Telefax Verwaltung (06103) 30 42 01

US-KW: DE113524852

Deutsche Bank Offenbach

Vollbank Frankfurt

Postbank Frankfurt

Nr. 072394000

Nr. 1181019

Nr. 93520801

BLZ 505 700 18

BLZ 501 900 00

BLZ 500 100 80

Rechnung an

SPOERLE ELECTRONIC ; Postfach 10 21 40 ; D-63287 Dreieich

GSI Gesellschaft für  
Schwerionenforschung mbH

Planckstr. 1

D 64291 Darmstadt

Auftrag an

SPOERLE ELECTRONIC  
Bereich Frankfurt Süd

Im Gefierth 11A

D-63303 Dreieich

Telefon 06103/304-349

Telefax 06103/304360

Datum

16.10.95

Sachbearbeiter / Telefon

BETTINA ENDLICH 06103/304-349

VB

1430

Ihre Bestelldaten

08641/95

Bestelldatum

13.10.95

Ihre Lieferantenummer

Kommisitionsnummer

1 . 14 . 8 8 7 4 4 9

Auftrag an

SPOERLE ELECTRONIC

Bereich Frankfurt Süd

Im Gefierth 11A

D-63303 Dreieich

Telefon 06103/304-349

Telefax 06103/304360

SEITE 1

Bitte bei Rückfragen angeben

Belegnummer 1910591

Kundennummer 027004

LIEFERSCHEIN

Lieferungen und Leistungen erfolgen ausschließlich zu unseren umsatzigen Lieferungs- und Zahlungsbedingungen. Es gelten die jeweils gültigen Preislisten.

Item No.	Liefermenge	Anzahl	Bezeichnung	Ihre Artikel-Nummer	EG	Bestellmenge	Bestmenge
1	25	147120	MC 1595 L	DIC14	MOT	68370	0

DUPLIKAT

Versandt

Neu im Lieferprogramm: FPGA's von ACTEL



19105911

Haupt-Verteileradresse: Bismarckstraße 9, D-64561 Darmstadt

Menge	Einheit	Preis	Netto	Brutto	Steuernummer	Steuernummer	Steuernummer
28	Stk	121,311			01588		

Bitte den Namen, die Menge, die Einheit, die Preisangabe und die Steuerangabe angeben.

Bitte den Namen der Bestellung und die Bestellnummer angeben.

Bitte den Namen des Lieferanten und die Lieferadresse angeben.

Bestellnummer: 121311  
 Datum: 12.11.11  
 Name: Müller  
 Adresse: Bismarckstraße 9, D-64561 Darmstadt



Darmstadt

Hdm GmbH & Co. KG

Anteil

Steuernummer: 01588  
 Preis: 121,311  
 Menge: 28  
 Einheit: Stk

Steuernummer

Preis

Menge

Einheit

Steuernummer

Steuernummer

Steuernummer

Steuernummer

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Steuernummer



# Gesellschaft für Schwerionenforschung mbH Darmstadt

Anforderer

Ges. für Schwerionenforschung mbH · Postfach 11 05 52 · D-64220 Darmstadt

Planckstraße 1  
D- 64291 Darmstadt  
Telefon (0 61 51) 3 59 - 0  
Druchwahl 359  
Telefax (0 61 51) 3 59 - 27 85  
Telex 04 19 593  
Stückgut-Nr. 1471

EDV-ELEKTRONIK  
BURGSTR. 81-83  
65817 ERNSTEIN/TS

Ihre Zeichen:

Unsere Zeichen:  
Ekt.

Tag:

Bitte auf Sendungen, Versandpapieren undr Korrespondenz stets angeben!

interne Best.-Nr.  
25A...Abruf zur Bestellung Nr. 1/71484/92

vom 22.10.92

Liefertermin

SOFORT

Wir rufen aus unserer oben genannten Bestellung zur Lieferung ab:

**Interne Vermerke**

- 1 = Neuteil zur Erweiterung einer bestehenden Anlage
- 2 = Neuteil zum Einbau in eine im Bau befindliche Anlage
- 3 = Ersatzteil
- 4 = neues, selbständig nutzbares Gerät
- 5 = Verbrauchsmaterial und Betriebsstoffe
- 6 = Reparatur
- 9 = Austauschgerät

anfordernde Kostenstelle  
81100

Menge	Einh.	Preise	Nr. (s.o.)	F A Konto
25	ST	JTET - 2N 4416 MOTOROLA	68370	10500
		Gesamt 3875		

Preis gem. Abgprache  
mit H. Schubert





# EBV ELEKTRONIK

AUTORISIERTER VERTRAGSHÄNDLER FÜR HALBLEITER UND MICROSYSTEME

Centre Européen

Hans-Pinsel-Straße 4 · D-85540 Haar/München

Telefon (089) 45610-0 · Telefax (089) 4644 88

Code 1102277 / +UA

HAAR, den 16/10/95

Bei Zahlung bitte angeben  
Rechnung Nr. 261181/129300

LIEFERSCHEIN Nr. 261181

Ihr Auftrag TEL.FR.L00S vom 13/10/95

Wir liefern heute FOB zu unseren umseitigen Liefer und Zahlungsbedingungen, auf Ihre Rechnung und Gefahr.

Pos	Bezeichnung	Herstell./ECCN	Menge
	Zolltarifnr.	AL.	

1	2N4416		25
		TMI21 /3A96G	
	8541 2190 0		
		25 DATECODE 9430	
	KOSTENLOSE MUSTER		

Lieferanschrift

GESELLSCHAFT F.  
SCHWERIONENFORSCHUNG \*  
PLANCKSTR. 1  
64291 DARMSTADT

EBV ELEKTRONIK GmbH · Postfach 1109 · 85529 Haar

GESELLSCHAFT FUER P  
SCHWERIONENFORSCHUNG MBH  
PLANCKSTR. 1  
64291 DARMSTADT

**DUPLIKAT**

Zahlungsbedingungen  
10 TAGE 2% SKONTO, 30 TAGE NETTO.  
\* 02/ 0 161046

EBV ELEKTRONIK Vertriebs GmbH · Amtsgericht München HRB 42104  
Allein-Inhaber: Erich Fischer · Geschäftsführer: Erich Fischer, Peter Gürtler

UST-IDENT-Nr.: DE 129334372

25



Dresdner Bank München Kto.-Nr. 5 018 961  
(BLZ 700 800 00)  
Postbank München Kto.-Nr. 686 28-800  
(BLZ 700 100 80)





Bestellung bei Spoerle für Kost. Stelle 68370  
oder E3V

25 x JC-MC1595L

15,56 DM/Stck

25 x MA 733 CN DIP

3,23 DM/Stck



Bestellung Fa. Bürklin für Kostenstelle: 68370

Metallschichtwiderstände 0,6W Typ. 0207

Stück	Wert	Best.Nr.			
100x	51R1	30E168	0,08 DM/Stück	12,-	8,-
100x	68R1	30E180	0,08 "		8,-
100x	196R	30E224	0,08 "		8,-
100x	261R	30E236	0,08 "		8,-
100x	511R	30E264	0,08 "		8,-
100x	1K0	30E292	0,08 "		8,-
100x	1K30	30E303	0,08 "		8,-
200x	1K96	30E320	0,06 "	12,-	
100x	2K74	30E334	0,08 "		8,-
100x	2K87	30E336	0,08 "		8,-
100x	4K75	30E358	0,08 "		8,-
100x	10K0	30E389	0,08 "		8,-
100x	12K1	30E397	0,08 "		8,-
100x	14K7	30E405	0,08 "		8,-
100x	34K8	30E441	0,08 "		8,-

SMD-Keramik-Vielstichtkondensatoren Typ Siemens 658337

100x	1µF	Best.Nr. 53D118	2,73 "	273,-
25x	0,33µF	" " 53D109	1,09 "	
25x	0,047µF	53D100	1,04 "	

Scheibentrimmer

25x	18-15pF	Best.Nr. 70D270	1,41 "
25x	2-6pF	" " 70D162	1,35 "

JFET-Transistoren

25x	2N4416	Best.Nr. 27S7700	1,96 "
-----	--------	------------------	--------



BFI IBEXSA Elektronik GmbH  
Microwave Division  
**TELEFAX NACHRICHT**



**AN:** Herrn Loos / GSI Darmstadt  
**VON:** Sabine Peick / BFI IBEXSA Elektronik GmbH  
**UNSERE TEL. NO.:** (089) 319767-17  
**UNSERE FAX NO.:** (089) 3193510  
**BETR.:** ANGEBOT NR. 960206  
**BEZ.:** Ihre Anfrage von heute  
**DATUM:** 2. Februar 1996

Sehr geehrter Herr Loos,

wir bedanken uns für Ihre Anfrage und unterbreiten Ihnen hierzu folgendes Angebot:

Pos.	Beschreibung	Menge	Preis DM/Stück
01	Modell GPD-464	50 St.	31,86

**Zahlung:** 30 Tage netto, Skonto wird nicht gewährt.

**Preisstellung:** Die angegebenen Preise sind netto, verzollt, frei Verladestelle Dietzenbach und zuzügl. der gesetzl. Mehrwertsteuer gestellt.

**Lieferzeit:** ca. 16-20 Wochen nach Auftragseingang

**Gültigkeit:** Die Bindefrist für dieses Angebot beträgt 30 Tage.

Es gelten ausschließlich die o.g. Angebots-, Liefer- und Zahlungsbedingungen sowie unsere "Allgemeinen Geschäftsbedingungen für Lieferungen und Leistungen (AGL)".

Es würde uns freuen, wenn Ihnen unser Angebot zusagen sollte und verbleiben

mit freundlichen Grüßen  
BFI IBEXSA Elektronik GmbH

Sabine Peick  
Vertriebsassistentin



1. FA \_\_\_\_\_  
 2. Einkauf: \_\_\_\_\_

# Bedarfsmeldung (BM)

Keine Bestellung für Lieferanten

No 029713

Die stark umrandeten Felder werden vom FA bzw. EKF ausgefüllt.

Name des Anforderers: <b>K. KOHL</b>		Abt./Sekt. <b>APL</b>	Tel. <b>2388</b>	Anford.Kostenst. <b>62100</b>	Bedarfszeitpunkt	Tag	
Pos.	Menge	Einh.	Genau Bezeichnung der zu beschaffenden Gegenstände: (gfls Verwendungszwecke [ Betriebsverhältnisse ] angeben)	Preise (soweit vorhanden)	Nr. (s.u.)	zu belastende Kostenstelle	FA Konto
1	50	Stck	<b>AVANTEK Amplifier Modul GPD-464</b>	<b>31,86/Stck</b>	<b>2</b>	<b>68370</b>	
			<b>Preis lt. Angebot Nr. 960206 vom 02.02.96</b>				

Der Bedarf kann aus vorhandenen Beständen — nicht — gedeckt werden.

Gesamtwert von Pos. 1 —

DM ca.

Abladestelle:	Lieferzeit:
Fracht:	Verp.:
Best. bei:	Z. b.:
Nr.:	am:

- 1 = Neuteil zur Erweiterung einer bestehenden Anlage
- 2 = Neuteil zum Einbau in eine im Bau befindliche Anlage
- 3 = Ersatzteil
- 4 = neues selbstständig nutzbares Gerät
- 5 = Verbrauchsmaterial und Betriebsstoffe
- 6 = Reparatur
- 7 = Austauschgerät
- 8 = Leiharbeitnehmer

Bearbeitungsvermerke EKF

Lieferantenvorschlag:

**BFI IBEXSA**  
**Eching**

Unterschrift des Anforderers

Sichtvermerk

genehmigt

Datum





# Special-Teile für COS-Suppressor / 1 Stück

Teil	Bemerkung	vorhanden	bestellen
2x 2N4416	JFET High-Freq. AMP	-	2 ✓
2x LM733	Differential AMP	2	-
2x RC747	OP Lg Nr 13144	2	-
1x MC1495	Linear 4 Quadr. Multiplier	-	2 ✓
5 Advantek GPD-462 ?	Verstärker	-	-

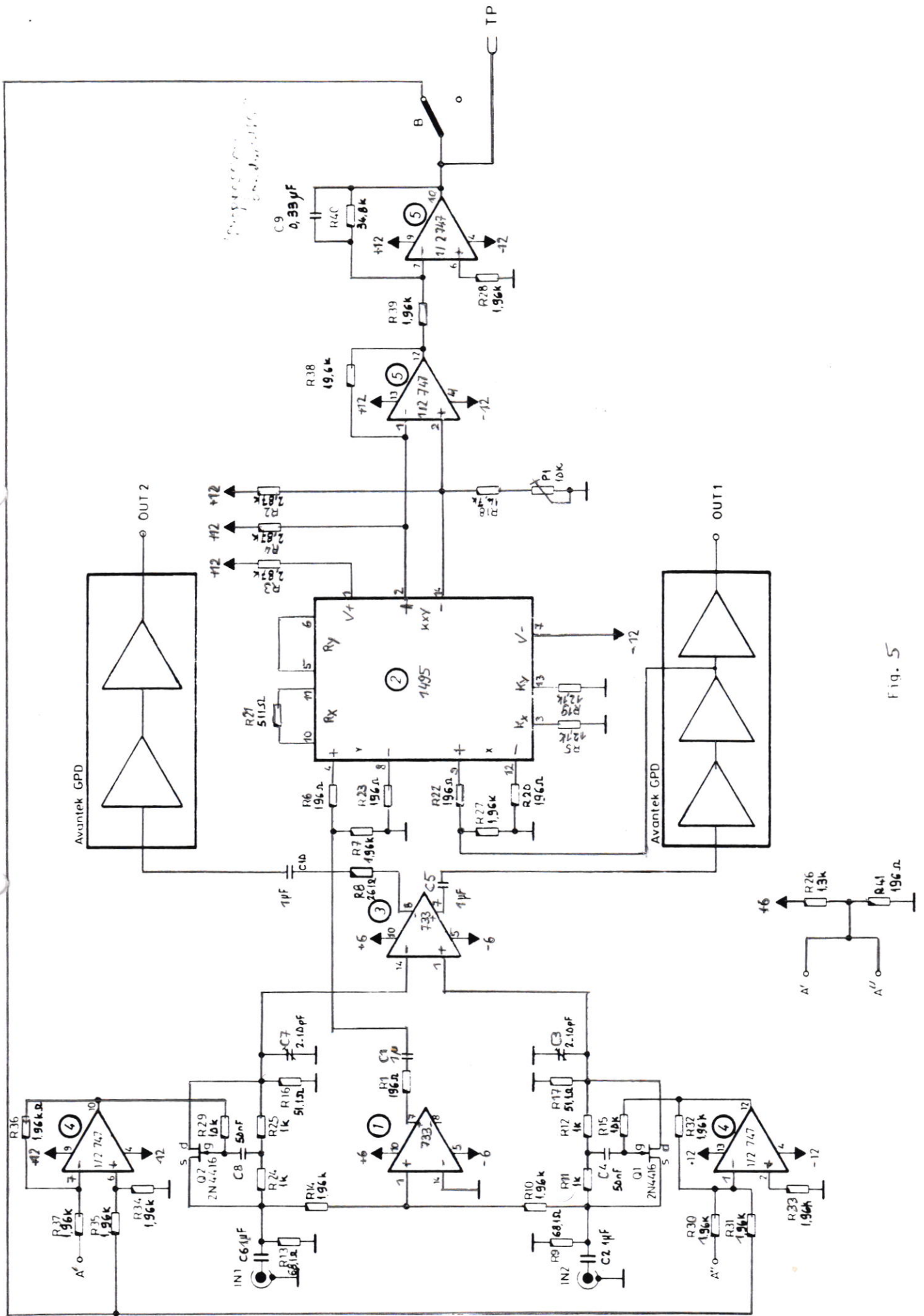


Fig. 5

[ Sabine . FG447.100 ] 81845.865

E L E K T R . S T U E C K L I S T E

ST.	BAUTEIL-BEZEICHNUNG	WERT/TYP	RM	HERSTELLER	LG-NR.	BEMERKUNGEN	*RM: 1 EINHEIT = 2.54MM	AUFRUF-NAHME
2	R22, R31	196R 200R	5		100 10-071	METALLSCHICHTWIDERSTAND 0.6W/1% RASTER=5		196R*J5
1	R8	19K6 18K	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4		19K6*J4
3	R6, R35, R40	1K0	4		10 047	METALLSCHICHTWIDERSTAND, 0.6W/1% RASTER=4		1K0*J4
1	R3	1K0	5		10 071	METALLSCHICHTWIDERSTAND 0.6W/1% RASTER=5		1K0*J5
1	R36	1K3	4		10 050	METALLSCHICHTWIDERSTAND, 0.6W/1% RASTER=4		1K3*J4
11	R4, R9, R11, R20, R23, R25	1K96 2K	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4		1K96*J4
	R27, R28, R29, R30, R37							
1	R32	1K96 2K	5		100 10-071	METALLSCHICHTWIDERSTAND 0.6W/1% RASTER=5		1K96*J5
1	R17	261R 270R	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4		261R*J4
3	R5, R13, R15	2K87 3K	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4		2K87*J4
1	R24	34K8 33K	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4		34K8*J4
1	R12	511R 510R	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4		511R*J4
2	R14, R41	51R1 51K	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4		51R1*J4
2	R1, R39	68R1 68R	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4		68R1*J4
2	F1, F2	2N4416		MOTOROLA	APL-DL	JFET HIGH-FREQUENCY AMPLIFIER, N-FET TO-72 GEH.		2N4416
1	TP1	VERO12			16 082	VERO-FEDERPIN O/BOHR.1.2		VERO12
	SILBERDRAHT IMM UND ISOLIERMAT. FÜR BRÜCKEN.							
1	LEITERPLATE	FG 447.100						
11	WIDERSTAND	0.2						
6	X 20, X 21, X 22, X 146, X 152, X 154	VERO 12			16082			
8	SMD KONDENSATOR	100NF						

(BEZEICHNEN)  
(ÄNDERUNG)

BEZ.:	NUMMER:	BEARB.:	DATUM
GSI DARMSTADT	FG 447.100	LOOS	24.Oct.1995
SISU			BLATT 2 VON 2

E L E K T R . S T U E C K L I S T E

\*RM: 1 EINHEIT = 2.54MM

ST.	BAUTEIL-BEZEICHNUNG	WERT/TYP	RM	HERSTELLER	LG-NR.	BEMERKUNGEN	AUFRUF-NAHME
5		DIP14*DI			13 322	CAB/DALEKTRON 110-91-314 DUAL-IN-LINE ZUM LOETEN	
4	BU1, BU2, BU3, BU4	BU594-211		ERNI	APL-DL	KOAXIALKONTAKT ABGEW., 5 LOETANSCHL, NR.: 594 211	BU594*DI
18	C1, C2, C3, C4, C6, C7	100N	1	UNION CARB	11 096	Union Carbide C320C104K1R5CA stehend Vielschicht-K	100N*A
	C8, C9, C10, C11, C12, C13						
	C15, C16, C17, C18, C19, C20			UNION CARB	11 313	UNION CARBIDE C330C105K5R5CA STEHEND VIELSCHICHT-K	1U0*A
5	CK1, CK2, CK4, CK5, CK6	1U0		UNION CARB	11 334	UNION CARBIDE C330C334K5R5CA STEHEND VIELSCHICHT-K	330N*A
1	CK3	330N		UNION CARB	11 309	Union Carbide C320C473K1R5CA stehend Vielschicht-K	47N*A
2	C5, C14	47N	1	UNION CARB	APL	KER. SCHEIBENTRIMMER NR.: 70D-163, TYP 10S-TRIKO 06	CT3P-12P*BI
2	CT1, CT2	CT3P-12P 4-20P		BUERKLIN	APL-DL	ACCESSORY-PACK, NR.: 330-006756-001	
5	MONTAGEMAT			AVANTEK	APL-DL	HF-VERSTAERKER	GPD-462
5	HF1, HF2, HF3, HF4, HF5	GPD-462		AVANTEK	APL-DL	KURZSCHLUSSBUCHSE ISOLIERT MIT OEFFNUNG F. PRUEFCL	
1	KURZSCHL.-BU			COMATEL	16 009	COMATEL 385.0358.120.400 WW-LEISTE ZUSCHN. 1X3PINS	JULX3*B2
	J1	WW-L. ZUSCHN	1		16 080	INDUKTIVITAET 14UH RM=15.24MM	14UH
5	IN1, IN2, IN3, IN4, IN5	14UH	6	NATIONAL	12 101	LM733 DIFFERENTIAL AMPLIFIER	LM733
2	OP2, OP3	LM733		MOTOROLA	APL-DL	MC1495 WIDEBAND LINEAR 4-QUADRANT MULTIPLIER	MC1495
1	OP1	MC1495 1595		SIGNETICS	13 144	RC747 GENERAL PURPOSE OPERATIONAL AMPLIFIER	RC747
2	OP4, OP5	RC747			10 807	POTI-BECKMAN 66WR- 10K/0.10W/5%	P10K*66
1	P1	10K	4		10 071	METALLSCHICHTWIDERSTAND, 0.6W/1% RASTER=4	10K*J4
1	R2	10K	5		10 071	METALLSCHICHTWIDERSTAND 0.6W/1% RASTER=5	10K*J5
1	R38	10K	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	12K1*J4
2	R16, R18	12K1 12K	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	14K7*J4
1	R7	14K7 15K	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	196K*J4
2	R10, R26	196K 180K	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	196R*J4
4	R19, R21, R33, R34	196R 200R	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	

BLATT 1 VON 2

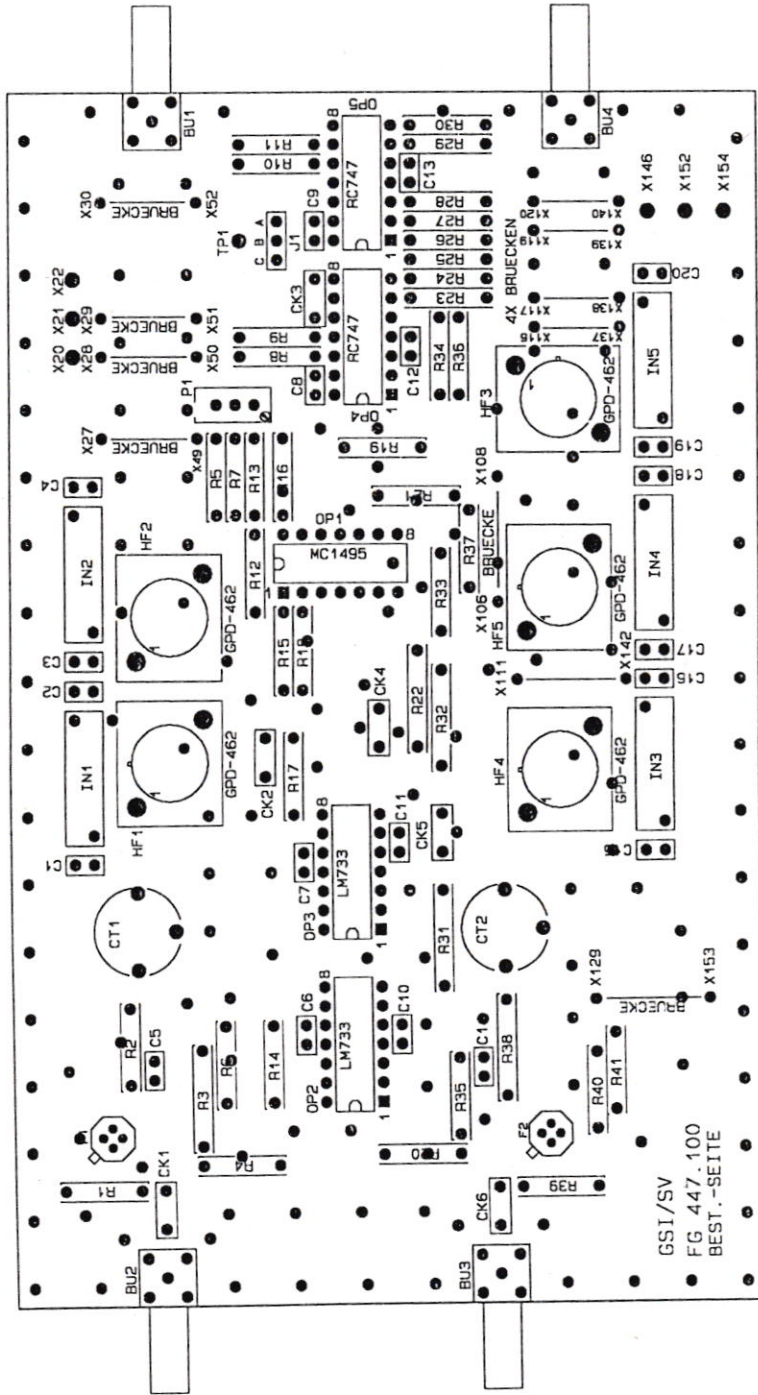
DATUM 24.Oct.1995

BEARB.: LOOS

NUMMER: FG 447.100

BEZ.:

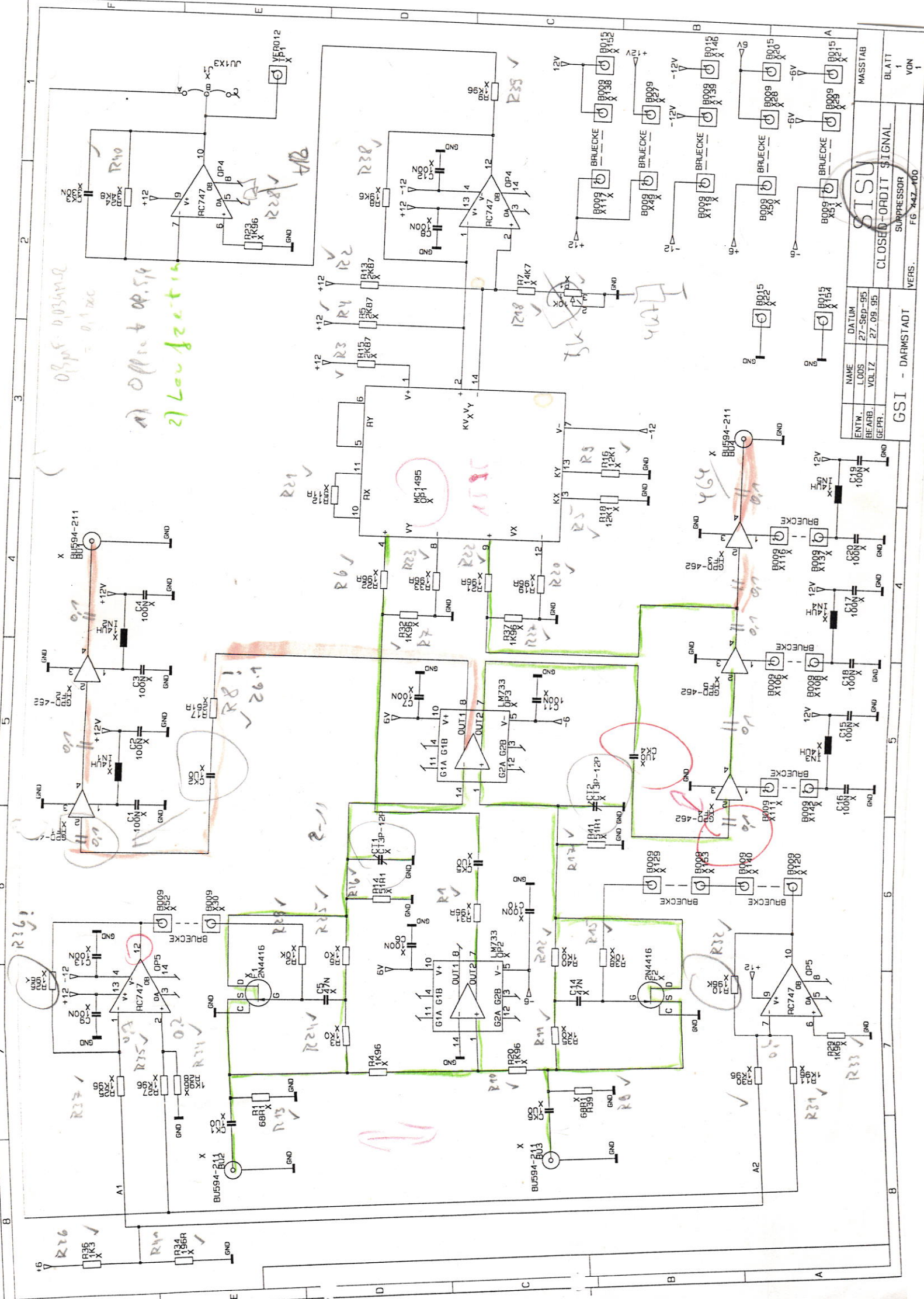
GSI DARMSTADT SISU



SISU FG 447.100

SISU

GSI SISUKON1  
25-OCT-1995 L005



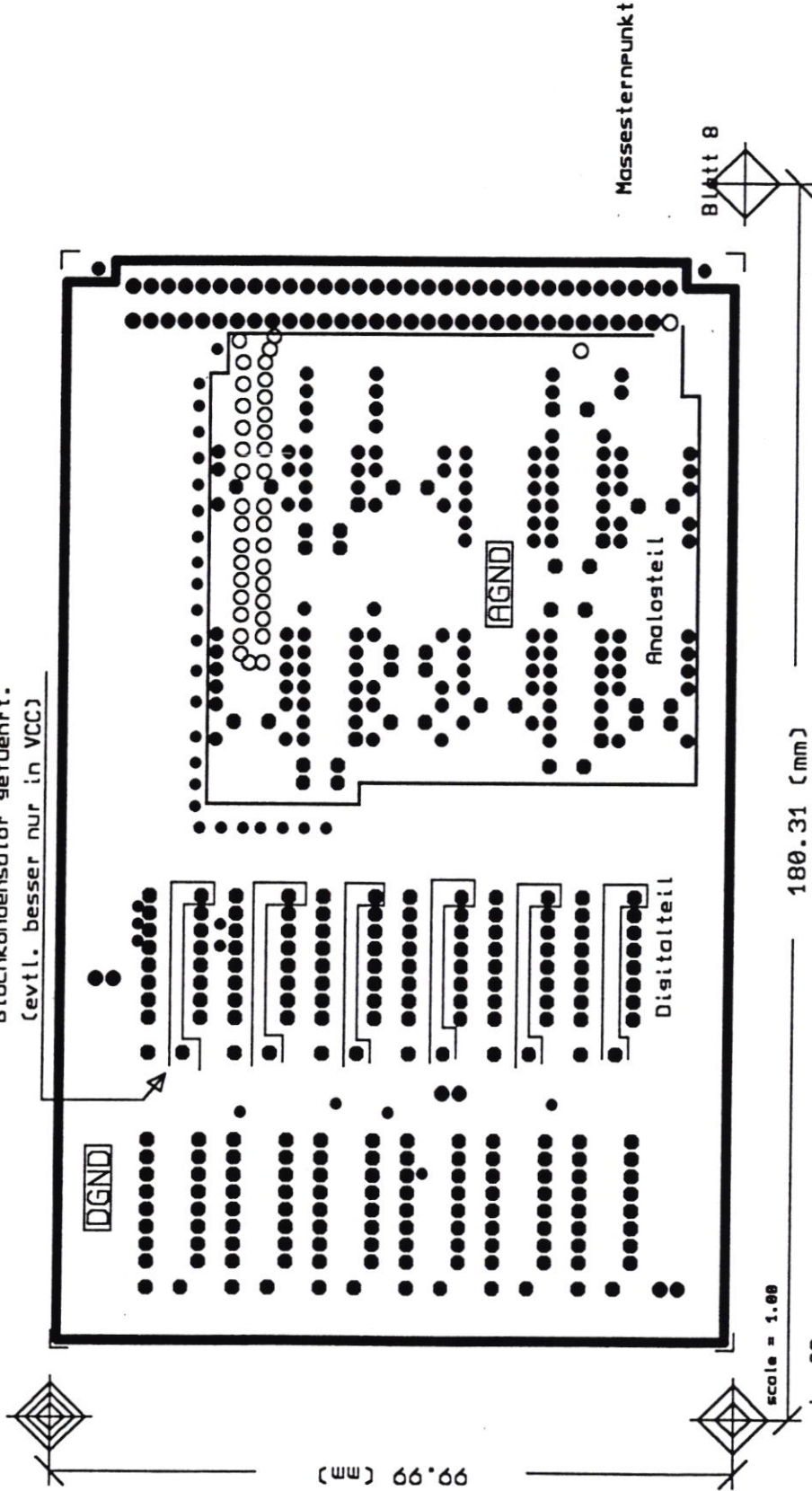
0,3µF - 0,03µF  
 = 0,1µF  
 1) Offset 0p.5µ  
 2) Lowfrequent

NAME		DATUM	
		L005	27.09.95
ENTM.		VOLIZ	
BEARB.		27.09.95	
GEPRI.			
GSI - DARMSTADT		VERS.	
MASSTAB		BLATT	
CLOSED-ORDIT SIGNAL		VON	
SUPPRESSOR		1	
FG 747-100			

GSI



Abschottung der Stromzufuehrung.  
 Hierdurch wird die Versorgung erst ueber den  
 Blockkondensator gefuehrt.  
 (evtl. besser nur in VCC)



99.99 (mm)

scale = 1.00

180.31 (mm)

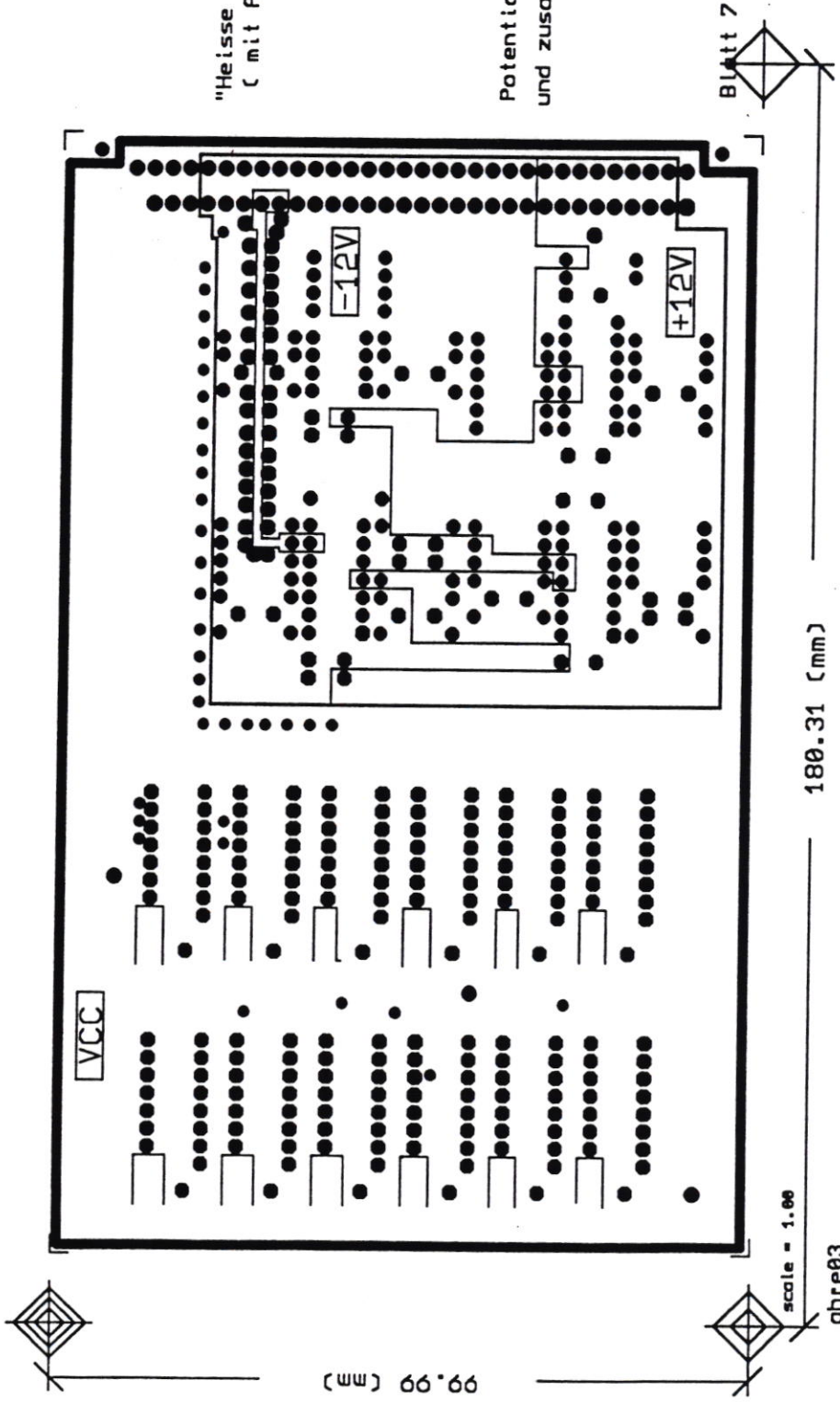
ahre03

5071

01-Jun-94

SEG=2

RES=2



"Heisse Leitung" zuzusieht  
( mit Abdeckums )

Potentialgruppen bilden  
und zusammen lassen.

Blatt 7

scale = 1.00

dhre03

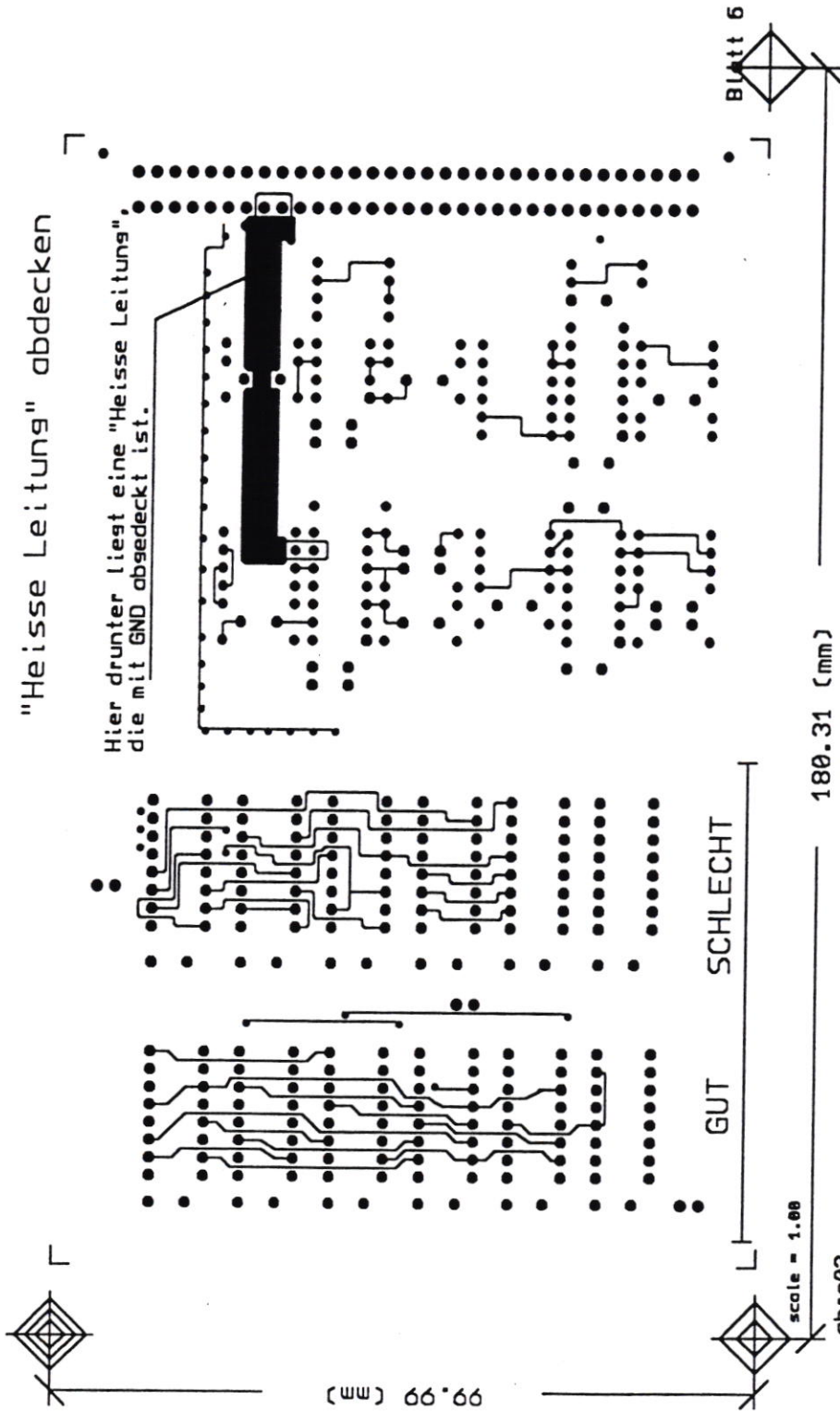
01-Jun-94

SEG=3

RES=3

180.31 (mm)

99.99 (mm)



Strecken Leitungen ueber lange Parallel gefuehrte  
 lesen, moest. weit auseinander  
 oder

Ueberspreudaempfung mit Abschirmungen zur  
 Versuch. Wie anbinden nur durch  
 ( Spannung min. od. max ) ( ein- od. doppelseitig )

Digital

Senkrechtes Schirmgitter  
 DK's on DGND  
 Zaun daempft mehr als  
 nur eine Leitung

Analog

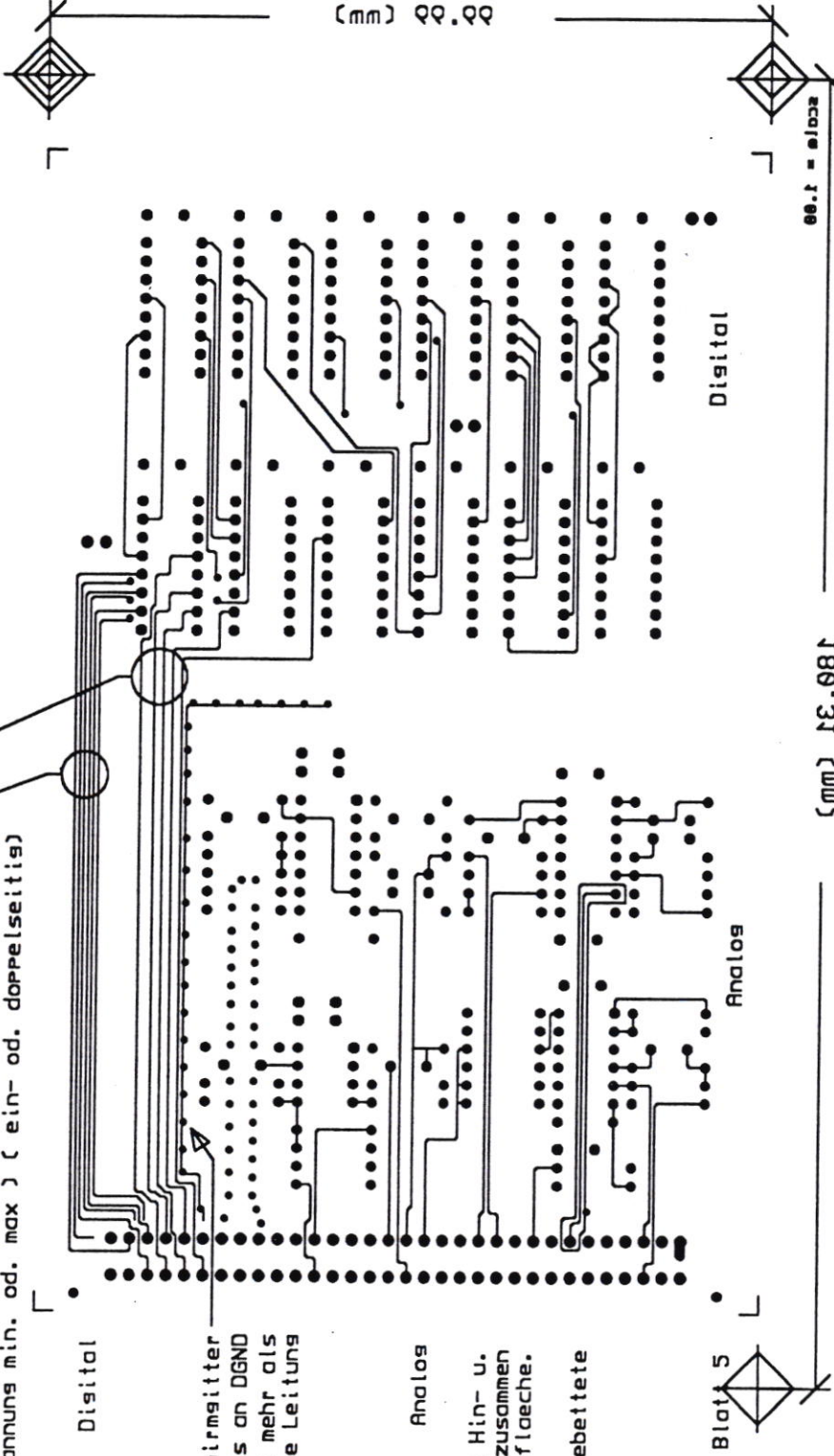
Stromfuehrende Hin- u.  
 Rueckleitung eng zusammen  
 Keine Antennenflaeche.

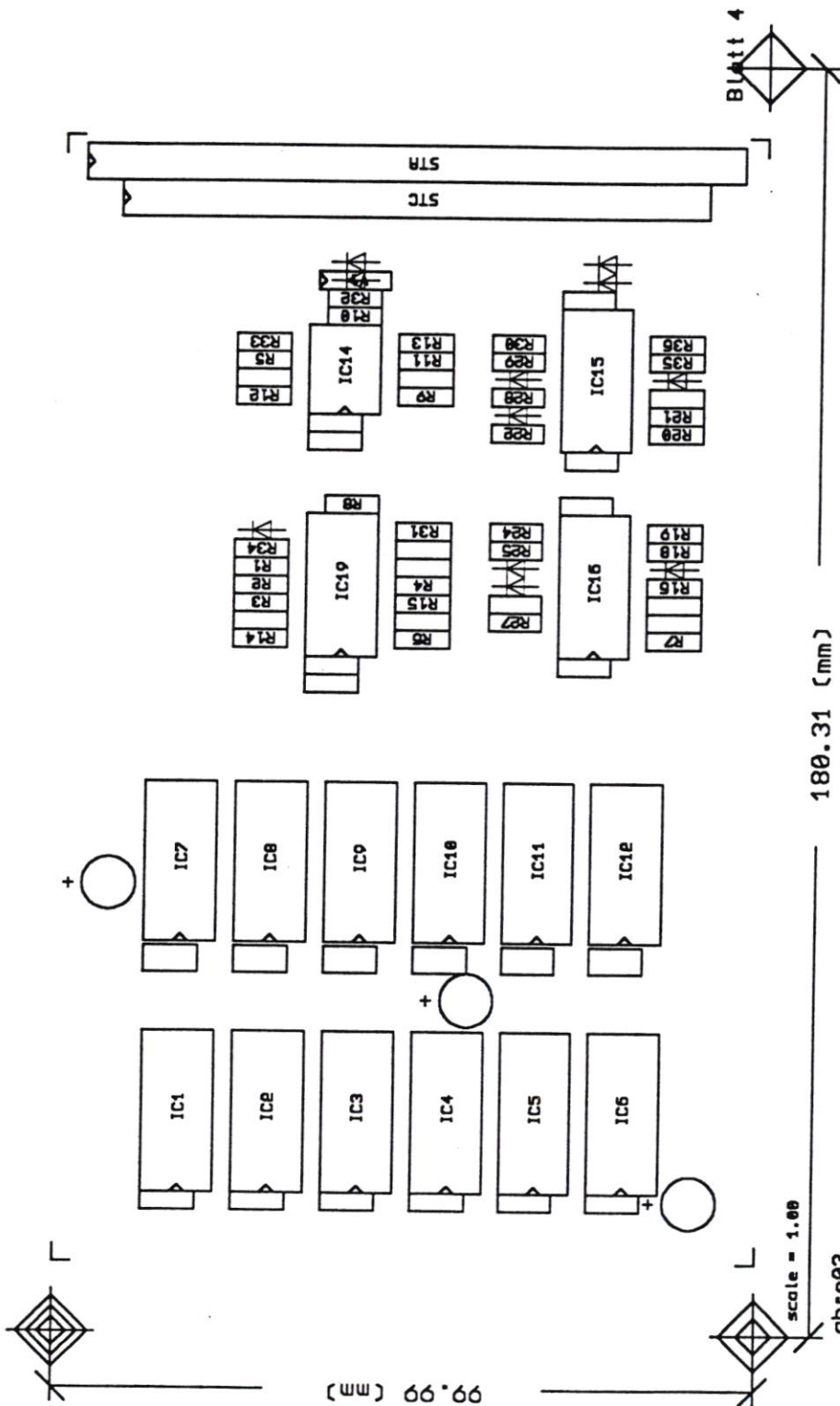
Leitungs In GND einsebettete

Blatt 5

Analog

Digital

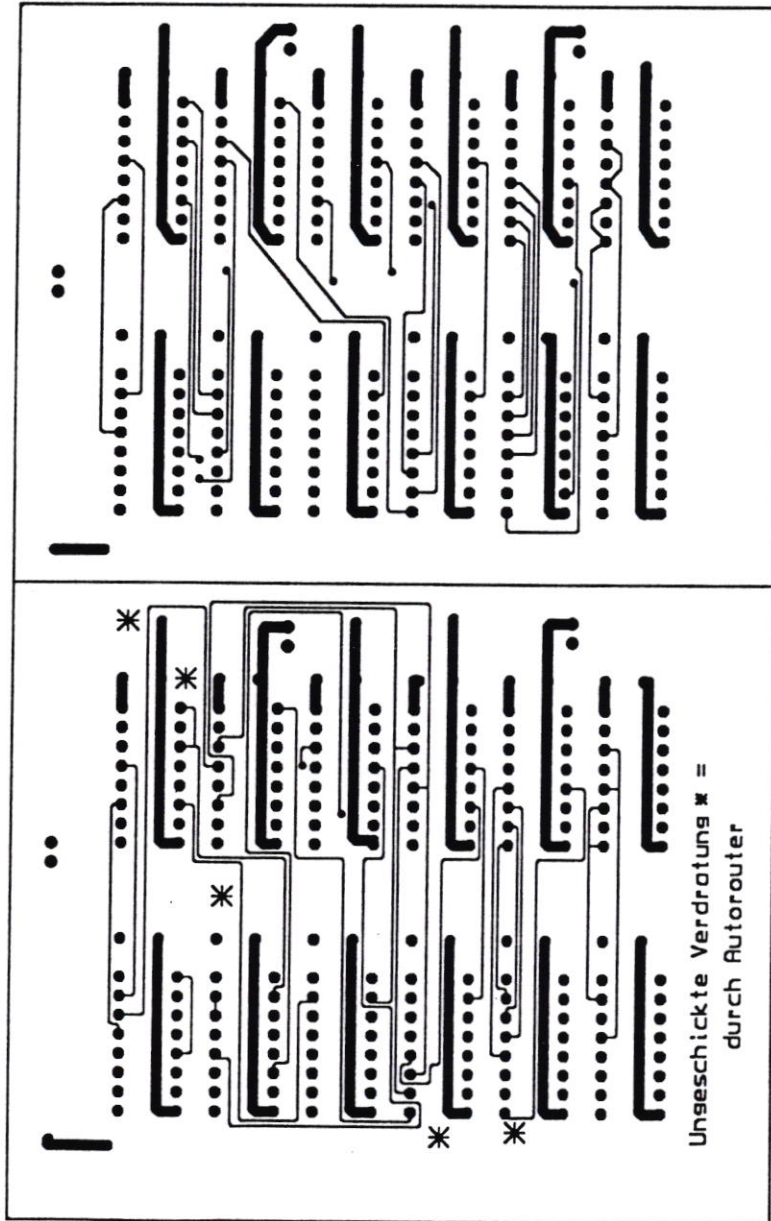




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01-Jun-04 [BPL=1] [SEG=1]

Thema Autorouter



18.081 (mm)

99.99 (mm)

9091rb

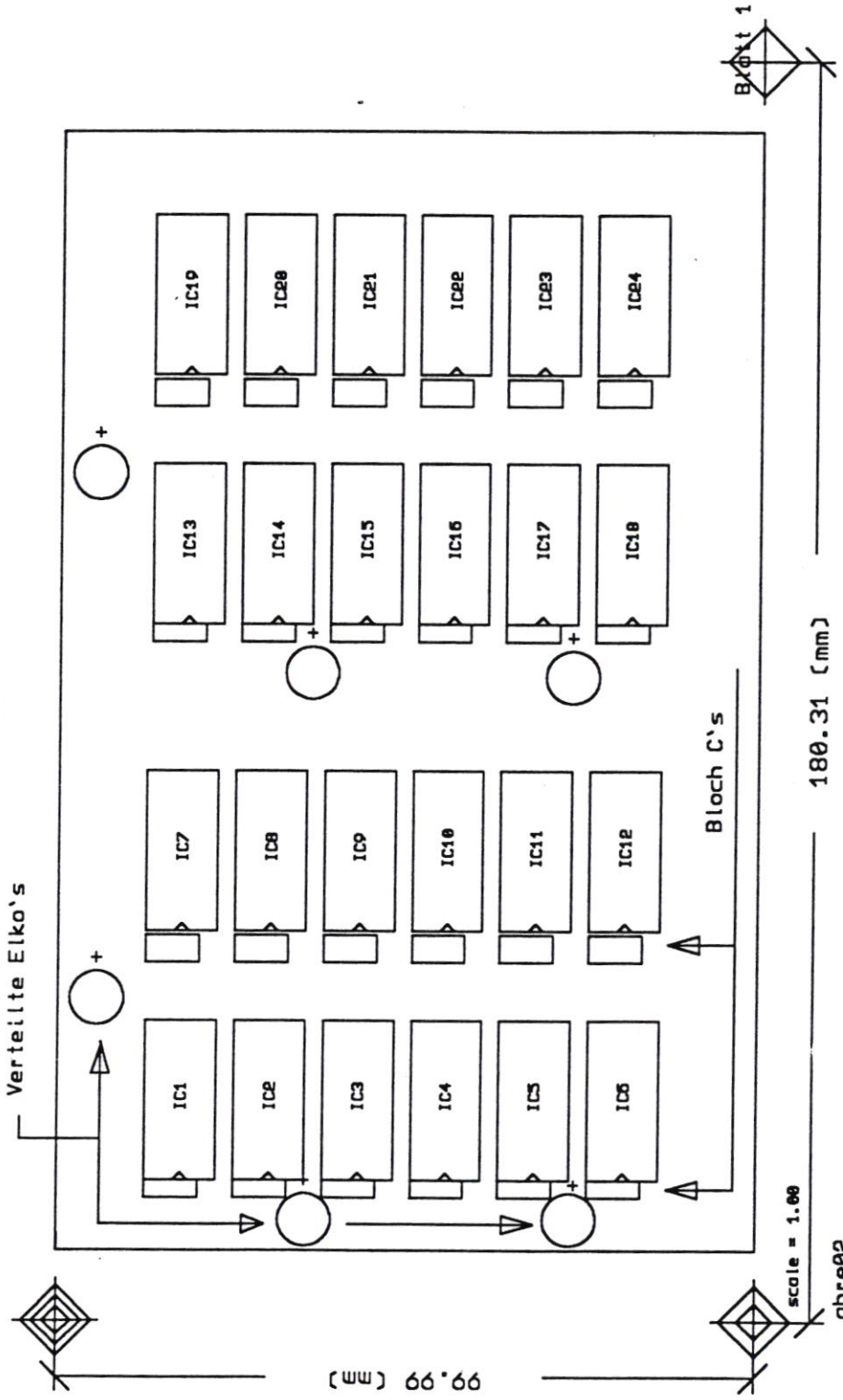
a

SEBEPAD=0

10-10-10



- 1.) Schaltung geht extrem mit ein
- 2.) Erdungskonzept
- 3.) Gehäusefrase
- 4.) Multilayer erschlaegt 85% der Probleme
- 5.) Oder Masseflaechen gross halten



01-Jun-94 BPL=1 SEG=1









Lfn Stk Abk	Bauteilbezeichnung	Wert/Typ	Gehaeuse	RM Hersteller	Lg.Nr.	Bemerkung	Soll	Ist
1	KURZSCHLUSSBUCHSE ISOLIERT MIT OEFFNUNG F. PRUEFCL	Socket		COMATEL	16 009			
2	4 BU KOAXIALKONTAKT ABGEW., 5 LOETANSCHL, NR.: 594 211	BU594-211		ERNI	APL-DL			
3	6 C CHIP-KONDENSATOR 0805	100N	0805		EL-EX			
4	5 C CHIP-KONDENSATOR 0805 FUER ABBLOCK	100N	A-0805		ELEX			
5	18 C Union Carbide C320C104K1R5CA stehend Vielschicht-K	100N		1 UNION CARB	11 096			
6	2 C Union Carbide C320C473K1R5CA stehend Vielschicht-K	47N		1 UNION CARB	11 309			
7	1 CK UNION CARBIDE C330C334K5R5CA STEHEND VIELSCHICHT-K	330N		UNION CARB	11 334			
8	2 CT KER. SCHEIBENTRIMMER NR.:70D-163, TYP 10S-TRIKO 06	CT3P-12P		BUERKLIN	APL			
9	2 F JFET HIGH-FREQUENCY AMPLIFIER, N-FET TO-72 GEH.	2N4416	TO-72	MOTOROLA	APL-DL			
10	5 HF HF-VERSTAERKER	GPD-462	TO-12	AVANTEK	APL-DL			
11	5 IN INDUKTIVITAET 14UH RM=15.24MM	14UH		6				
12	2 OP RC747 GENERAL PURPOSE OPERATIONAL AMPLIFIER	RC747	DIP14	SIGNETICS	13 144			
13	1 OP MC1495 WIDEBAND LINEAR 4-QUADRANT MULTIPLIER	MC1495	DIP14	MOTOROLA	APL-DL			
14	2 OP LM733 DIFFERENTIAL AMPLIFIER	LM733	DIP14	NATIONAL	APL-DL			
15	1 P POTI-BECKMAN 66WR- 5K/0.10W/5%	5K			10 806			
16	1 P POTI-BECKMAN 66WR- 10K/0.10W/5%	10K			10 807			
17	2 R METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	51R1		4 BEYSCHLAG				
18	1 R METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	19K6		4 BEYSCHLAG				











La	Bez.	Wert/Typ	Gehaeuse	RM	Bemerkung	Aufruf-Name	Lg-Nr
B	R3	1K0	MBB0207	4	METALLSCHICHTWIDERSTAND, 0.6W/1% RASTER=4	1K0*J4	10 047
B	R4	1K96		4	METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	1K96*J4	
B	R5	2K87		4	METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	2K87*J4	
B	R6	1K0	MBB0207	4	METALLSCHICHTWIDERSTAND, 0.6W/1% RASTER=4	1K0*J4	10 047
B	R7	14K7		4	METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	14K7*J4	
B	R8	19K6		4	METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	19K6*J4	
B	R9,R10,R11	1K96		4	METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	1K96*J4	
B	R12	511R		4	METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	511R*J4	
B	R13	2K87		4	METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	2K87*J4	
B	R14	51R1		4	METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	51R1*J4	
B	R15	2K87		4	METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	2K87*J4	
B	R16	12K1		4	METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	12K1*J4	
B	R17	261R		4	METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	261R*J4	
B	R18	12K1		4	METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	12K1*J4	
B	R19	196R		4	METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	196R*J4	
B	R20	1K96		4	METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	1K96*J4	
B	R21	196R		4	METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	196R*J4	
B	R22	196R		5	METALLSCHICHTWIDERSTAND 0.6W/1% RASTER=5	196R*J5	10 071
B	R23	1K96		4	METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	1K96*J4	
B	R24	34K8		4	METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	34K8*J4	
B	R25,R26,R27	1K96		4	METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	1K96*J4	
	R28,R29,R30						
B	R31	196R		5	METALLSCHICHTWIDERSTAND 0.6W/1% RASTER=5	196R*J5	10 071
B	R32	1K96		5	METALLSCHICHTWIDERSTAND 0.6W/1% RASTER=5	1K96*J5	10 071
B	R33,R34	196R		4	METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	196R*J4	
B	R35	1K0	MBB0207	4	METALLSCHICHTWIDERSTAND, 0.6W/1% RASTER=4	1K0*J4	10 047

<- B = Bauteilseite / L = Loetseite

La	Bez.	Wert/Typ	Gehaeuse	RM	Bemerkung	Aufruf-Name	Lq-Nr
B	BU1, BU2, BU3	BU594-211			KOAXIALKONTAKT ABGEW., 5 LOETANSCHL, NR.: 594 211	BU594*DI	APL-DL
	BU4						
B	C1, C2, C3	100N		1	Union Carbide C320C104K1R5CA stehend Vielschicht-K	100N*A	11 096
	C4						
B	C5	47N		1	Union Carbide C320C473K1R5CA stehend Vielschicht-K	47N*A	11 309
B	C6, C7, C8	100N		1	Union Carbide C320C104K1R5CA stehend Vielschicht-K	100N*A	11 096
	C9, C10, C11						
	C12, C13						
B	C14	47N		1	Union Carbide C320C473K1R5CA stehend Vielschicht-K	47N*A	11 309
B	C15, C16, C17	100N		1	Union Carbide C320C104K1R5CA stehend Vielschicht-K	100N*A	11 096
	C18, C19, C20						
B	CK3	330N			UNION CARBIDE C330C334K5R5CA STEHEND VIELSCHICHT-K	330N*A	11 334
B	CT1, CT2	CT3P-12P			KER. SCHREIBENTRIMMER NR.: 70D-163, TYP 10S-TRIKO 06	CT3P-12P*B1	APL
B	F1, F2	2N4416	TO-72		JFET HIGH-FREQUENCY AMPLIFIER, N-FET TO-72 GEH.	2N4416	APL-DL
B	HF1, HF2, HF3	GPD-462	TO-12		HF-VERSTAERKER	GPD-462	APL-DL
	HF4, HF5						
B	IN1, IN2, IN3	14UH		6	INDUKTIVITAET 14UH RM=15.24MM	14UH	
	IN4, IN5						
B	J1	Socket			KURZSCHLUSSBUCHSE ISOLIERT MIT OFFENUNG F. PRUEFCL	JU1X3*B2	16 009
B	OP1	MC1495	DIP14		MC1495 WIDEBAND LINEAR 4-QUADRANT MULTIPLIER	MC1495	APL-DL
B	OP2, OP3	LM733	DIP14		LM733 DIFFERENTIAL AMPLIFIER	LM733	APL-DL
B	OP4, OP5	RC747	DIP14		RC747 GENERAL PURPOSE OPERATIONAL AMPLIFIER	RC747	13 144
B	P1	5K			POTI-BECKMAN 66WR- 5K/0.10W/5%	P5K*66	10 806
B	P2	10K			POTI-BECKMAN 66WR- 10K/0.10W/5%	P10K*66	10 807
B	R1	68R1		4	METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	68R1*J4	
B	R2	10K	MBB0207	4	METALLSCHICHTWIDERSTAND, 0.6W/1% RASTER=4	10K*J4	10 071

<- B = Bauteilseite / L = Loetseite

GST	Name:	Datum:	Bestueckungsliste	Version:	Blatt:
Darmstadt	LOOS	11.06.1996	zur Platine SISUI	FG 447.100	1 von 3



E L E K T R . S T U E C K L I S T E

\*RM: 1 EINHEIT = 2.54MM

ST.	BAUTEIL-BEZEICHNUNG	WERT/TYP	RM	HERSTELLER	LG-NR.	BEMERKUNGEN	AUFRUF-NAHME
1	R42	4K7	4		10 063	METALLSCHICHTWIDERSTAND, 0.6W/1% RASTER=4	4K7*J
1	R12	511R	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	511R*J4
2	R14, R41	51R1	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	51R1*J4
2	R1, R39	68R1	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	68R1*J4
2	F1, F2	2N4416		MOTOROLA	APL-DL	JFET HIGH-FREQUENCY AMPLIFIER, N-FET TO-72 GEH.	2N4416
1	TP1	VERO12			16 082	VERO-FEDERPIN O/BOHR.1.2	VERO12
1	LEITERPLATTE	FG 447.100					

GSI DARMSTADT SISU1	BEZ.: -----	NUMBER: FG 447.100	BEARB.: -----	DATUM 11.Jun.1996	BLATT 3 VON 3
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E L E K T R . S T U E C K L I S T E

\*RM: 1 EINHEIT = 2.54MM

ST.	BAUTEIL-BEZEICHNUNG	WERT/TYP	RM	HERSTELLER	LG-NR.	BEMERKUNGEN	AUFRUF-NAHME
1	OP1	MC1495		MOTOROLA	APL-DL	MC1495 WIDEAND LINEAR 4-QUADRANT MULTIPLIER	MC1495
2	OP4,OP5	RC747		SIGNETICS	13 144	RC747 GENERAL PURPOSE OPERATIONAL AMPLIFIER	RC747
1	P2	10K			10 807	POTI-BECKMAN 66WR- 10K/0.10W/5%	P10K*66
1	P1	5K			10 806	POTI-BECKMAN 66WR- 5K/0.10W/5%	P5K*66
1	R2	10K	4	BEYSCHLAG	10 071	METALLSCHICHTWIDERSTAND, 0.6W/1% RASTER=4	10K*J4
1	R38	10K	5		10 071	METALLSCHICHTWIDERSTAND 0.6W/1% RASTER=5	10K*J5
2	R16,R18	12K1	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	12K1*J4
1	R7	14K7	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	14K7*J4
4	R19,R21,R33	196R	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	196R*J4
	R34						
2	R22,R31	196R	5		10 071	METALLSCHICHTWIDERSTAND 0.6W/1% RASTER=5	196R*J5
1	R8	19K6	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	19K6*J4
4	R3,R6,R35	1K0	4	BEYSCHLAG	10 047	METALLSCHICHTWIDERSTAND, 0.6W/1% RASTER=4	1K0*J4
	R40						
1	R36	1K3	4		10 050	METALLSCHICHTWIDERSTAND, 0.6W/1% RASTER=4	1K3*J4
13	R4,R9,R10	1K96	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	1K96*J4
	R11,R20,R23						
	R25,R26,R27						
	R28,R29,R30						
	R37						
1	R32	1K96	5		10 071	METALLSCHICHTWIDERSTAND 0.6W/1% RASTER=5	1K96*J5
1	R17	261R	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	261R*J4
3	R5,R13,R15	2K87	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	2K87*J4
1	R24	34K8	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	34K8*J4

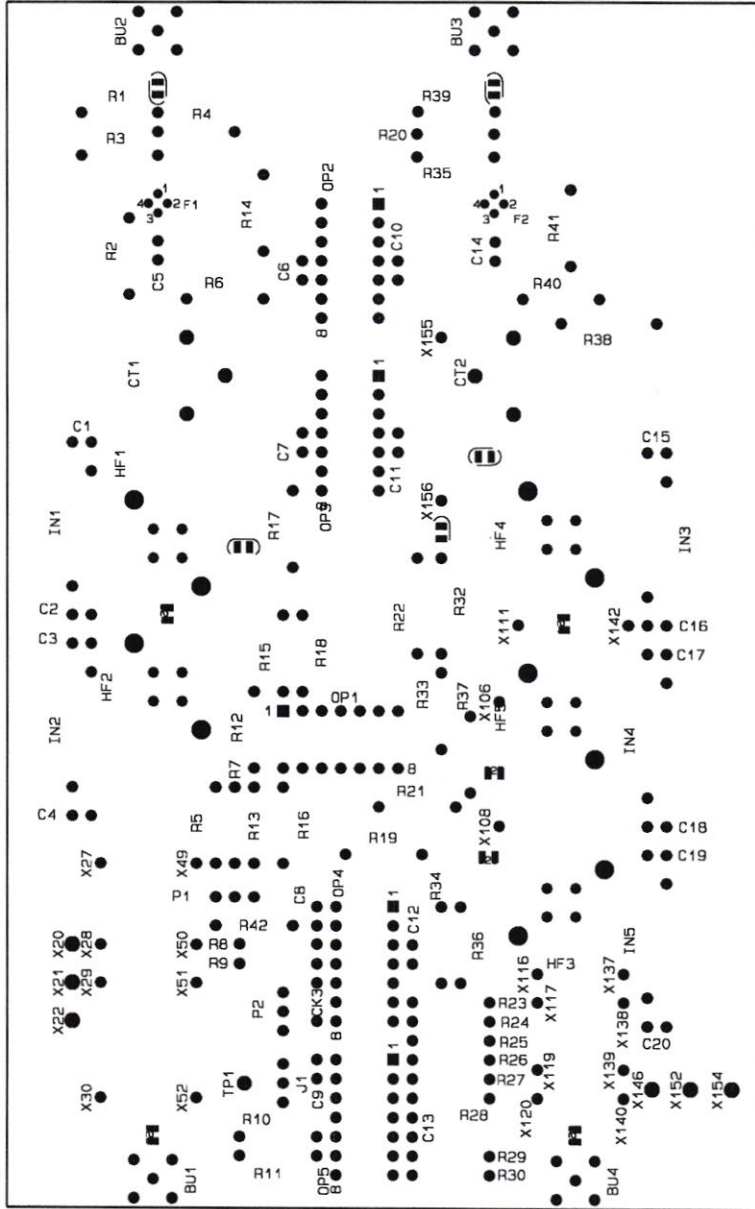
BEZ.:	NUMMER:	BEARB.:	DATUM	BLATT
GSI	FG 447.100	-----	11.Jun.1996	2 VON 3
DARMSTADT		LOOS		
SISUI				

E L E K T R . S T U E C K L I S T E

\*RM: 1 EINHEIT = 2.54MM

ST.	BAUTEIL-BEZEICHNUNG	WERT/TYP	RM	HERSTELLER	LG-NR.	BEMERKUNGEN	AUFRUF-NAHME
5	DIP14*DI				13 322	CAB/DALEKTRON 110-91-314 DUAL-IN-LINE ZUM LOETEN	
4	BU1, BU2, BU3	BU594-211		ERNI	APL-DL	KOAXIALKONTAKT ABGEW., 5 LOETANSCHL, NR.: 594 211	BU594*DI
	BU4				EL-EX	CHIP-KONDENSATOR 0805	100N*0805
6	C21, C22, C23	100N			ELEX	CHIP-KONDENSATOR 0805 FUER ABBLOCK	100N*0805A
5	C24, C25, C26	100N					
	C27, C28, C29						
	C30, C31						
18	C1, C2, C3	100N	1	UNION CARB	11 096	Union Carbide C320C104K1R5CA stehend Vielschicht-K	100N*A
	C4, C6, C7						
	C8, C9, C10						
	C11, C12, C13						
	C15, C16, C17						
	C18, C19, C20						
1	CK3	330N		UNION CARB	11 334	UNION CARBIDE C330C334K5R5CA STEHEND VIELSCHICHT-K	330N*A
2	C5, C14	47N	1	UNION CARB	11 309	Union Carbide C320C473K1R5CA stehend Vielschicht-K	47N*A
2	CT1, CT2	CT3P-12P		BUERKLIN	APL	KER. SCHEIBENTRIMMER NR.:70D-163, TYP 10S-TRIKO 06	CT3P-12P*B1
5	MONTAGEMAT			AVANTEK	APL-DL	ACCESSORY-PACK, NR.: 330-006756-001	
5	HF1, HF2, HF3	GPD-462		AVANTEK	APL-DL	HF-VERSTAERKER	GPD-462
	HF4, HF5						
1	KURZSCHL. -BU			COMATEL	16 009	KURZSCHLUSSBUCHSE ISOLIERT MIT OEFFNUNG F. PRUEFCL	
	J1	WW-L. ZUSCHN	1		16 080	COMATEL 385.0358.120.400 WW-LEISTE ZUSCHN. 1X3PINS	JUIX3*B2
5	IN1, IN2, IN3	14UH	6			INDUKTIVITAET 14UH RM=15.24MM	14UH
	IN4, IN5						
2	OP2, OP3	LM733		NATIONAL	APL-DL	LM733 DIFFERENTIAL AMPLIFIER	LM733

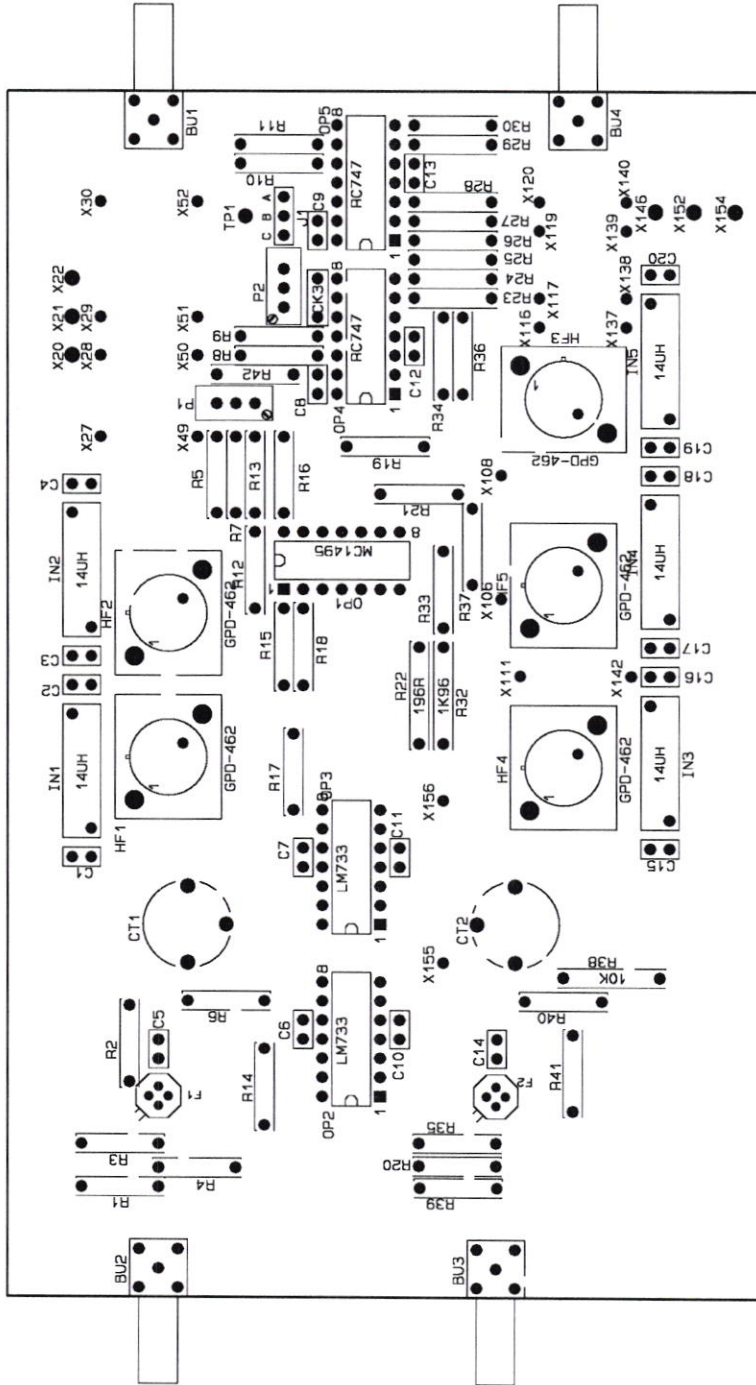
GS1 DARMSTADT	BEZ.:	NUMMER:	BEARB.:	DATUM	BLATT
SISU1		FG 447.100	11.Jun.1996	11.Jun.1996	1 VON 3
			LOOS		



1 FG 447.100

SISU1

GSI SISU1KON2  
11-JUN-1996 L005



GSI SISU1KON1  
11-JUN-1996 L00S

SISU1 FG 447.100

*New!*



E L E K T R . S T U E C K L I S T E

\*RM: 1 EINHEIT = 2.54MM

ST.	BAUTEIL-BEZEICHNUNG	WERT/TYP	RM	HERSTELLER	LG-NR.	BEMERKUNGEN	AUFRUF-NAHME
1	R42	4K7S	4		10 063	METALLSCHICHTWIDERSTAND, 0.6W/1% RASTER=4	4K7*J
1	R12	511R	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	511R*J4
2	R14, R41	51R1	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	51R1*J4
2	R1, R39	68R1	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	68R1*J4
2	F1, F2	2N4416		MOTOROLA	APL-DL	JFET HIGH-FREQUENCY AMPLIFIER, N-FET TO-72 GEH.	2N4416
1	TP1	VERO12			16 082	VERO-FEDERPIN O/BOHR.1.2	VERO12
1	LEITERPLATTE	FG 447.100					

BEZ.: GSI DARMSTADT	NUMMER: FG 447.100	BEARB.: LOOS	DATUM: 11.Jun.1996	BLATT 3 VON 3
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E L E K T R . S T U E C K L I S T E

\*RM: 1 EINHEIT = 2.54MM

ST.	BAUTEIL-BEZEICHNUNG	WERT/TYP	RM	HERSTELLER	LG-NR.	BEMERKUNGEN	AUFRUF-NAHME
1	OP1	MC1495		MOTOROLA	APL-DL	MC1495 WIDEBAND LINEAR 4-QUADRANT MULTIPLIER	MC1495
2	OP4,OP5	RC747		SIGNETICS	13 144	RC747 GENERAL PURPOSE OPERATIONAL AMPLIFIER	RC747
1	P2	10K			10 807	POTI-BECKMAN 66WR- 10K/0.10W/5%	P10K*66
1	P1	5K			10 806	POTI-BECKMAN 66WR- 5K/0.10W/5%	P5K*66
1	R2	10K	4	BEYSCHLAG	10 071	METALLSCHICHTWIDERSTAND, 0.6W/1% RASTER=4	10K*J4
1	R38	10K	5		10 071	METALLSCHICHTWIDERSTAND 0.6W/1% RASTER=5	10K*J5
2	R16,R18	12K1	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	12K1*J4
1	R7	14K7	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	14K7*J4
4	R19,R21,R33	196R	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	196R*J4
	R34						
2	R22,R31,2	196R	5		10 071	METALLSCHICHTWIDERSTAND 0.6W/1% RASTER=5	196R*J5
1	R8	19K6 (20K)	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), 2M=4	19K6*J4
4	R3,R6,R35	1K0	4	BEYSCHLAG	10 047	METALLSCHICHTWIDERSTAND, 0.6W/1% RASTER=4	1K0*J4
	R40						
1	R36	1K3	4		10 050	METALLSCHICHTWIDERSTAND, 0.6W/1% RASTER=4	1K3*J4
13	R4,R9,R10	1K96	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	1K96*J4
	R11,R20,R23						
	R25,R26,R27						
	R28,R29,R30						
	R37						
1	R32	1K96	5		10 071	METALLSCHICHTWIDERSTAND 0.6W/1% RASTER=5	1K96*J5
1	R17	261R	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	261R*J4
3	R5,R13,R15	2K87	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	2K87*J4
1	R24	34K8	4	BEYSCHLAG		METALLSCHICHT-R 0.6W/1%, MBB0207-50BX (E96), RM=4	34K8*J4

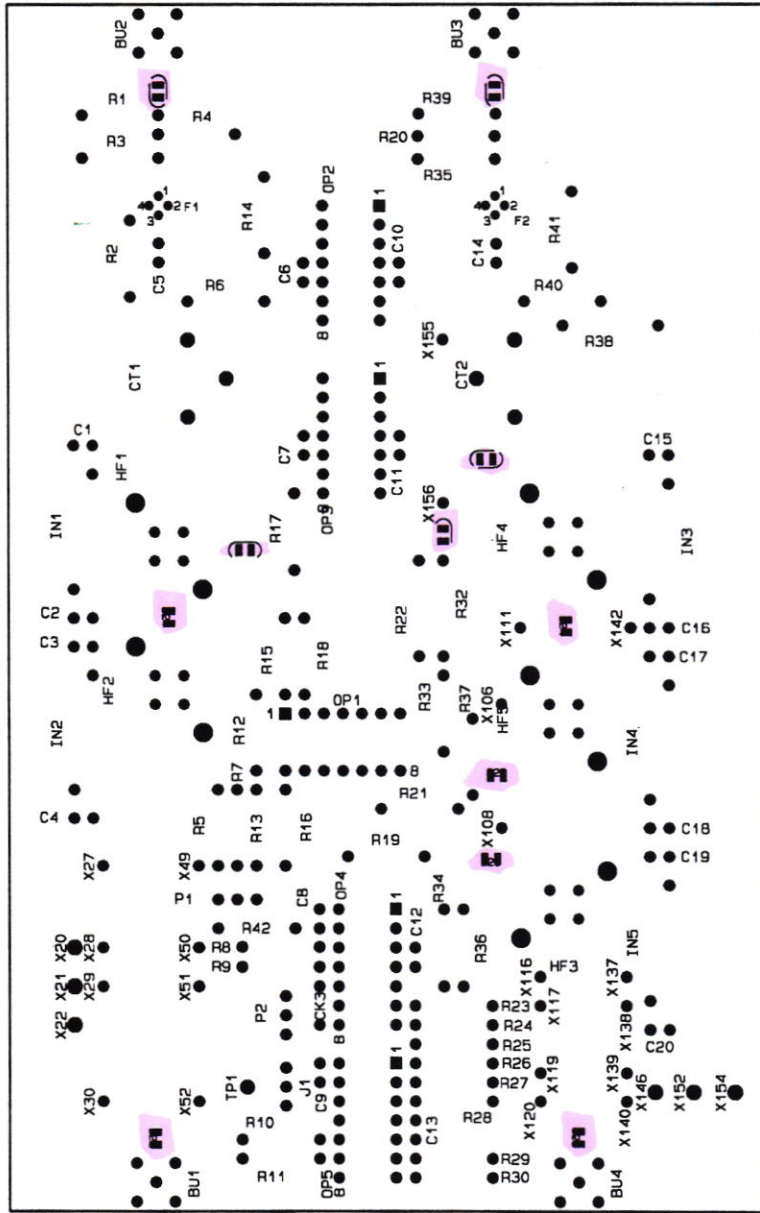
BEZ.: GSI DARMSTADT  
 BEARB.:  
 NUMBER: FG 447.100  
 DATUM: 11. Jun. 1996  
 LOOS  
 BLATT 2 VON 3

E L E K T R . S T U E C K L I S T E

\*RM: 1 EINHEIT = 2.54MM

ST.	BAUTEIL-BEZEICHNUNG	WERT/TYP	RM	HERSTELLER	LG-NR.	BEMERKUNGEN	AUFRUF-NAHME
5	DIP14*DI				13 322	CAB/DALEKTRON 110-91-314 DUAL-IN-LINE ZUM LOETEN	
4	BU1, BU2, BU3	BU594-211		ERNI	APL-DL	KOAXIALKONTAKT ABGEW., 5 LOETANSCHL, NR.: 594 211	BU594*DI
	BU4				EL-EX	CHIP-KONDENSATOR 0805	100N*0805
6	C21, C22, C23	100N			ELEX	CHIP-KONDENSATOR 0805 FUER ABBLOCK	100N*0805A
	C24, C25, C26						
5	C27, C28, C29	100N					
	C30, C31						
18	C1, C2, C3	100N	1	UNION CARB	11 096	Union Carbide C320C104K1R5CA stehend Vielschicht-K	100N*A
	C4, C6, C7						
	C8, C9, C10						
	C11, C12, C13						
	C15, C16, C17						
	C18, C19, C20						
1	CK3	330N		UNION CARB	11 334	UNION CARBIDE C330C334K5R5CA STEHEND VIELSCHICHT-K	330N*A
2	C5, C14	47N	1	UNION CARB	11 309	Union Carbide C320C473K1R5CA stehend Vielschicht-K	47N*A
2	CT1, CT2	CT3P-12P		BUERKLIN	APL	KER. SCHEIBENTRIMMER NR.:70D-163, TYP 10S-TRIKO 06	CT3P-12P*B1
5	MONTAGEMAT			AVANTEK	APL-DL	ACCESSORY-PACK, NR.: 330-006756-001	
5	HF1, HF2, HF3	GPD-462		AVANTEK	APL-DL	HF-VERSTAERKER	GPD-462
	HF4, HF5						
1	KURZSCHL.-BU			COMATEL	16 009	KURZSCHLUSSBUCHSE ISOLIERT MIT OEFFNUNG F. PRUEFCL	
	J1	WW-L. ZUSCHN	1		16 080	COMATEL 385.0358.120.400 WW-LEISTE ZUSCHN. 1X3PINS	JULX3*B2
5	IN1, IN2, IN3	14UH	6			INDUKTIVITAET 14UH RM=15.24MM	14UH
	IN4, IN5						
2	OP2, OP3	LM733		NATIONAL	APL-DL	LM733 DIFFERENTIAL AMPLIFIER	LM733

GS1 DARMSTADT	BEZ.:	NUMMER:	BEARB.:	DATUM	BLATT
SISU1		FG 447.100	LOOS	11.Jun.1996	1 VON 3

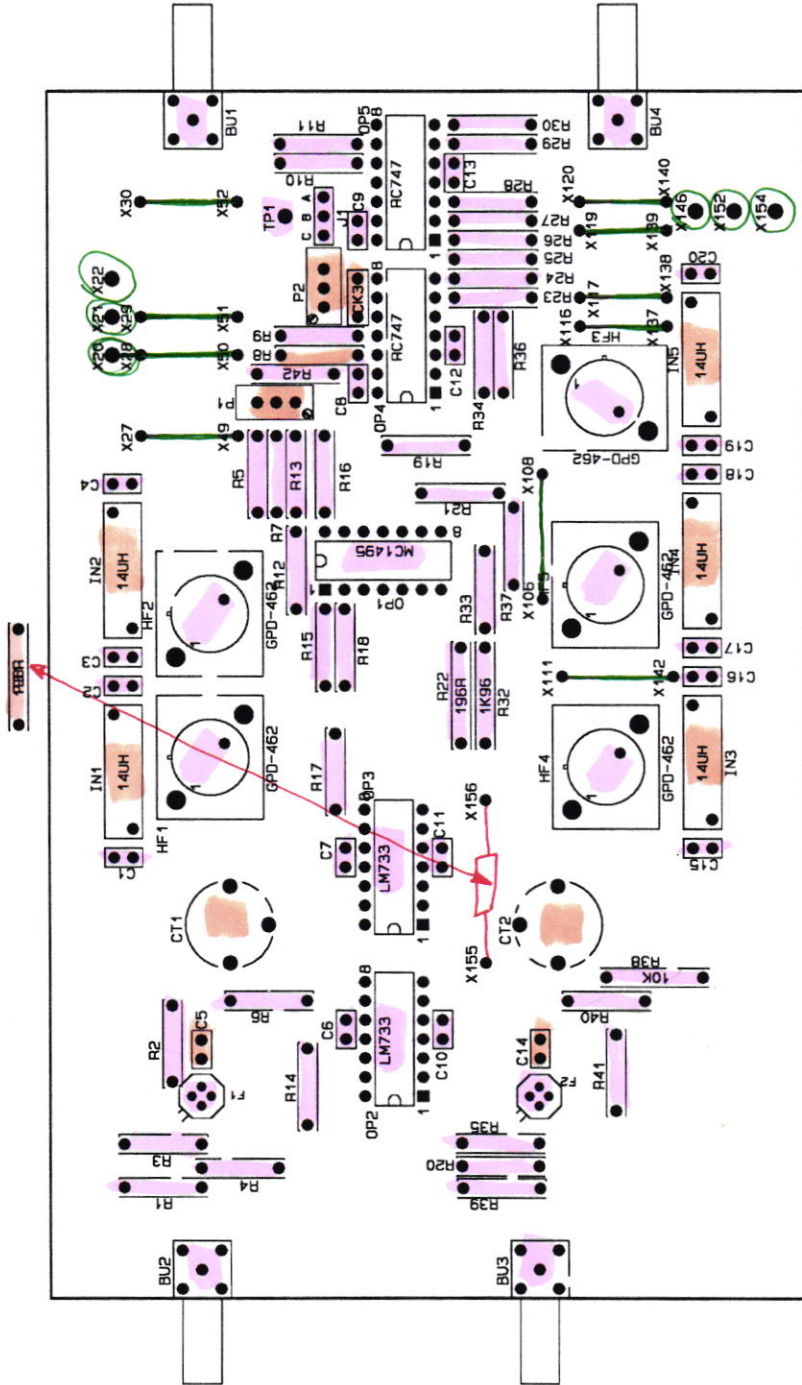


FG 447.100

SISU1

GSI SISU1KON2  
11-JUN-1996 L005



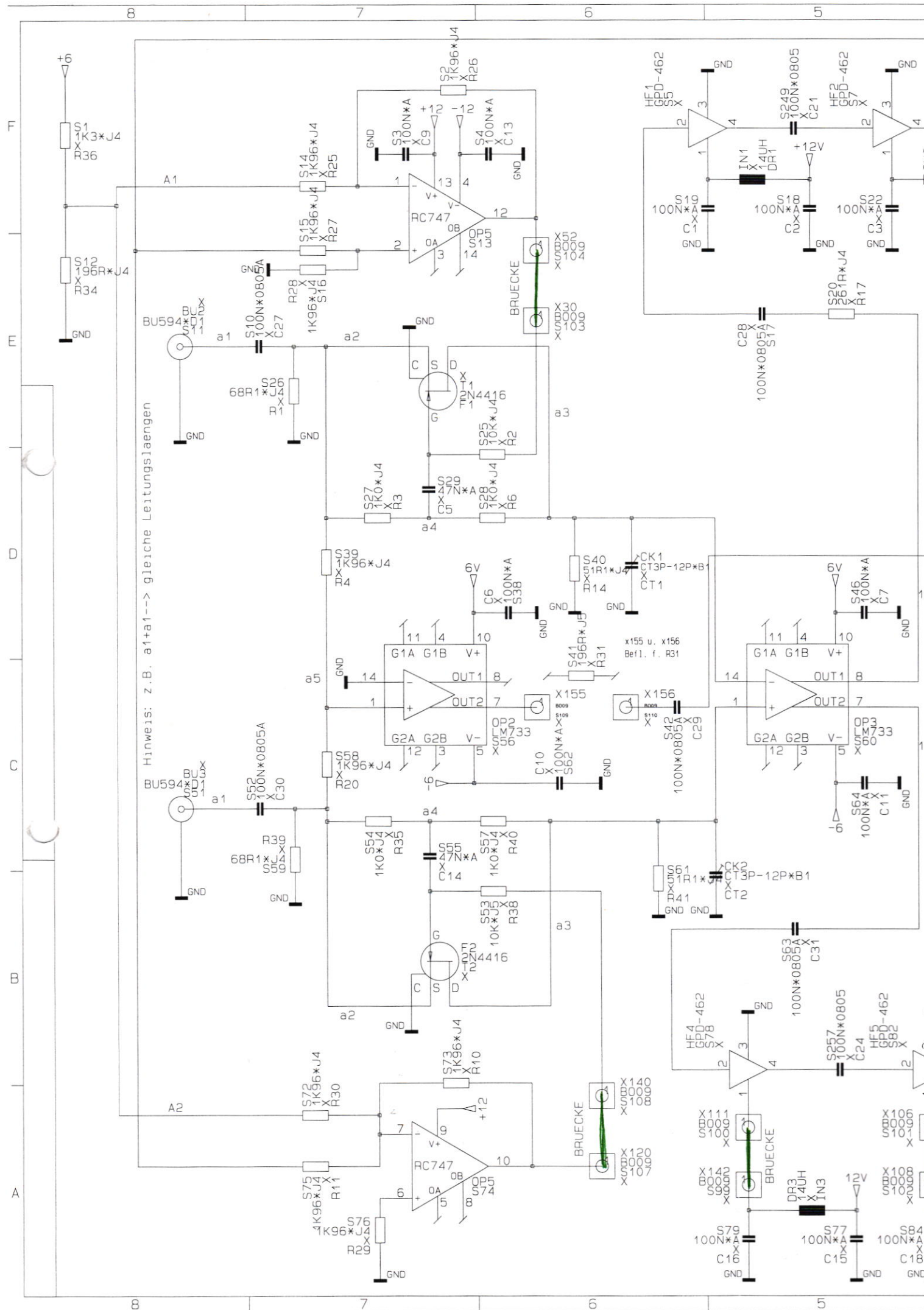


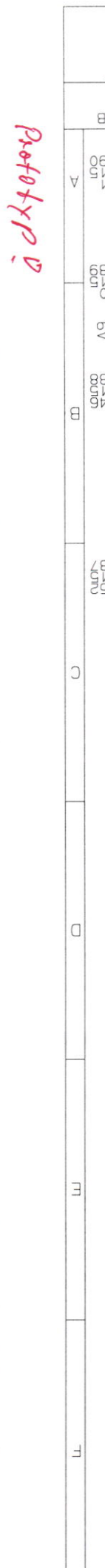
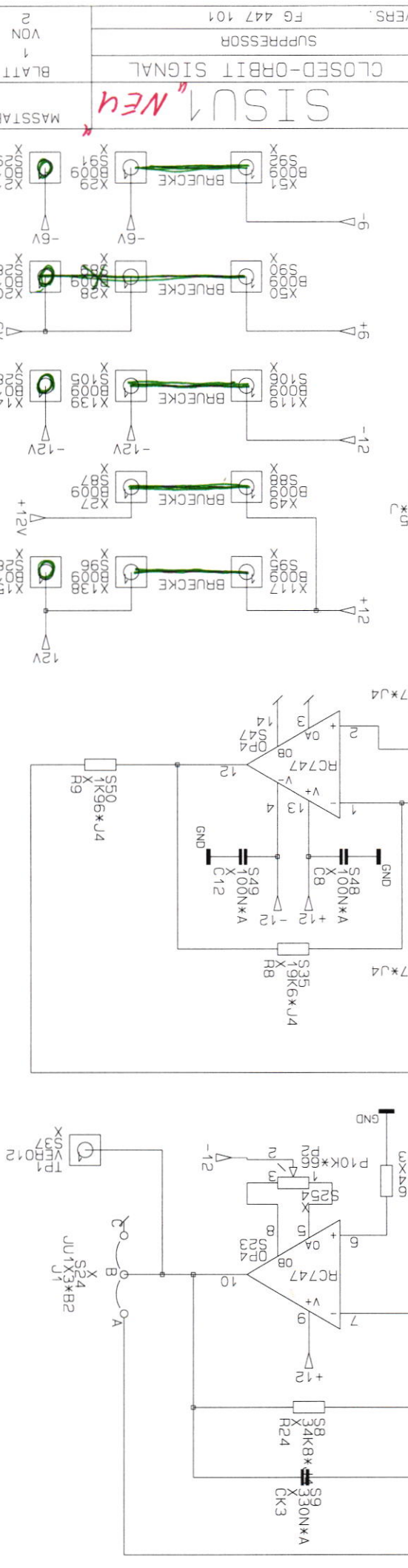
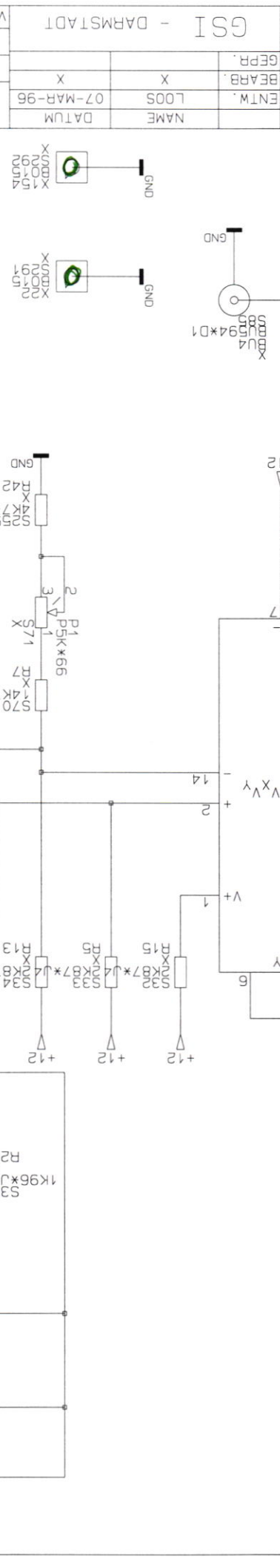
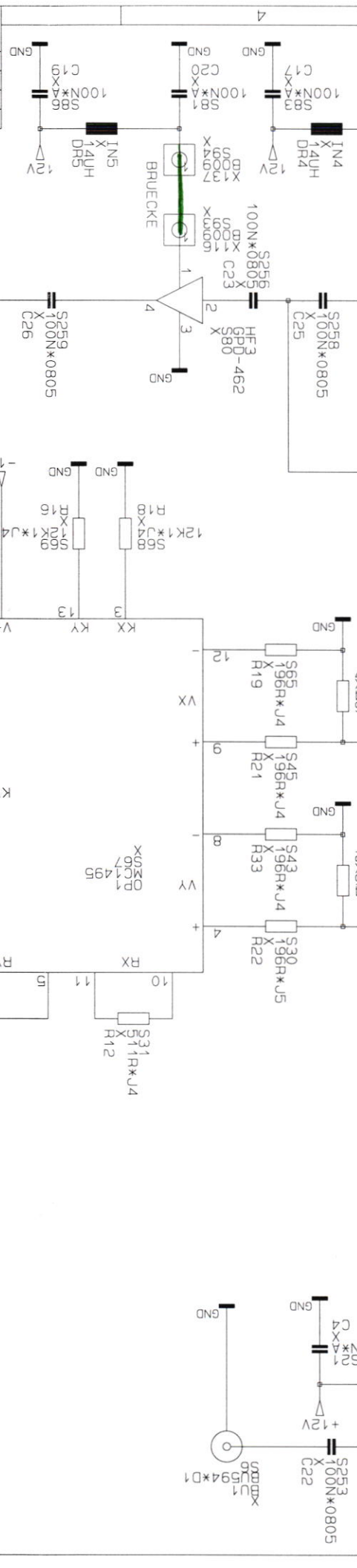
GSI SISU1KON1  
11-JUN-1996 L00S

SISU1

FG 447.100

*New!*





Prototyp 2

GSI - DARMASTADT		VERS.	FG 447 101
GSI - DARMASTADT		SUPPRESSOR	
CLOSED-ORBIT SIGNAL		BLATT	2
MASSSTAB		VON	1
SISU1 "NEU"			
NAME	DATUM	ENTW.	GEPR.
BEARB.	LOOS	07-MAR-96	X



Bestellung bei Spoerle für Kart. Stelle  
oder E3V

25 x JC-MC1595L

15,56 DM/Stk

25 x MA 733 CN DIP

3,23 DM/Stk

Typ → 19K6 fehlt

Bestellung Fa. Bürklin für Kostenstelle: 68370

Metallschichtwiderstände 0,6W Typ. 0207

Stück	Wert	Best.Nr.			
100 x	51 R 1	30 E 168	0,08 DM/stück	8,-	
100 x	68 R 1	30 E 180	0,08 "	8,-	
100 x	196 R	30 E 224	0,08 "	8,-	
100 x	261 R	30 E 236	0,08 "	8,-	
100 x	511 R	30 E 264	0,08 "	8,-	
100 x	1K0	30 E 292	0,08 "	8,-	
100 x	1K30	30 E 303	0,08 "	8,-	
200 x	1K96	30 E 320	0,06 "	12,-	
100 x	2K74	30 E 334	0,08 "	8,-	
100 x	2K87	30 E 336	0,08 "	8,-	
100 x	4K75	30 E 358	0,08 "	8,-	
100 x	10K0	30 E 389	0,08 "	8,-	
100 x	12K1	30 E 397	0,08 "	8,-	
100 x	14K7	30 E 405	0,08 "	8,-	
100 x	34K8	30 E 441	0,08 "	8,-	

SMD-Keramik-Vielstichtkondensatoren Typ Siamero

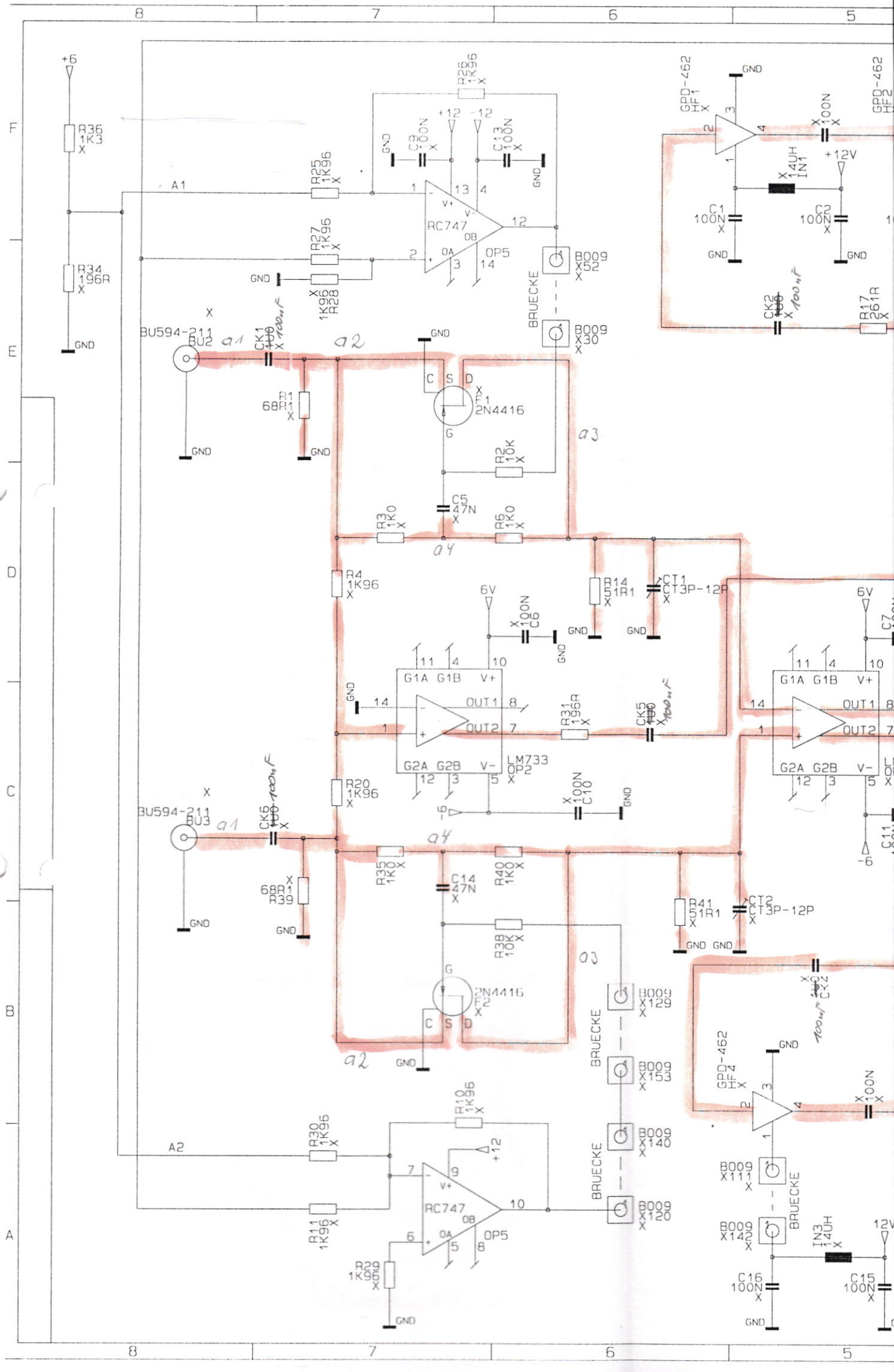
100 x	1µF	Best.Nr. 53D 118	2,73 "	273,-
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25 x	0,047µF	53D 100	1,04 "	

Scheibentrimmer

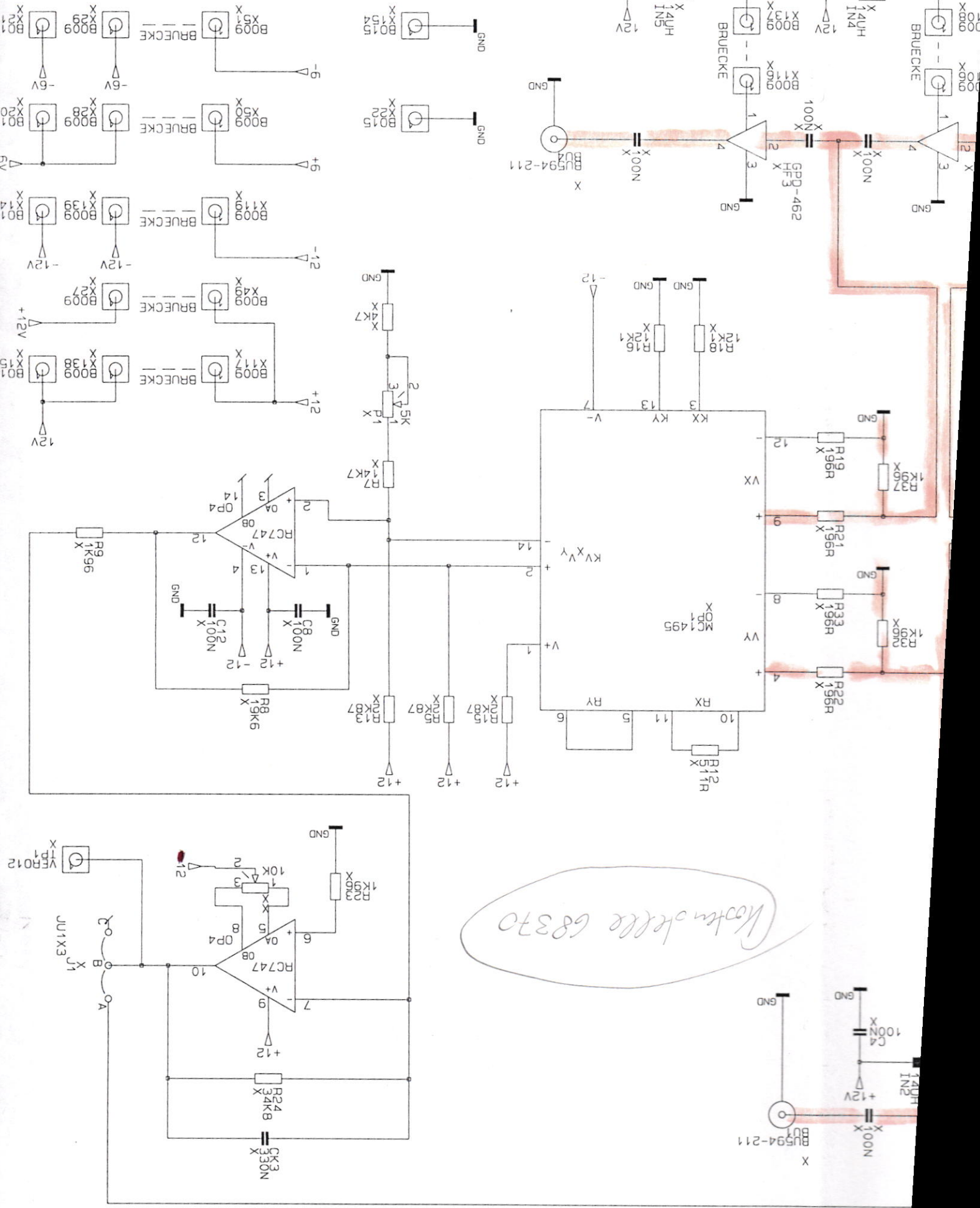
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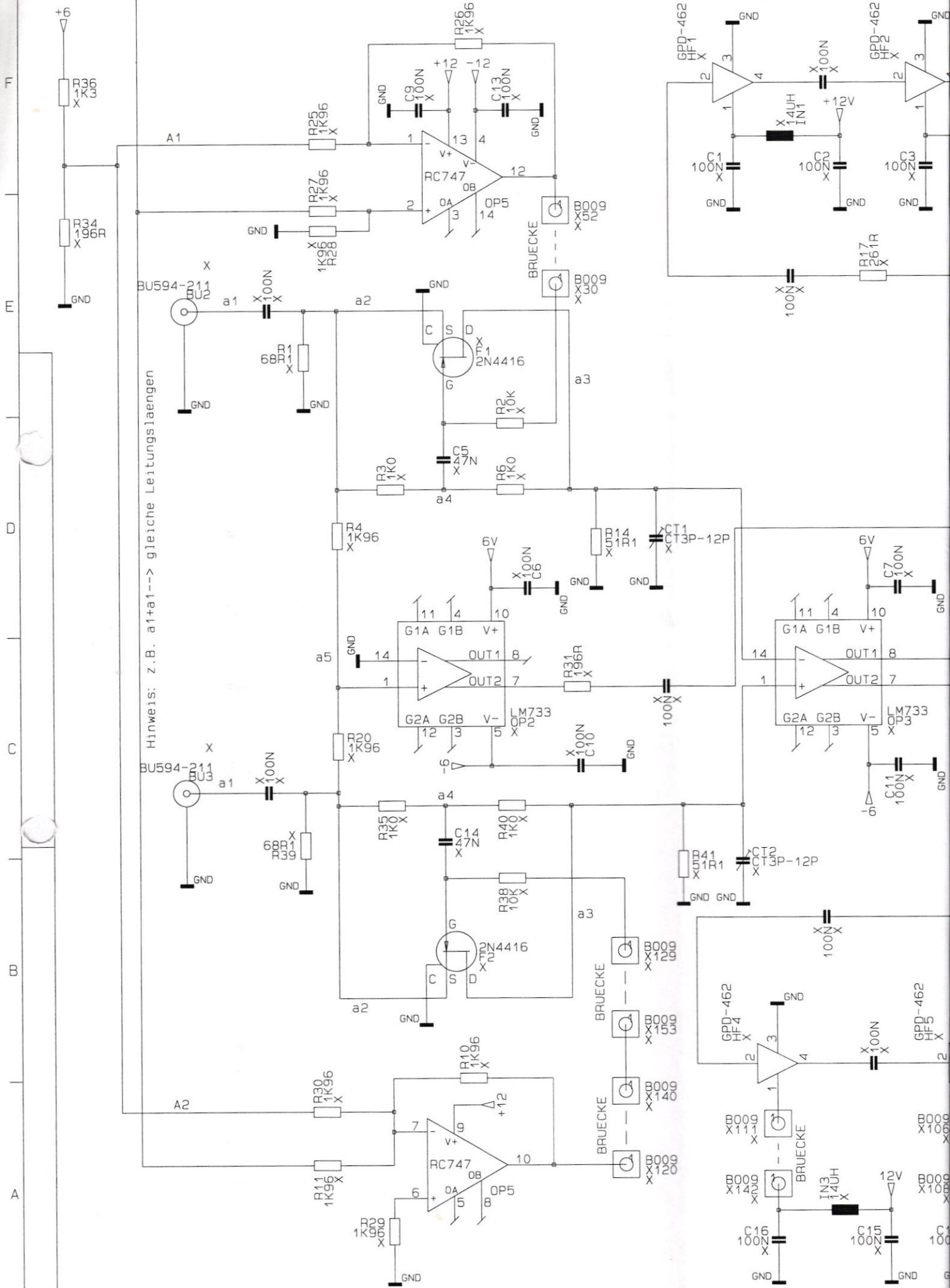
JFET-Transistoren

25 x	2N4416	Best.Nr. 27 S 7700	1,96 "
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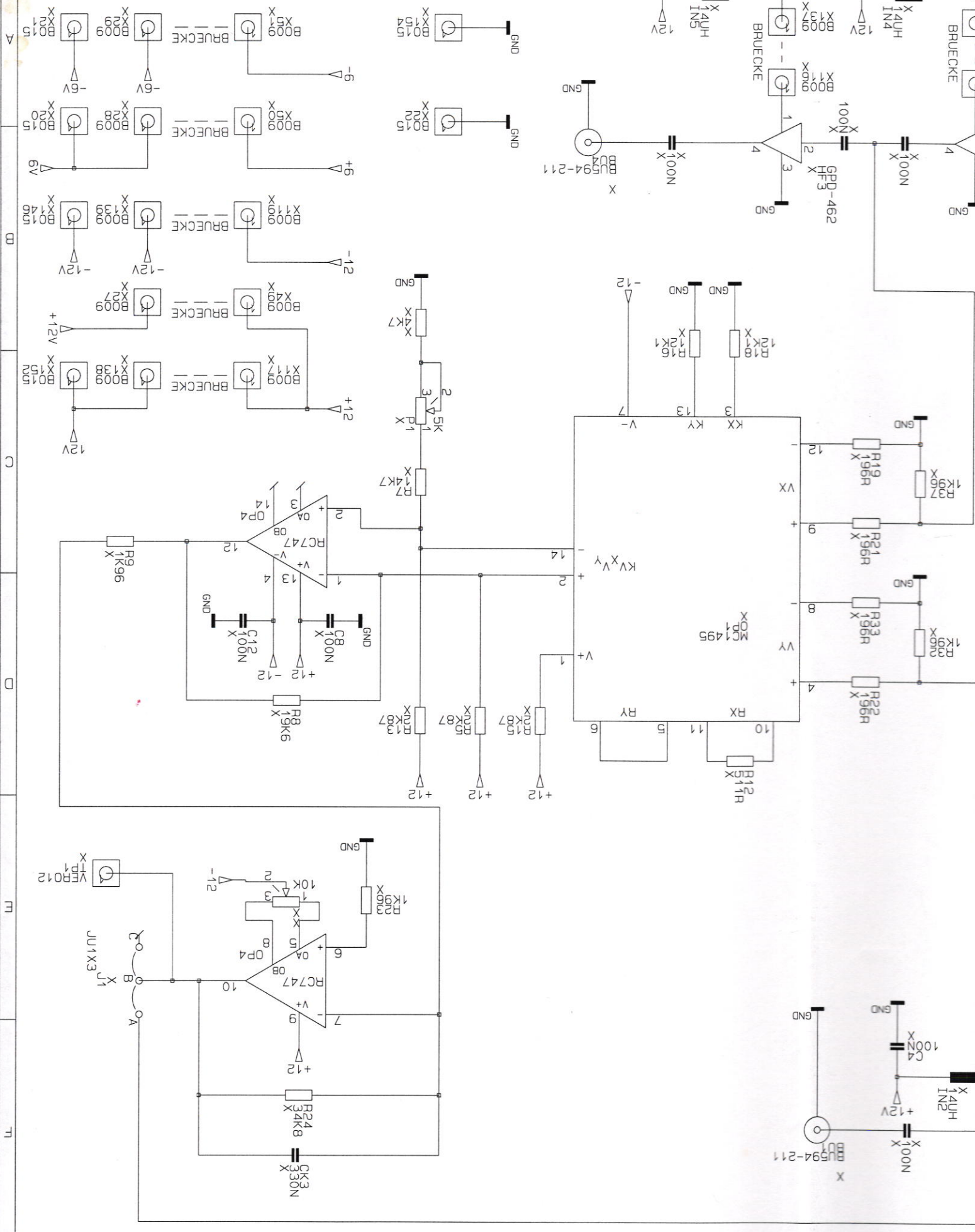
SISU1



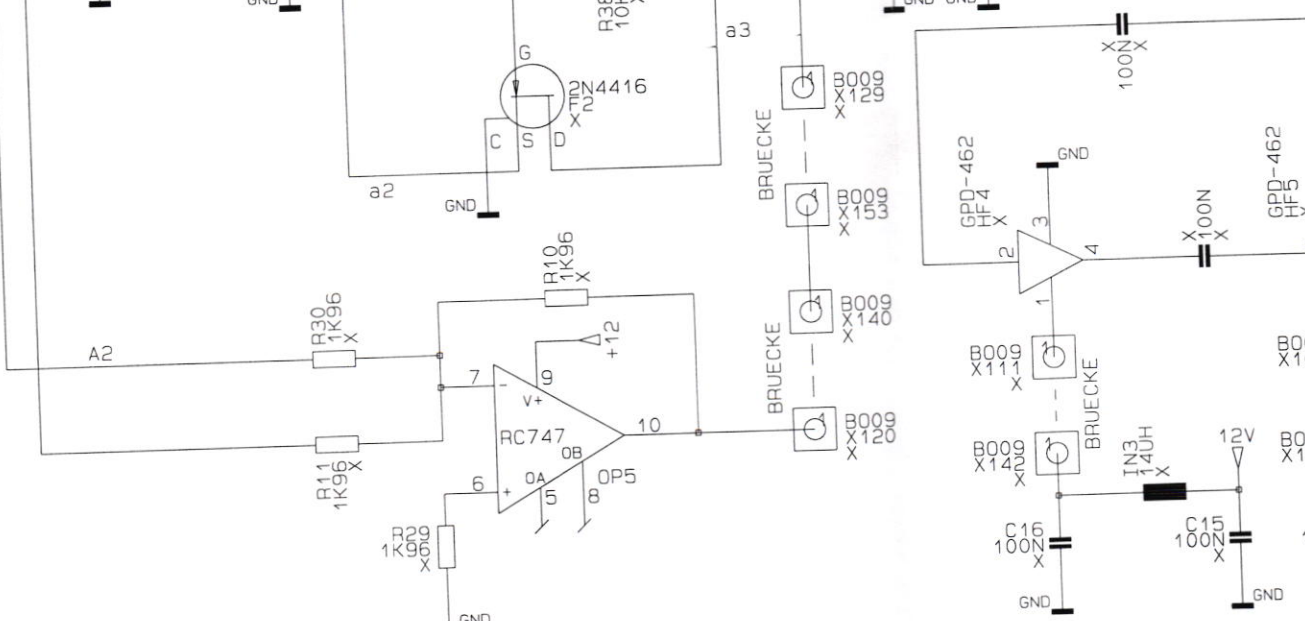
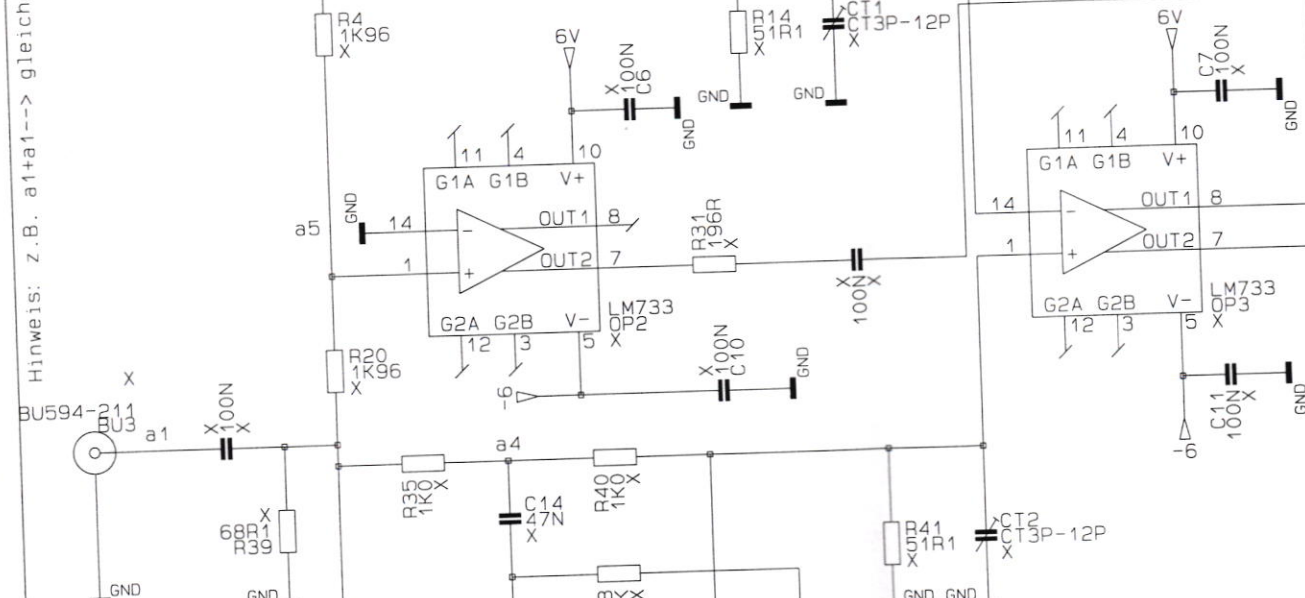
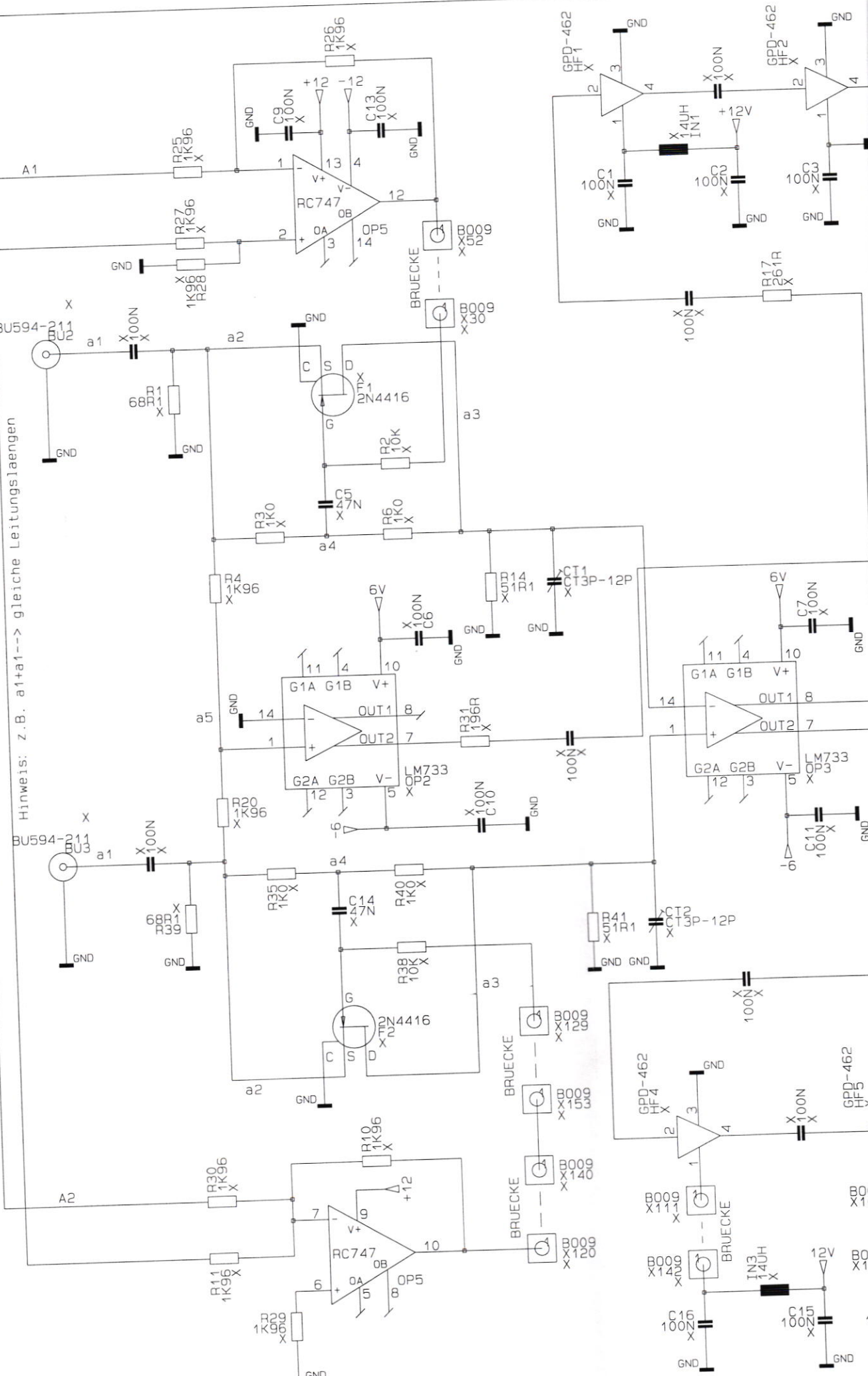
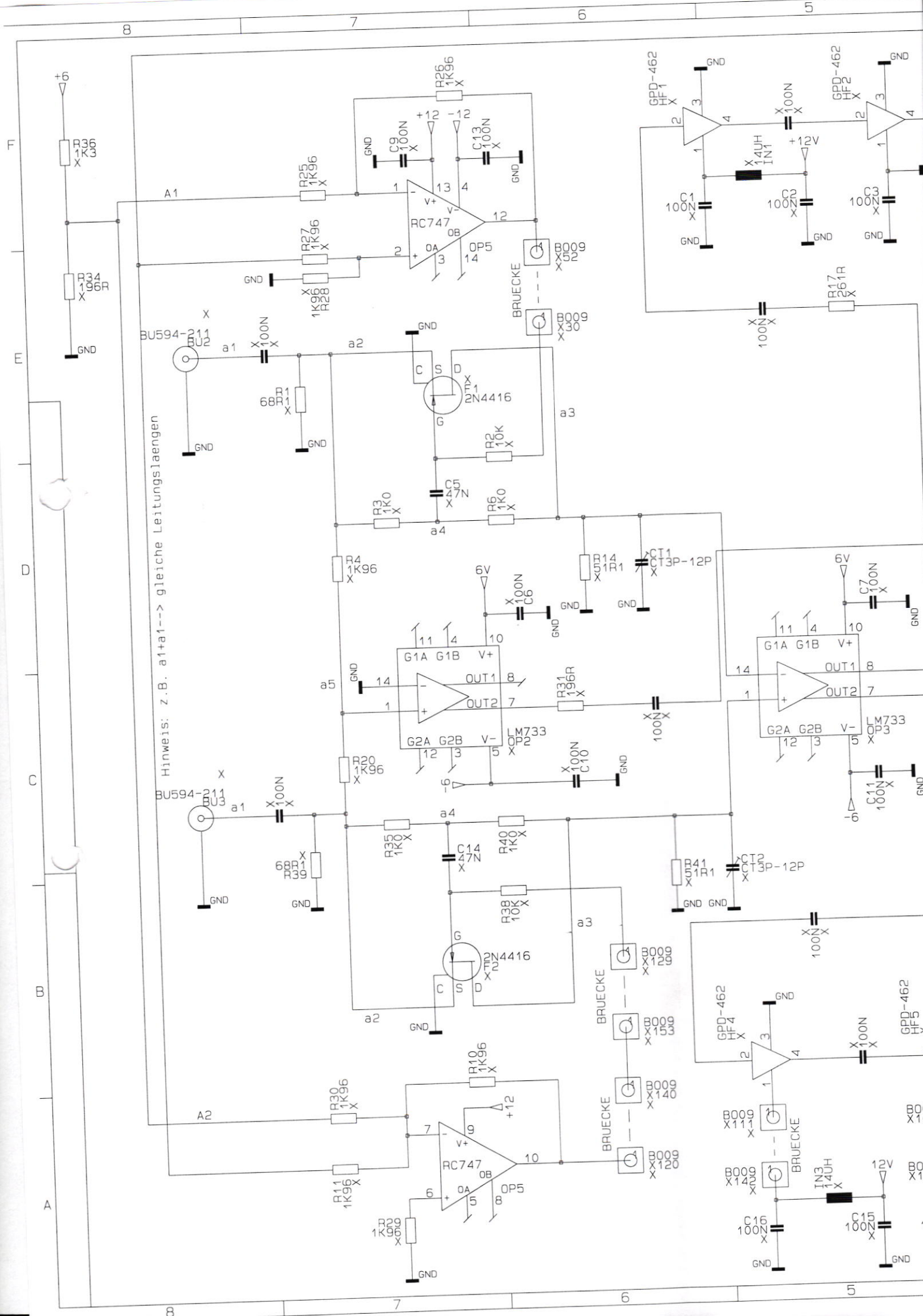


GSI - DARMASTADT		VERS.	FG 447 101
ENTW.		LOOS	07-MAR-96
BEARB.		X	
GEP.		X	
SUPPRESSOR			
CLOSED-ORBIT SIGNAL			
BLATT 1		VON 2	
MASSTAB			

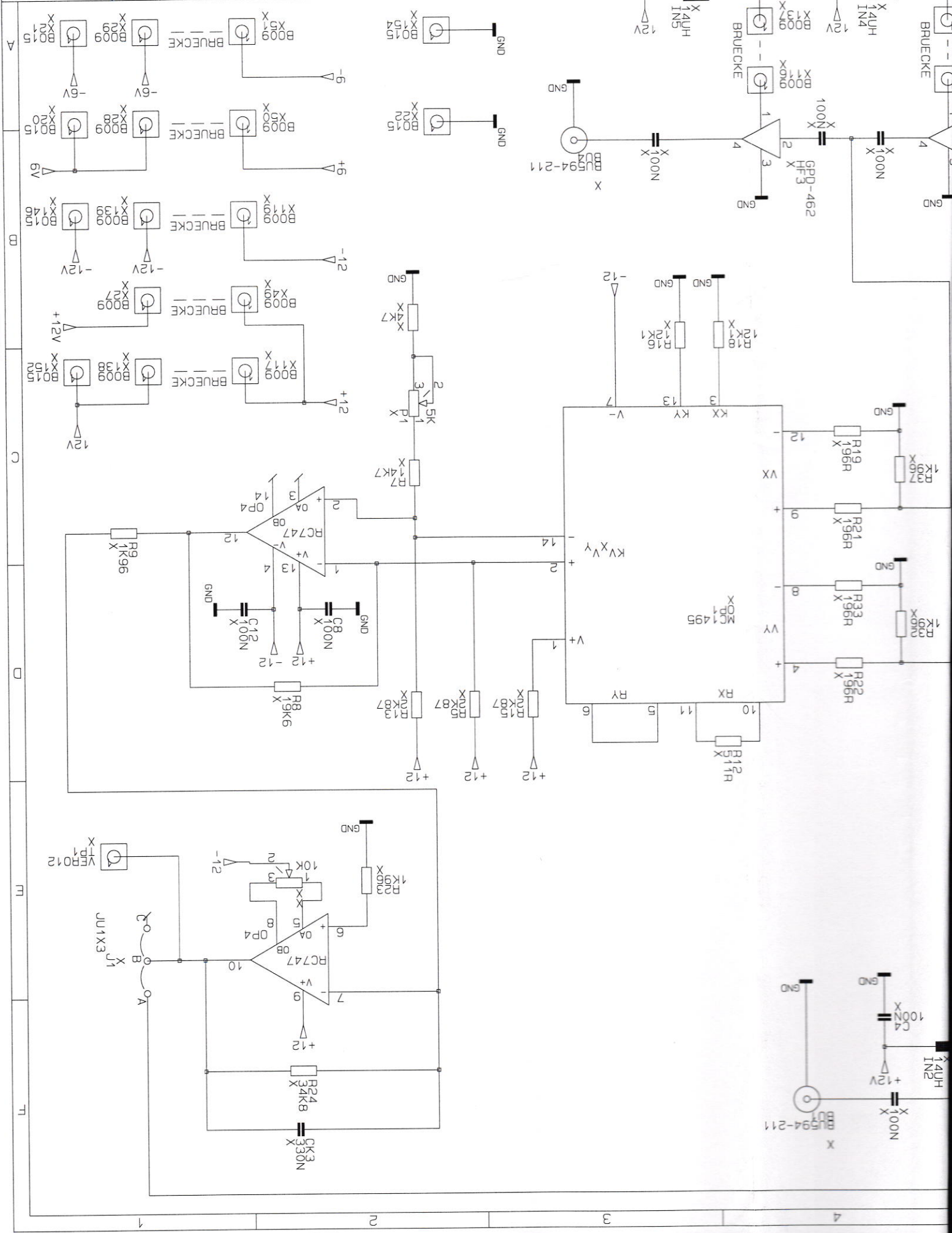
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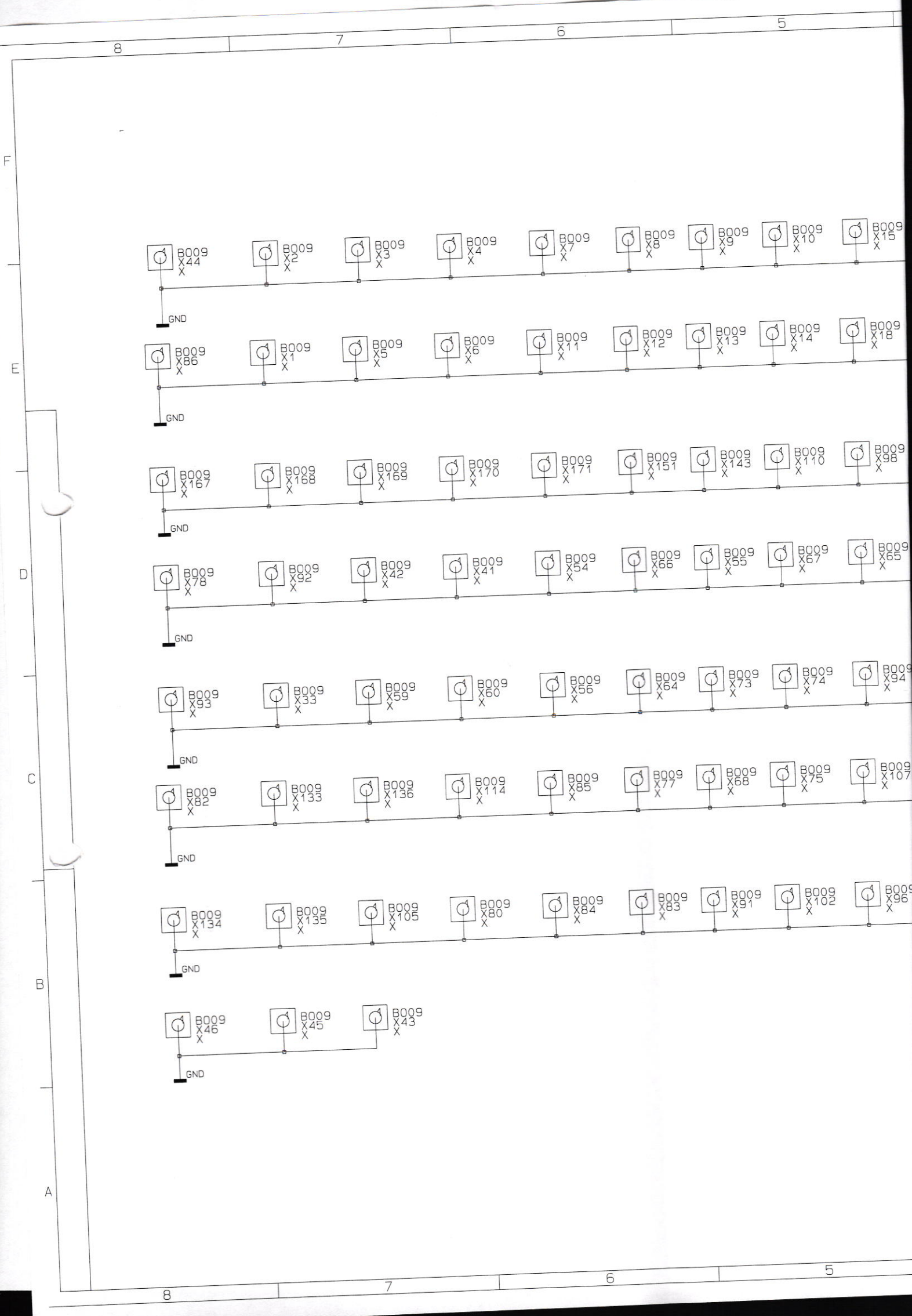
Hinweis: z.B. a1+a1--> gleiche Leitungslaengen



GSI - DARMASTADT		ENTW. LOOS	BEARB. X	GEPR. X	
NAME		DATUM 07-MAR-96			
SUPPRESSOR		CLOSED-ORBIT SIGNAL			VERS. FG 447 101
MASSSTAB		BLATT 1			VON 2

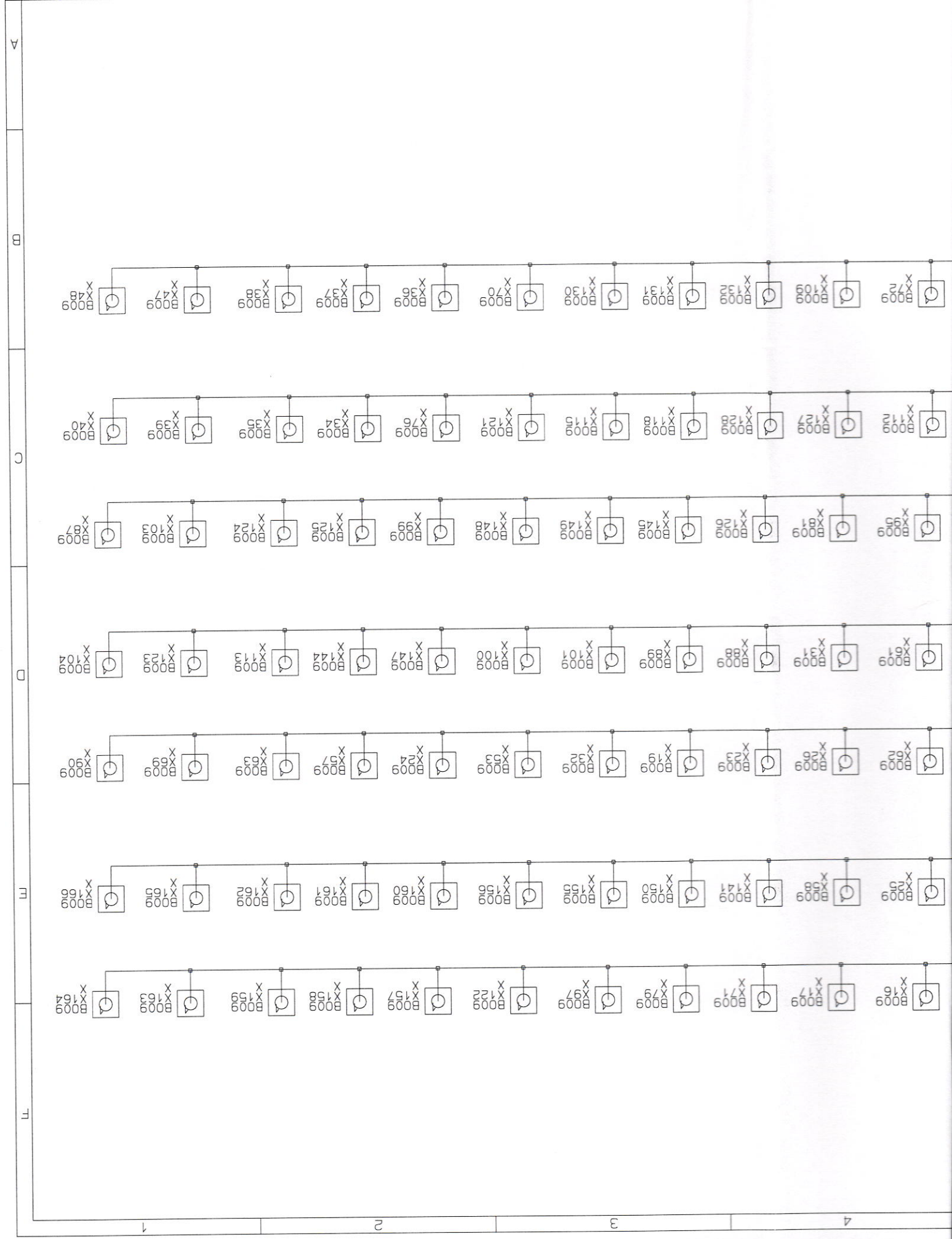


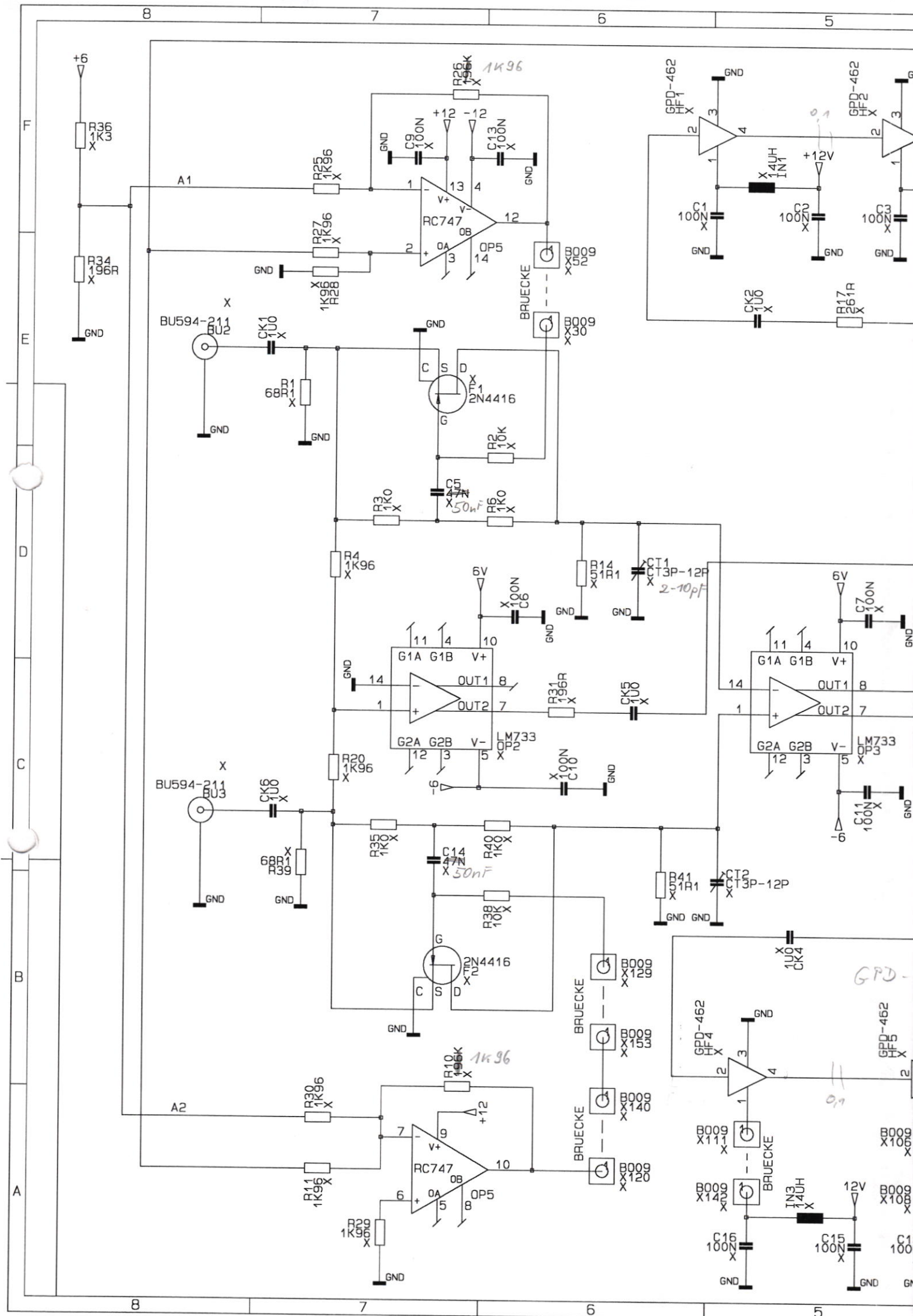


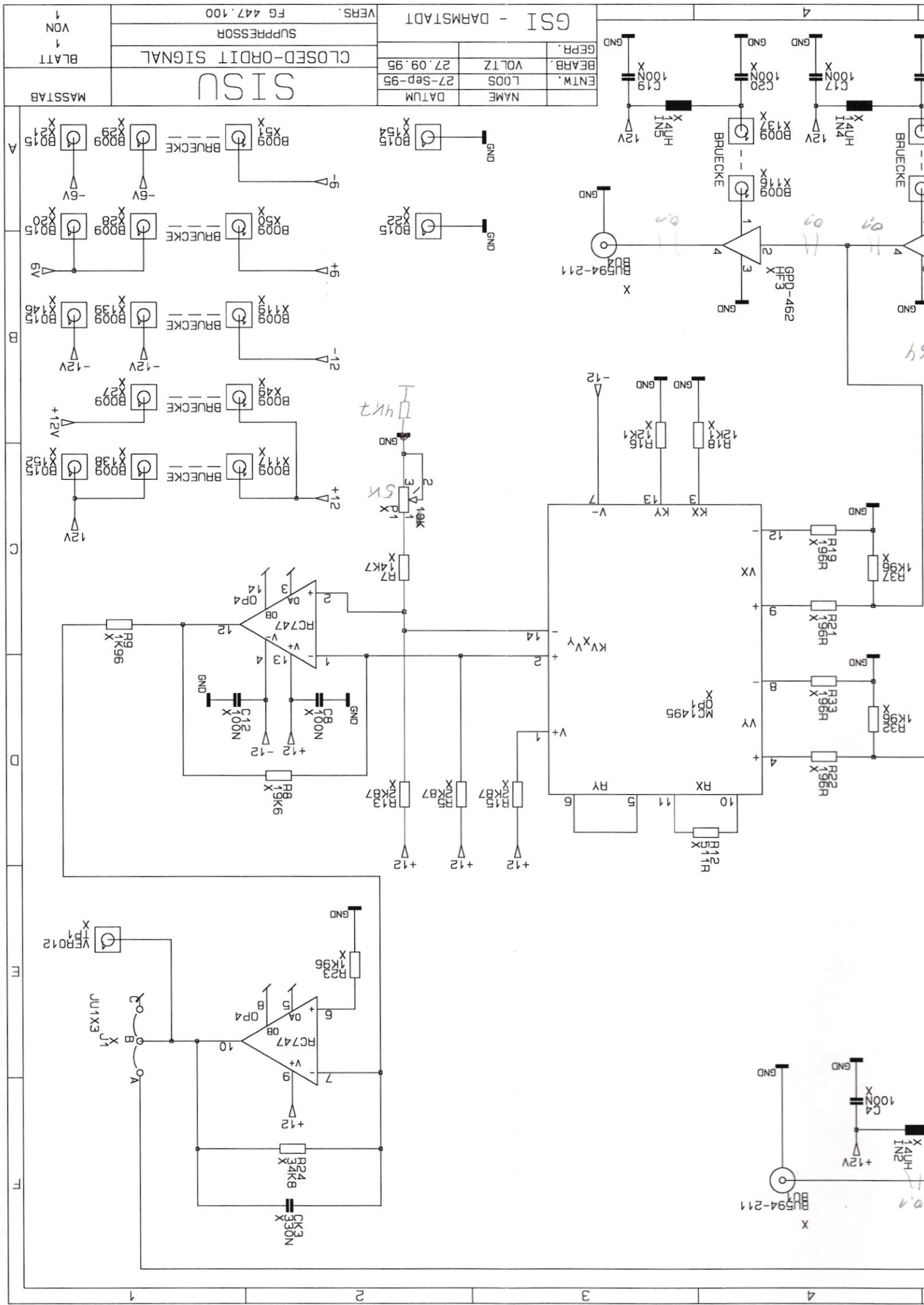


MASSTAB	SISU1	ENTM.	LOOS	27-Sep-95	
		BEARB.	VOLTZ	27.09.95	
BLATT	2	VERS.	FG 447 101		
VON	2	GSI - DARMSTADT			

4
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NAME	DATUM	27-Sep-95
ENTW.	L00S	
BEARB.	VOLTZ	27.09.95
GEPF.		

SISU  
 CLOSED-ORDIT SIGNAL  
 SUPPRESSOR  
 FG 447.100  
 VER.S.

MASSSTAB  
 BLATT  
 VON

NAME	DATUM	27-Sep-95
ENTW.	L00S	
BEARB.	VOLTZ	27.09.95
GEPF.		

SISU  
 CLOSED-ORDIT SIGNAL  
 SUPPRESSOR  
 FG 447.100  
 VER.S.

MASSSTAB  
 BLATT  
 VON