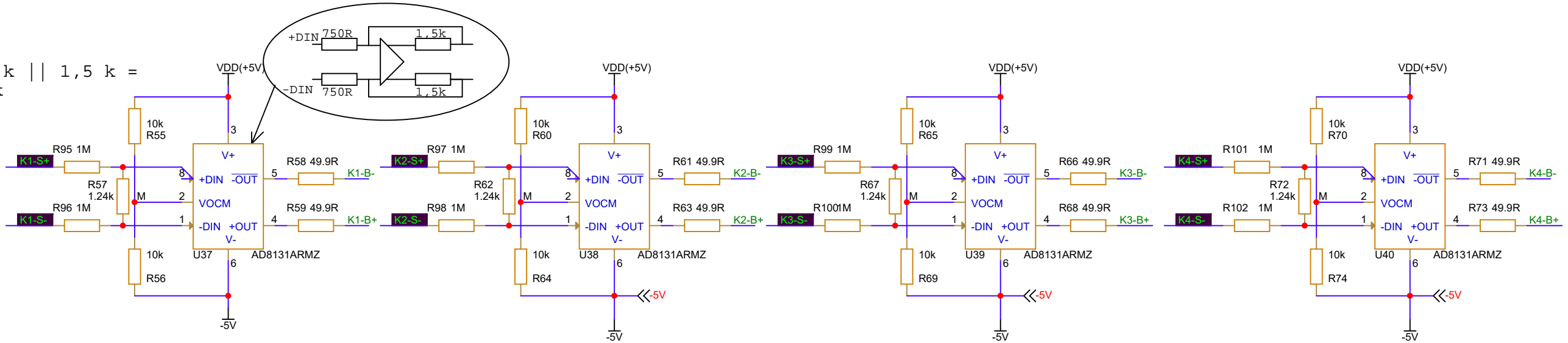
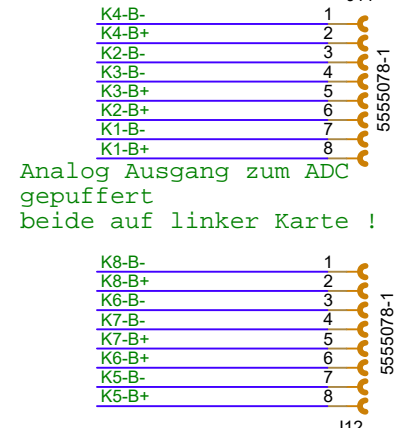


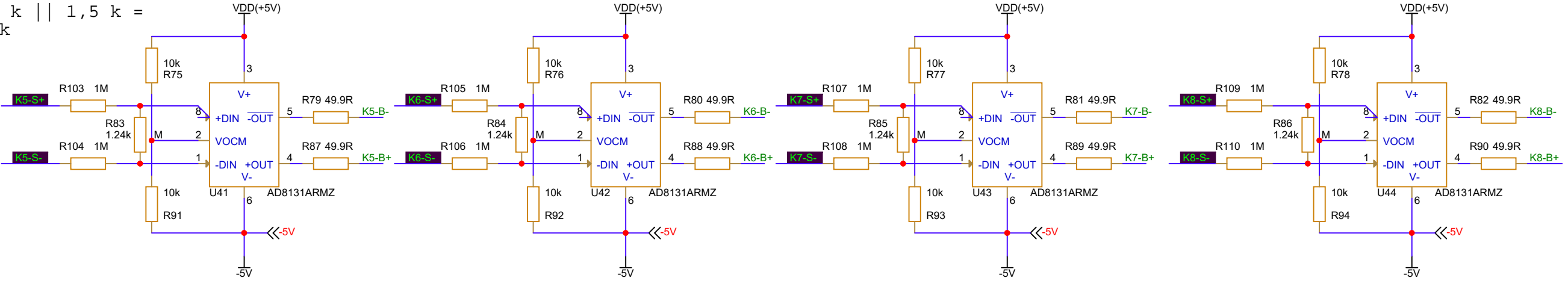
$1,24\text{ k} \parallel 1,5\text{ k} = 680\text{ k}$



Die Trafosignale werden 680 Ohm abgeschlossen am Digitizer hier nur parallel gebuffert und an den Netzwerkbuchsen an die ADCS übergeben!

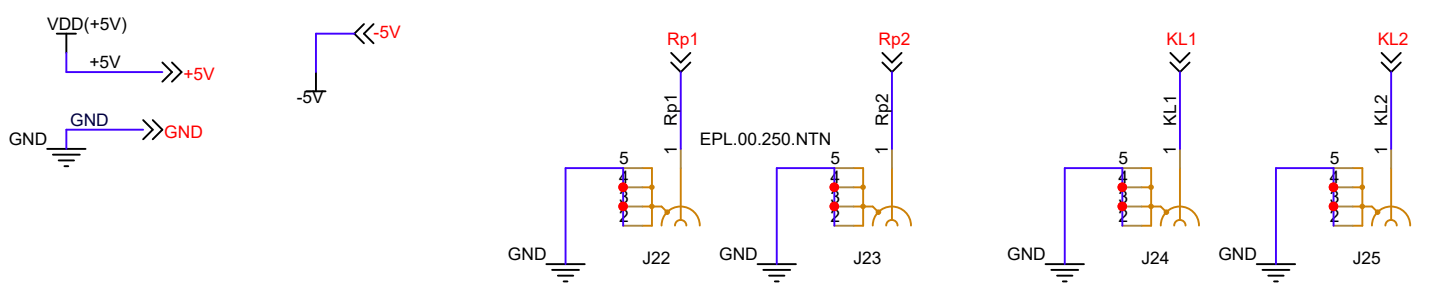


$1,24\text{ k} \parallel 1,5\text{ k} = 680\text{ k}$



Rahmenpulse  
TTL in

Klemmpulse  
CMOS in



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**MDR-IO-QuicknDirty Signalabschluss**

Design: K:\GSI\JOB\SD\MAPS-2021\MDR-Q+DIMDR-IO-Q+D.DSN  
 Modified: Tuesday, January 11, 2022  
 Designer: <Designer>

Size: A3  
 Page: 1 / 1  
 Layouter: <Layouter>