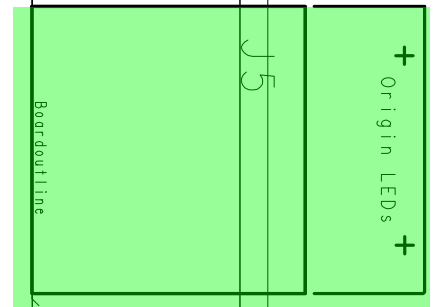
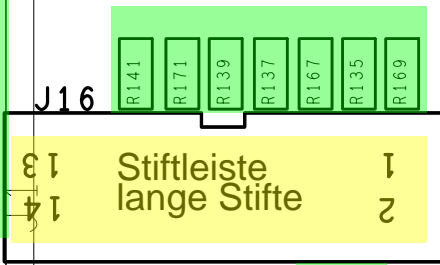
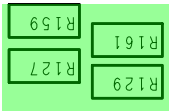
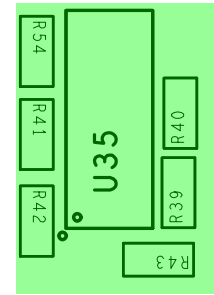
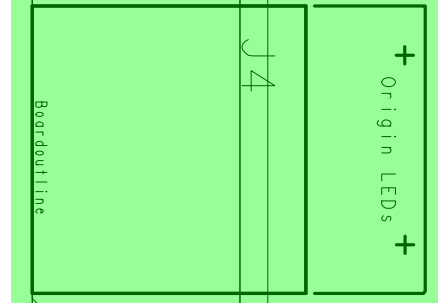


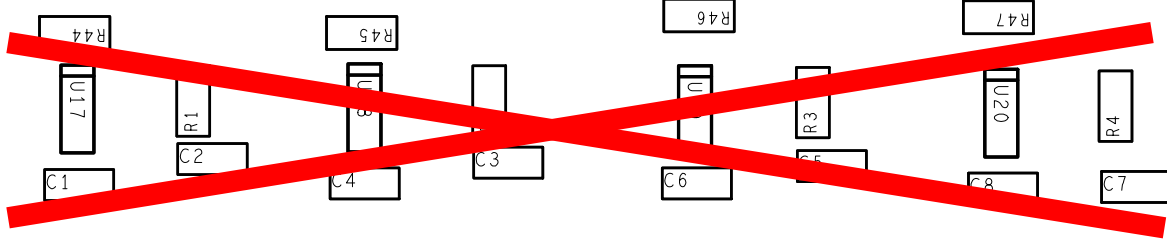
Oberseite Platine MDR-IO in Variante R-O (rechts oder oben):

RJ45 Rahmenpulse

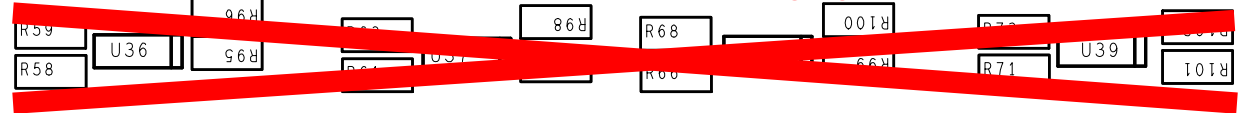


RJ45 Klempulse

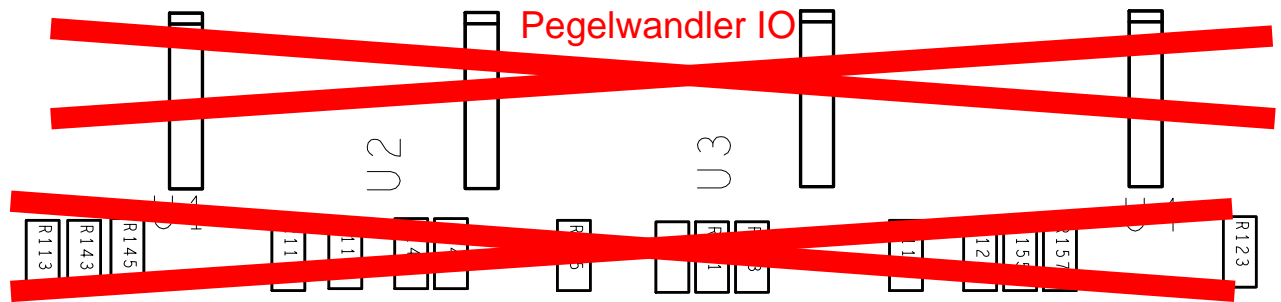
Single Ended Monitorbuchsentreiber



Differentielle Buffer für Analogsignale

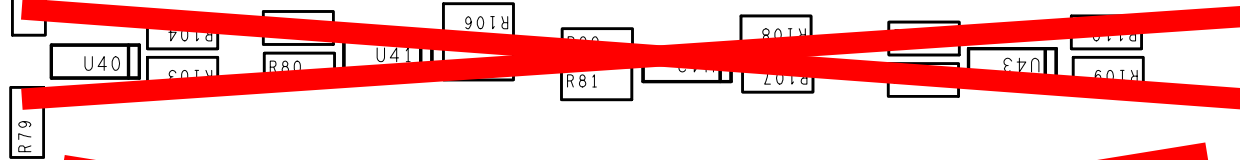


Pegelwandler IO

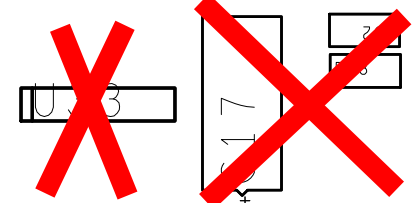
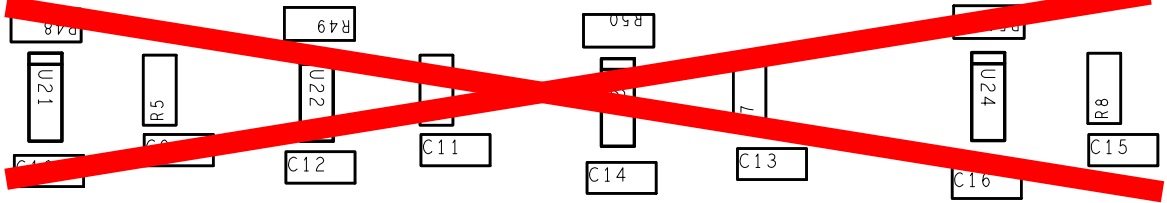


Nullohm Widerstände

Differentielle Buffer für Analogsignale

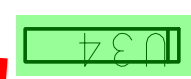
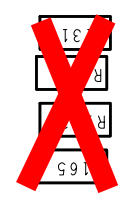


Single Ended Monitorbuchsentreiber

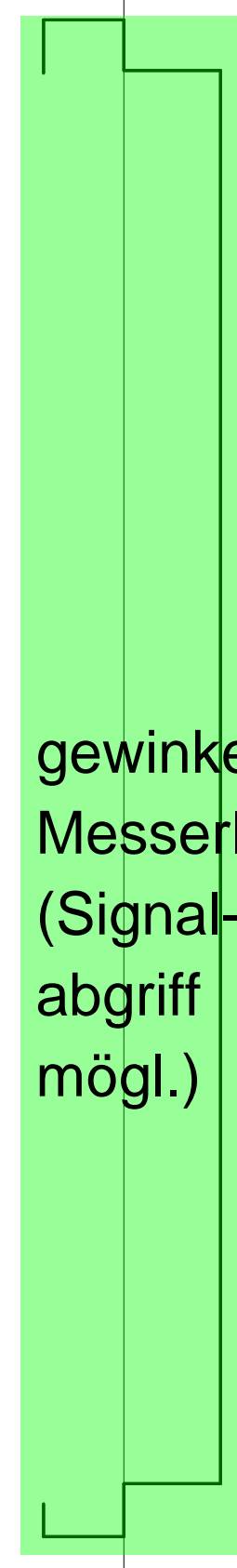
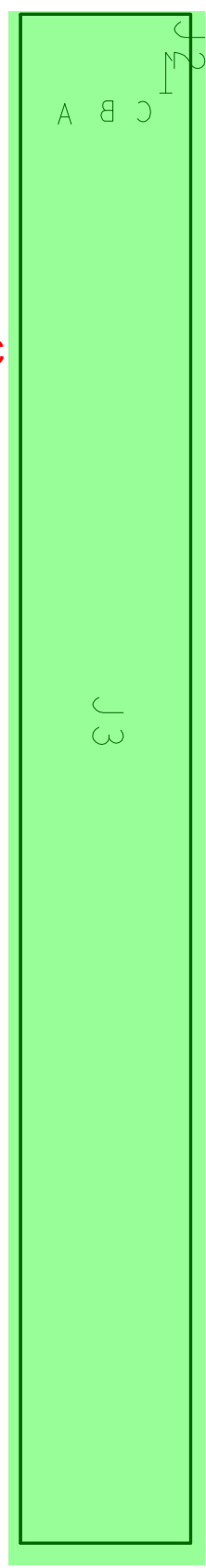


Klemmpulsfreischaltung nur unten!

Buffermitte 2,5V + Monitor VCC Glättung



Klemmpulsfreischaltung nur oben!



gewinkelte Messerleiste (Signalabgriff mögl.)